

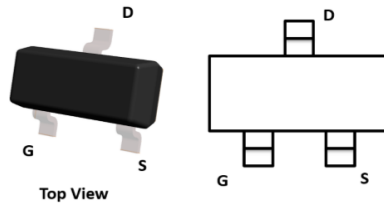
**Features**

- 100V, 3.2A
- $R_{DS(ON)} = 130m\Omega$  (Max.) @  $V_{GS} = 10V, I_D = 3A$
- High Power and Current Handling Capability
- Lead Free Product is Acquired
- Surface Mount Package

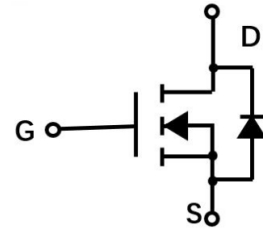
**Application**

- PWM Application
- Load Switch
- Power Management

**Package**



**SOT-23  
SEY9582AG**



**Absolute Maximum Ratings**  $T_C=25^\circ C$  unless otherwise specified

Symbol	Parameter	Max.	Units
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>note5</sup>	$T_C = 25^\circ C$ 3.2	A
$I_D$	Continuous Drain Current <sup>note5</sup>	$T_C = 100^\circ C$ 2.0	A
$I_{DM}$	Pulsed Drain Current <sup>note3</sup>	19.6	A
$P_D$	Power Dissipation <sup>note2</sup>	$T_C = 25^\circ C$ 3.6	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>note1,4</sup>	85	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ C$

**Electrical Characteristics**  $T_C=25^{\circ}\text{C}$  unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	100	-	-	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS} = 80V, V_{GS} = 0V$	-	-	1	$\mu A$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.2	1.8	2.6	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 3A$	-	105	130	$m\Omega$
		$V_{GS} = 4.5V, I_D = 2A$	-	135	150	$m\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 50V, V_{GS} = 0V,$ $f = 1.0MHz$	-	212	-	pF
$C_{oss}$	Output Capacitance		-	27.5	-	pF
$C_{riss}$	Reverse Transfer Capacitance		-	1.6	-	pF
<b>Switching Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{DS} = 50V, I_D = 3A,$ $V_{GS} = 10V$	-	3.3	-	nC
$Q_{gs}$	Gate-Source Charge		-	0.35	-	
$Q_{gd}$	Gate-Drain("Miller") Charge		-	0.87	-	
$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 50V, I_D = 3A,$ $R_G = 2\Omega, V_{GS} = 10V$	-	13.2	-	ns
$t_r$	Turn-On Rise Time		-	2.2	-	
$t_{d(off)}$	Turn-Off Delay Time		-	11	-	
$t_f$	Turn-Off Fall Time		-	1.1	-	
<b>Diode Characteristics</b>						
$I_S$	Continuous Source Current		-	-	4.9	A
$V_{SD}$	Diode Forward Voltage	$I_S = 3A, V_{GS} = 0V$	-	-	1.0	V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = 3A,$	-	27	-	ns
$Q_{rr}$	Reverse Recovery Charge	$di_{SD}/dt = 100A/\mu s$	-	35	-	nC

## Notes:

1. The value of  $R_{\theta JC}$  is measured in a still air environment with  $T_A = 25^{\circ}\text{C}$  and the maximum allowed junction temperature of  $150^{\circ}\text{C}$ . The value in any given application depends on the user's specific board design.
2. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 150^{\circ}\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
3. Single pulse width limited by junction temperature  $T_{J(MAX)} = 150^{\circ}\text{C}$ .
4. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to case  $R_{\theta JC}$  and case to ambient.
5. The maximum current rating is package limited.

### Typical Performance Characteristics

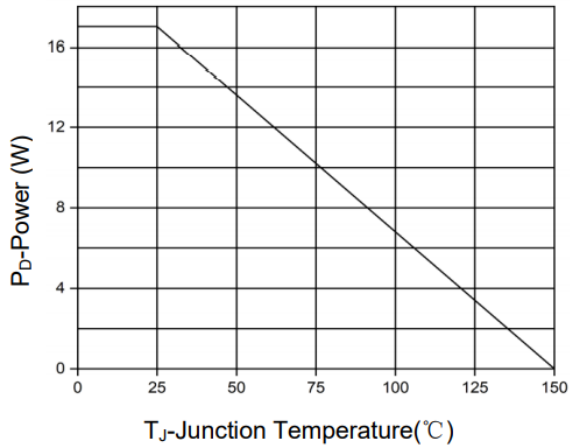


Figure 1. Power Dissipation

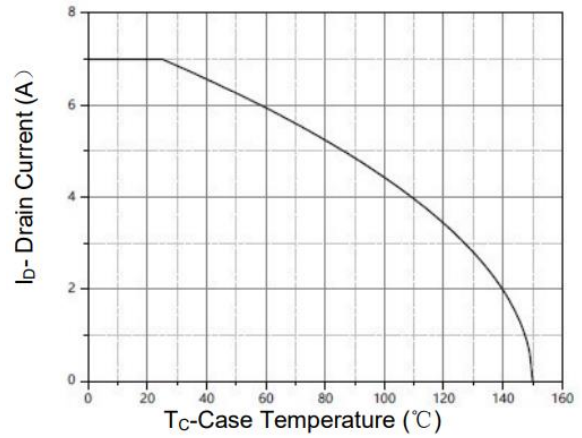


Figure 2. Drain Current

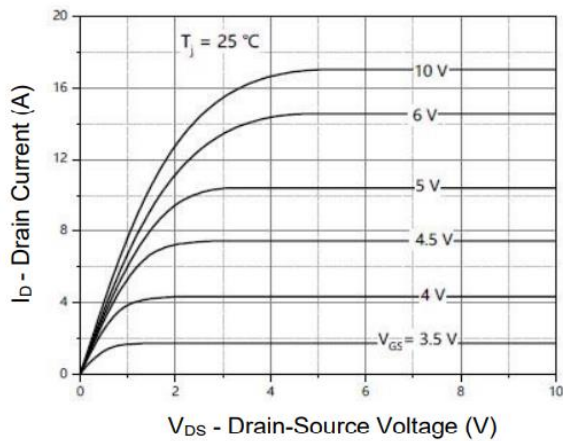


Figure 3. Output characteristics

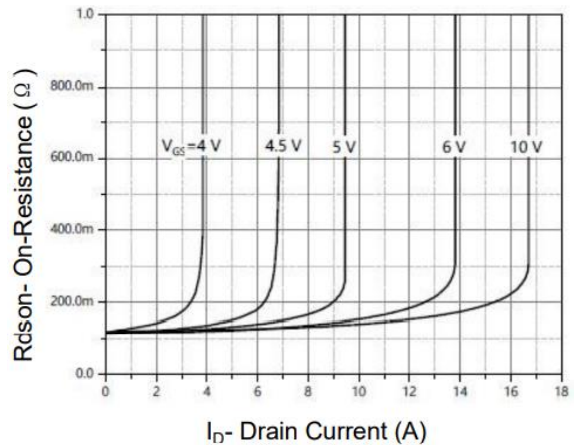


Figure 4. Drain-Source On-state resistance

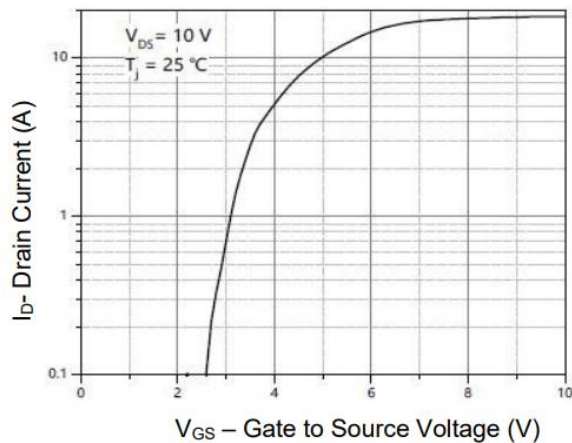


Figure 5. Transfer Characteristics

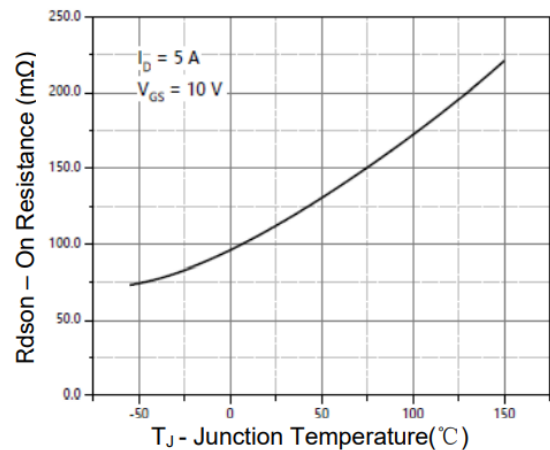
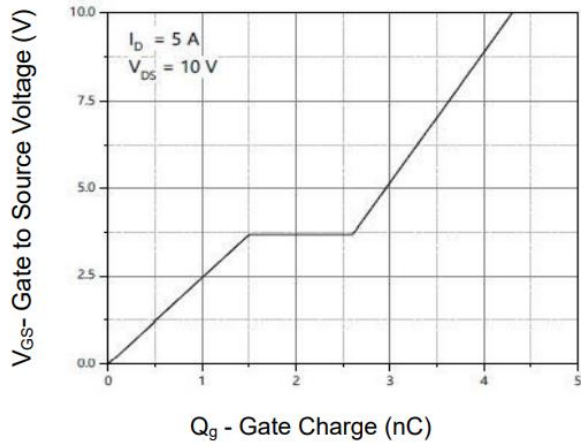
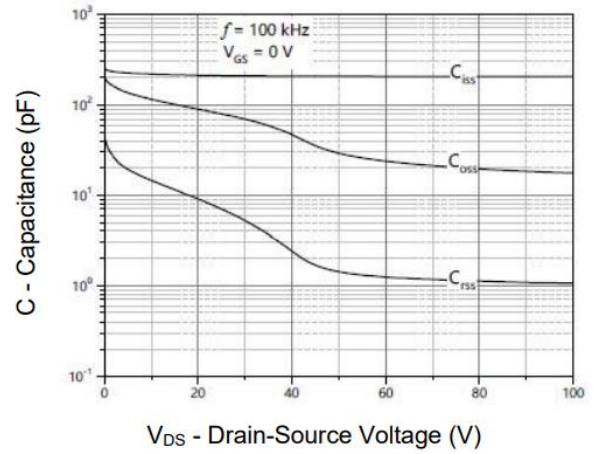


Figure 6. Drain-Source On-State Resistance



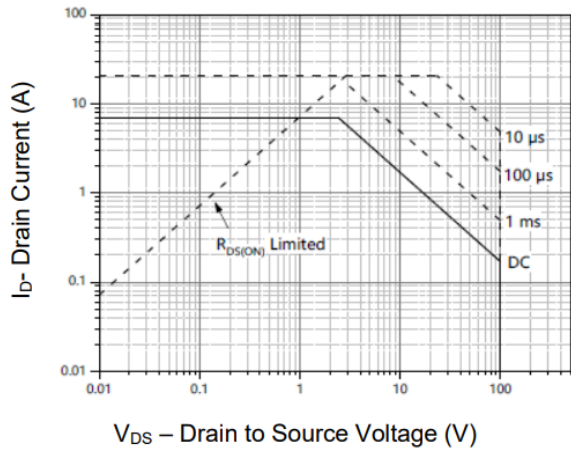
$Q_g$  - Gate Charge (nC)

Figure 7. Gate Charge



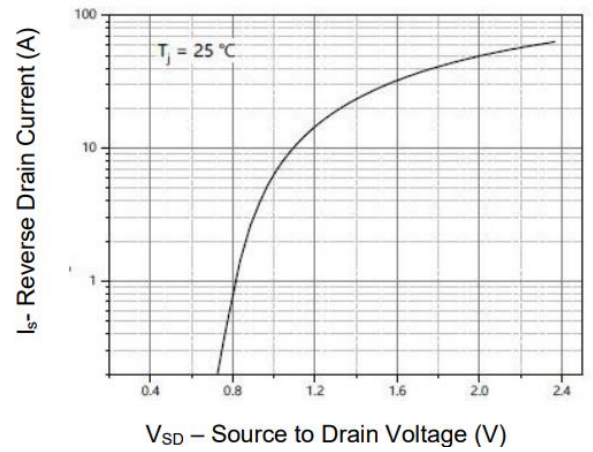
$V_{DS}$  - Drain-Source Voltage (V)

Figure 8. Capacitance vs Vds



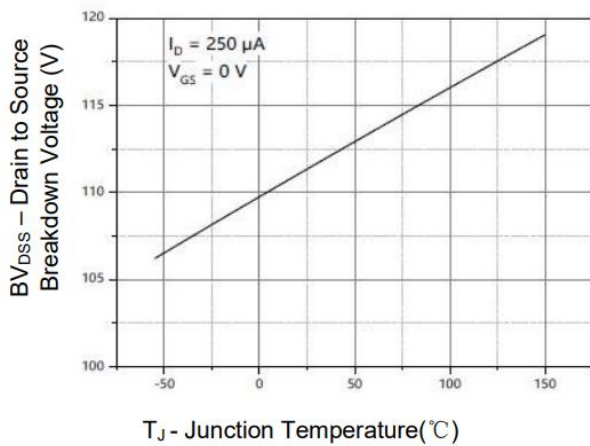
$V_{DS}$  - Drain to Source Voltage (V)

Figure 9. Safe Operation Area



$V_{SD}$  - Source to Drain Voltage (V)

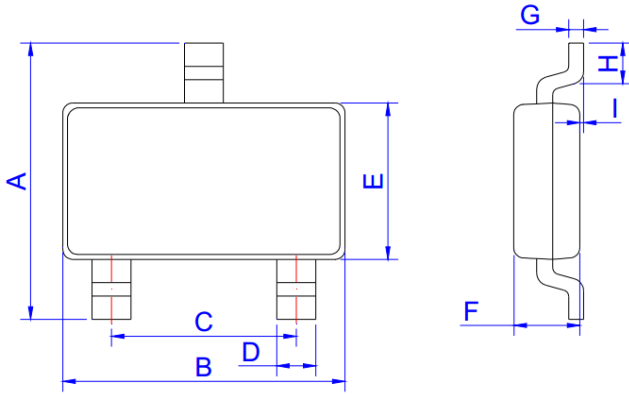
Figure 10. Source- Drain Diode Forward



$T_j$  - Junction Temperature(°C)

Figure 11. Drain-source breakdown voltage

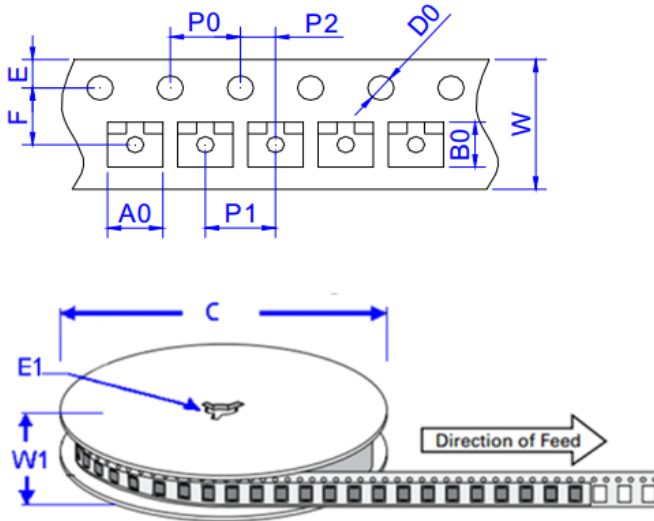
### SOT-23 Package Mechanical Data



SOT-23

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.30	2.40	2.50	0.091	0.095	0.098
B	2.80	2.90	3.00	0.110	0.114	0.118
C	1.90 REF			0.075 REF		
D	0.35	0.40	0.45	0.014	0.016	0.018
E	1.20	1.30	1.40	0.047	0.051	0.055
F	0.90	1.00	1.10	0.035	0.039	0.043
G		0.10	0.15		0.004	0.006
H	0.20			0.008		
I	0		0.10	0		0.004

### Package Information-SOT-23



Ref.	Dimensions	
	Millimeters	Inches
A0	3.15 ± 0.3	0.124 ± 0.012
B0	2.77 ± 0.3	0.109 ± 0.012
C	178	7.0
D0	1.50±0.1	0.059 ± 0.004
E	1.75 ± 0.2	0.069 ± 0.008
E1	13.3±0.3	0.524± 0.012
F	3.5 ± 0.2	0.138 ± 0.008
P0	4.00 ± 0.2	0.157 ± 0.008
P1	4.00 ± 0.2	0.157 ± 0.008
P2	2.00 ± 0.2	0.079 ± 0.008
W	8.00 ± 0.2	0.315 ± 0.008
W1	11.5±1.0	0.453 ± 0.039

### Ordering Information-SOT-23

OUTLINE	PACKAGE TYPE	QUANTITY REEL	DESCRIPTION
TAPING	SOT-23	3,000pcs	7 inch reel pack

## SEY9582AG Product Description

Silicon N-Channel MOSFET



### NOTE:

1. We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
2. Please do not exceed the absolute maximum ratings of the device when circuit designing.
3. Winsemi Microelectronics Co., Ltd reserved the right to make changes in this specification sheet and is subject to change without prior notice.

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