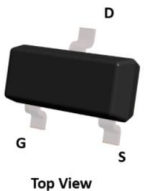
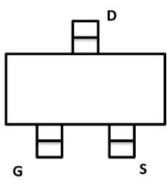
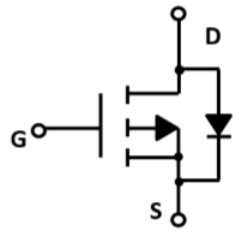


<p>Features</p> <ul style="list-style-type: none"> • -30V, -4.2A • $R_{DS(ON)} = 55m\Omega$ (Max.) @ $V_{GS} = -10V, I_D = -4.2A$ • High Power and Current Handling Capability • Lead Free Product is Acquired • Surface Mount Package 	<p>Application</p> <ul style="list-style-type: none"> • PWM Application • Load Switch • Power Management
<p>Package</p> <div style="display: flex; justify-content: space-around; align-items: center;">    </div> <p style="text-align: center;">SOT-23 SFY3401AT</p>	

Absolute Maximum Ratings $T_C=25^\circ C$ unless otherwise specified

Symbol	Parameter	Max.	Units
V_{DSS}	Drain-Source Voltage	-30	V
V_{GSS}	Gate-Source Voltage	± 12	V
I_D	Continuous Drain Current	$T_C = 25^\circ C$	-4.2
		$T_C = 100^\circ C$	-2.7
I_{DM}	Pulsed Drain Current ^{note1}	-17	A
P_D	Power Dissipation	$T_C = 25^\circ C$	1.2
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	104	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ C$

*Drain current limited by maximum junction temperature

Electrical Characteristics $T_C=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = -250\mu A$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -24V, V_{GS} = 0V$	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 10V$	-	-	± 100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.7	-1	-1.3	V
$R_{DS(on)}$	Static Drain-Source On-Resistance ^{note2}	$V_{GS} = -10V, I_D = -4.2A$	-	-	55	m Ω
		$V_{GS} = -4.5V, I_D = -4A$	-	-	75	
		$V_{GS} = -2.5V, I_D = -1A$	-	-	90	
g_{FS}	Forward Transconductance	$V_{DS} = -5V, I_D = -4.2A$	-	10	-	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = -15V, V_{GS} = 0V,$ $f = 1.0MHz$	-	880	-	pF
C_{oss}	Output Capacitance		-	105	-	pF
C_{riss}	Reverse Transfer Capacitance		-	65	-	pF
Q_g	Total Gate Charge	$V_{DD} = -15V, I_D = -4.2A,$ $V_{GS} = -4.5V$	-	8.5	-	nC
Q_{gs}	Gate-Source Charge		-	1.8	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	2.7	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = -15V, I_D = -4.2A,$ $R_{GEN} = 6\Omega, V_{GS} = -10V$	-	7	-	ns
t_r	Turn-On Rise Time		-	3	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	30	-	ns
t_f	Turn-Off Fall Time		-	12	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	-4.2	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-17	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_S = -4.2A$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time	$V_{GS} = 0V, I_F = -4.2A,$ $di/dt = 100A/\mu s$	-	11	-	ns
Q_{rr}	Reverse Recovery Charge		-	3.5	-	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.

Typical Performance Characteristics

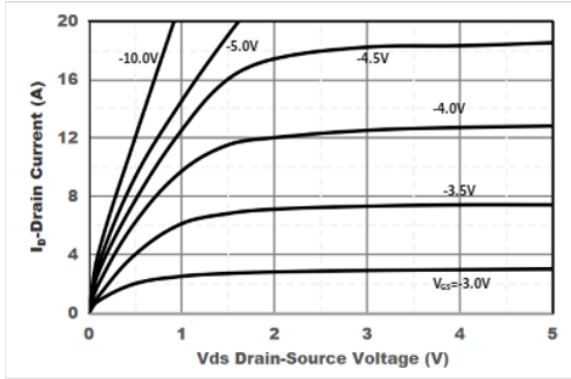


Figure 1. Output Characteristics

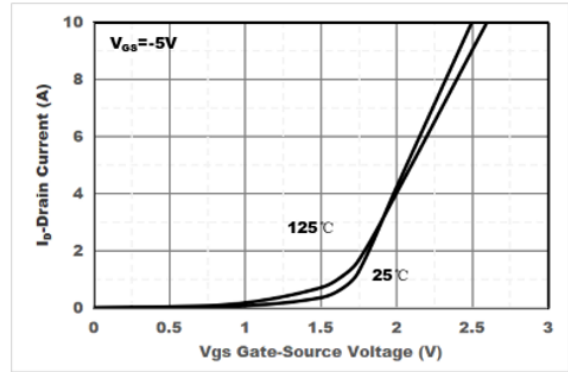


Figure 2. Transfer Characteristics

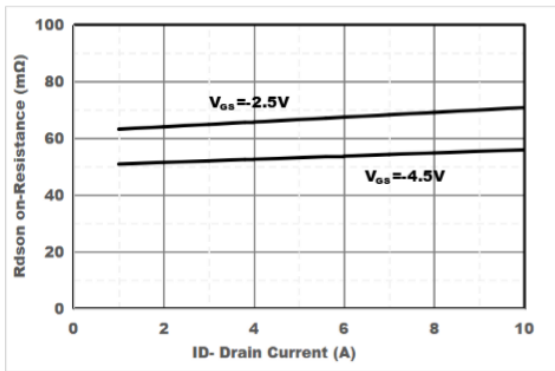


Figure 3. Drain-to-Source On Resistance

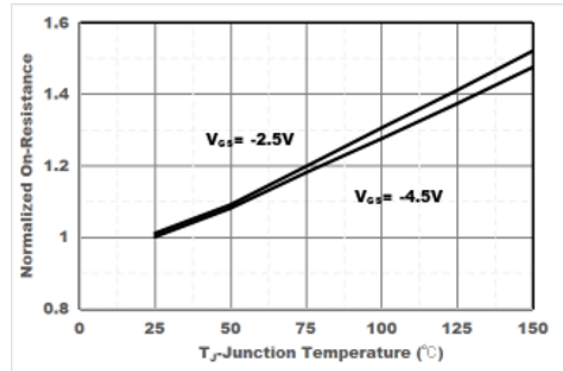


Figure 4. Drain-to-Source On Resistance

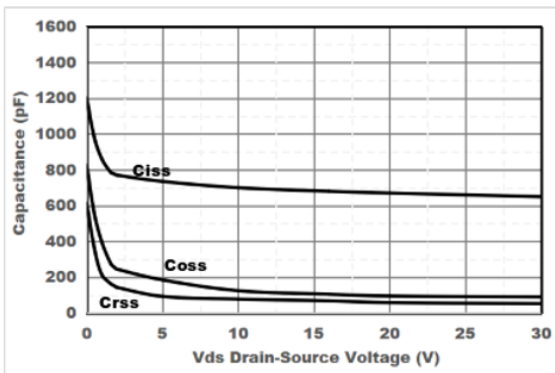


Figure 5. Capacitance Characteristics

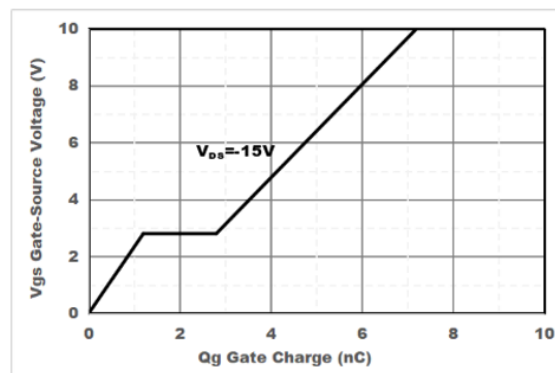


Figure 6. Gate Charge Characteristics

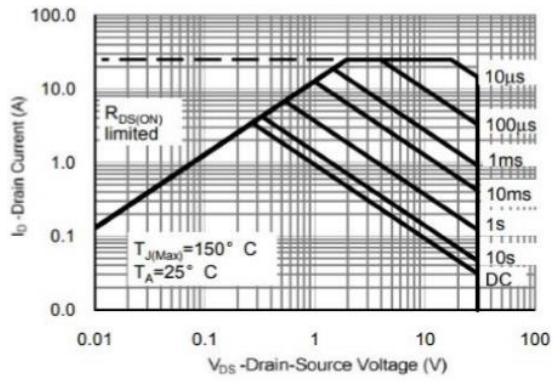


Figure 7. Maximum Safe Operating Area

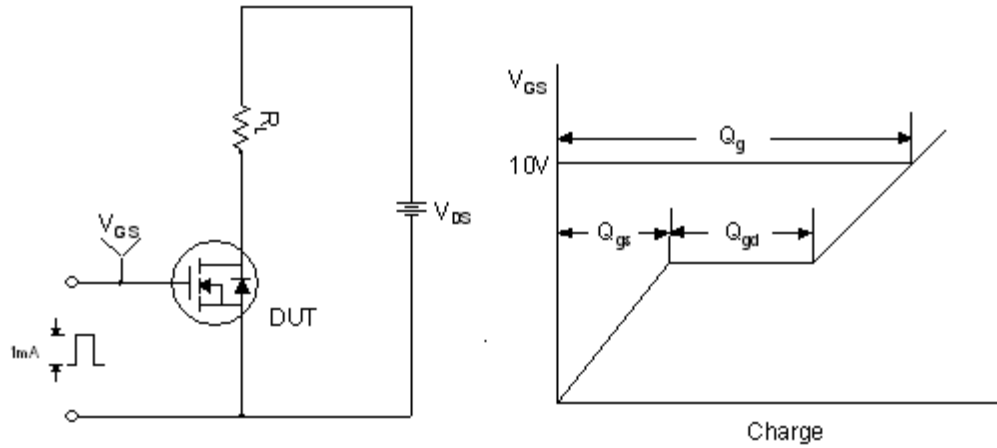


Figure 8. Gate Charge Test Circuit & Waveform

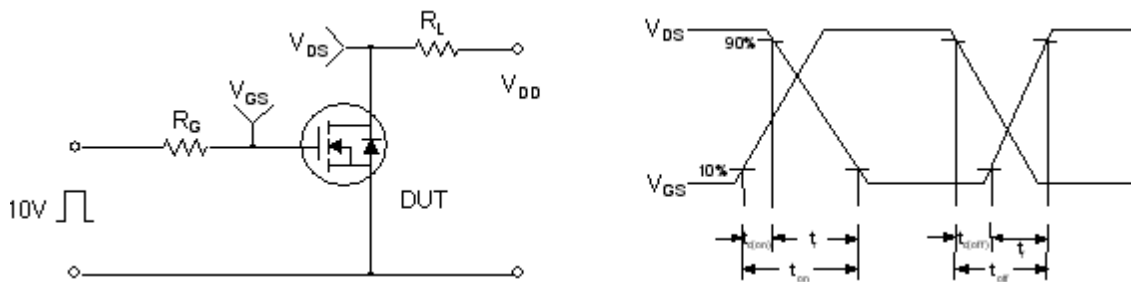


Figure 9. Resistive Switching Test Circuit & Waveforms

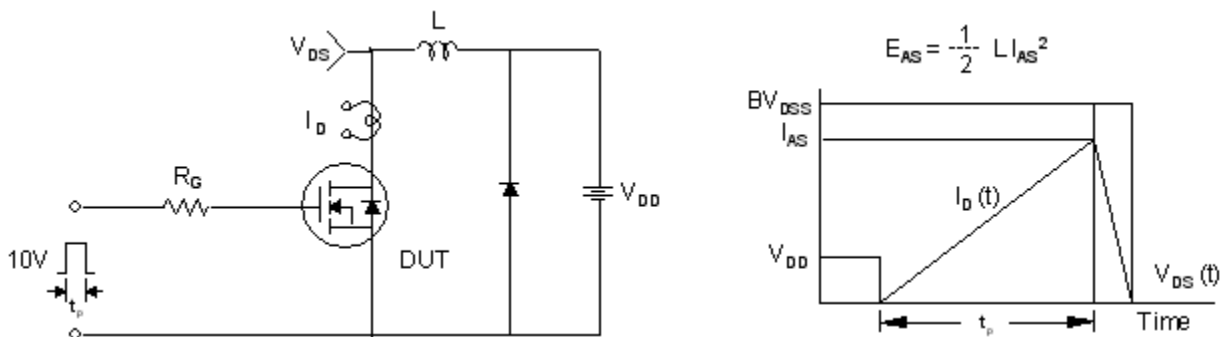


Figure 10. Unclamped Inductive Switching Test Circuit & Waveforms

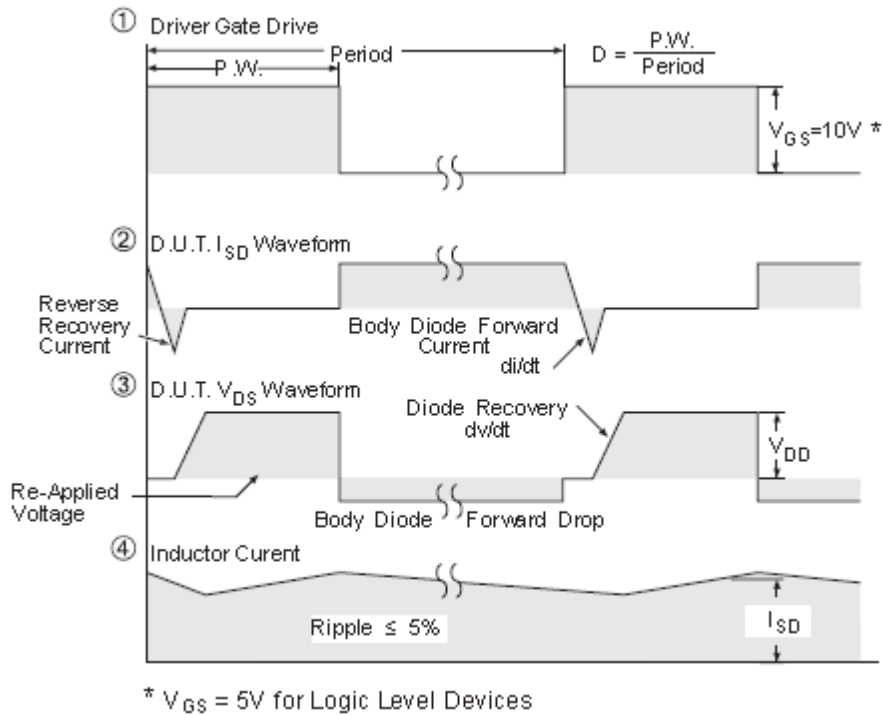
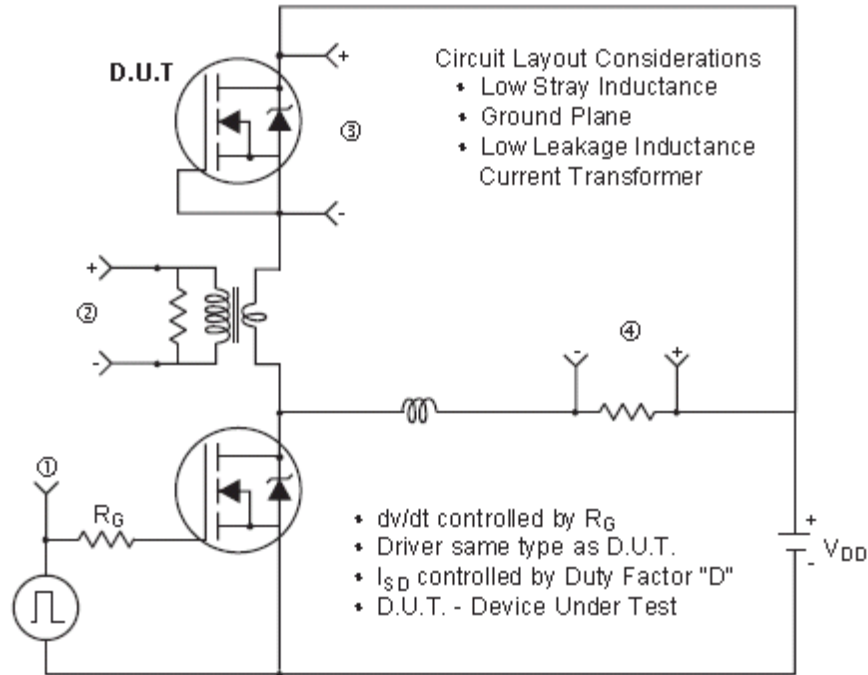
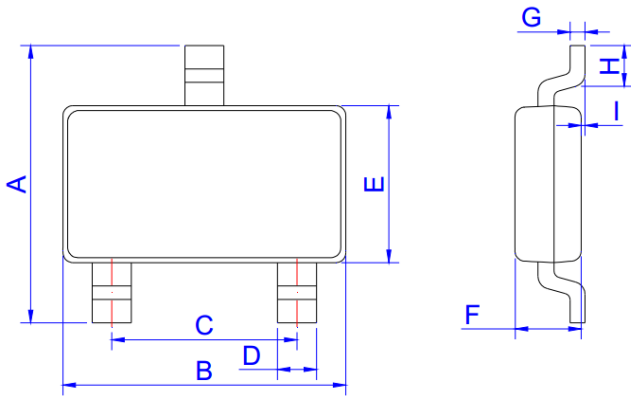


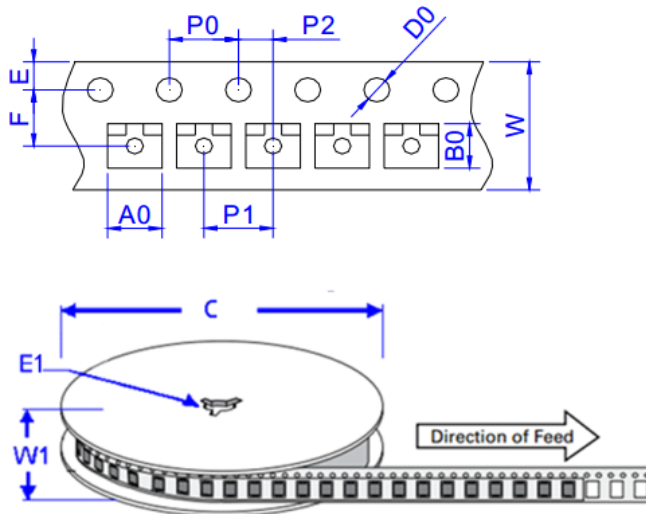
Figure 11. Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)



SOT-23

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.30	2.40	2.50	0.091	0.095	0.098
B	2.80	2.90	3.00	0.110	0.114	0.118
C	1.90 REF			0.075 REF		
D	0.35	0.40	0.45	0.014	0.016	0.018
E	1.20	1.30	1.40	0.047	0.051	0.055
F	0.90	1.00	1.10	0.035	0.039	0.043
G		0.10	0.15		0.004	0.006
H	0.20			0.008		
I	0		0.10	0		0.004

Package Information-SOT-23



Ref.	Dimensions	
	Millimeters	Inches
A0	3.15 ± 0.3	0.124 ± 0.012
B0	2.77 ± 0.3	0.109 ± 0.012
C	178	7.0
D0	1.50±0.1	0.059 ± 0.004
E	1.75 ± 0.2	0.069 ± 0.008
E1	13.3±0.3	0.524± 0.012
F	3.5 ± 0.2	0.138 ± 0.008
P0	4.00 ± 0.2	0.157 ± 0.008
P1	4.00 ± 0.2	0.157 ± 0.008
P2	2.00 ± 0.2	0.079 ± 0.008
W	8.00 ± 0.2	0.315 ± 0.008
W1	11.5±1.0	0.453 ± 0.039

Ordering Information-SOT-23

OUTLINE	PACKAGE TYPE	QUANTITY REEL	DESCRIPTION
TAPING	SOT-23	3,000pcs	7 inch reel pack

SFY3401AT Product Description

Silicon P-Channel MOSFET



NOTE:

1. We strongly recommend customers check carefully on the trademark when buying our product, if there is any question, please don't be hesitate to contact us.
2. Please do not exceed the absolute maximum ratings of the device when circuit designing.
3. Winsemi Microelectronics Co., Ltd reserved the right to make changes in this specification sheet and is subject to change without prior notice.

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