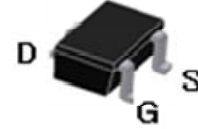
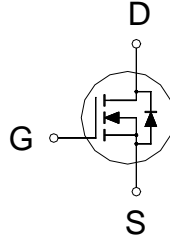


N-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

$BV_{DSS}$	30V
$R_{DS(on) (MAX.)}$	50m $\Omega$
$I_D$	2.2A



Pb-Free Lead Plating & Halogen Free



ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ }^\circ\text{C}$  Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNIT
Gate-Source Voltage		$V_{GS}$	$\pm 12$	V
Continuous Drain Current	$T_A = 25\text{ }^\circ\text{C}$	$I_D$	2.2	A
	$T_A = 70\text{ }^\circ\text{C}$		1.65	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	14	
Power Dissipation	$T_A = 25\text{ }^\circ\text{C}$	$P_D$	0.29	W
	$T_A = 70\text{ }^\circ\text{C}$		0.19	
Operating Junction & Storage Temperature Range		$T_{j}, T_{stg}$	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Ambient	$R_{\theta JA}$		425	$^\circ\text{C} / \text{W}$
Junction-to-Lead	$R_{\theta JL}$		320	

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Duty cycle  $\leq 1\%$

ELECTRICAL CHARACTERISTICS ( $T_A = 25\text{ }^\circ\text{C}$ , Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.45	0.75	1.2	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$			1	$\mu A$
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 125\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>1</sup>	$I_{D(ON)}$	$V_{DS} = 5V, V_{GS} = 10V$	3.5			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 2.2A$		43	50	$m\Omega$
		$V_{GS} = 2.5V, I_D = 1.8A$		60	80	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 2.5A$		5		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 10V, f = 1MHz$		362		$pF$
Output Capacitance	$C_{oss}$			52		
Reverse Transfer Capacitance	$C_{rss}$			39		
Total Gate Charge <sup>1,2</sup>	$Q_g$	$V_{DS} = 10V, V_{GS} = 4.5V, I_D = 2.2A$		5.2		nC
Gate-Source Charge <sup>1,2</sup>	$Q_{gs}$			1.5		
Gate-Drain Charge <sup>1,2</sup>	$Q_{gd}$			1.8		
Turn-On Delay Time <sup>1,2</sup>	$t_{d(on)}$	$V_{DS} = 10V, I_D = 1A, V_{GS} = 4.5V, R_{GS} = 6\Omega$		8		nS
Rise Time <sup>1,2</sup>	$t_r$			2.5		
Turn-Off Delay Time <sup>1,2</sup>	$t_{d(off)}$			20		
Fall Time <sup>1,2</sup>	$t_f$			5		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_C = 25\text{ }^\circ\text{C}</math>)</b>						
Continuous Current	$I_S$				2	A
Pulsed Current <sup>3</sup>	$I_{SM}$				8	
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = I_S, V_{GS} = 0V$			1.2	V

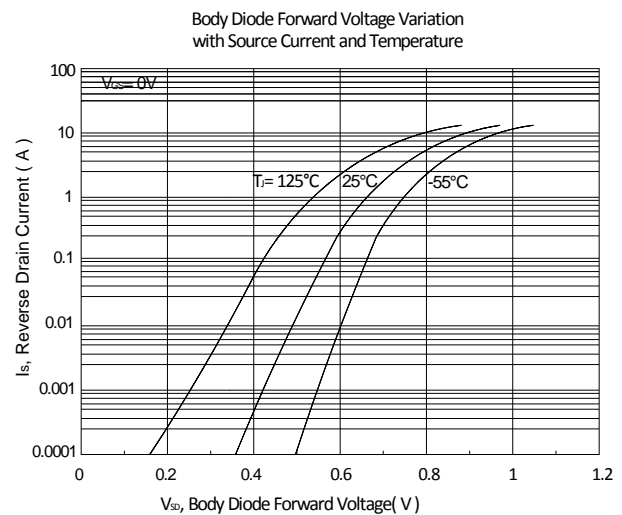
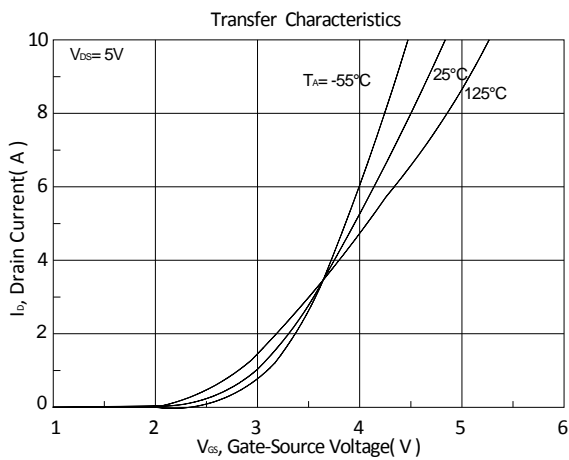
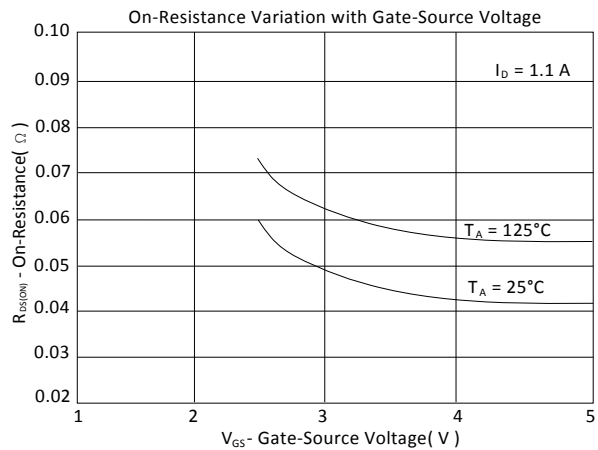
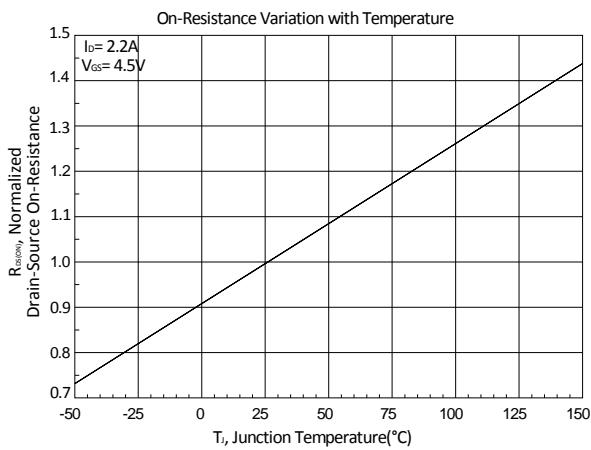
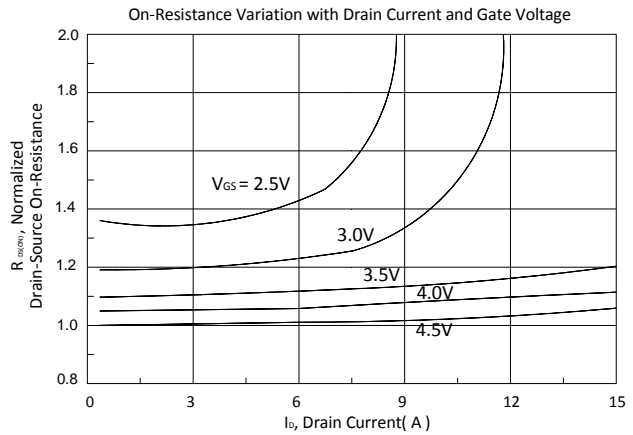
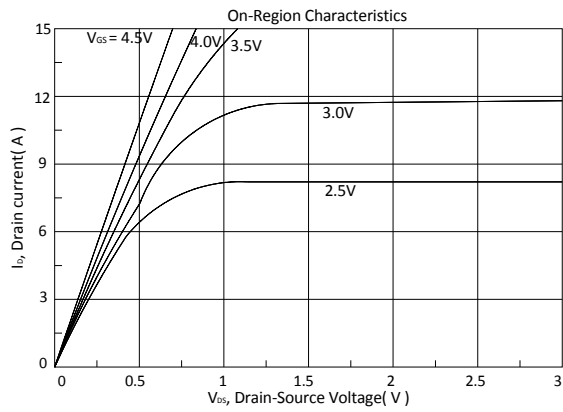
<sup>1</sup>Pulse test : Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$ .

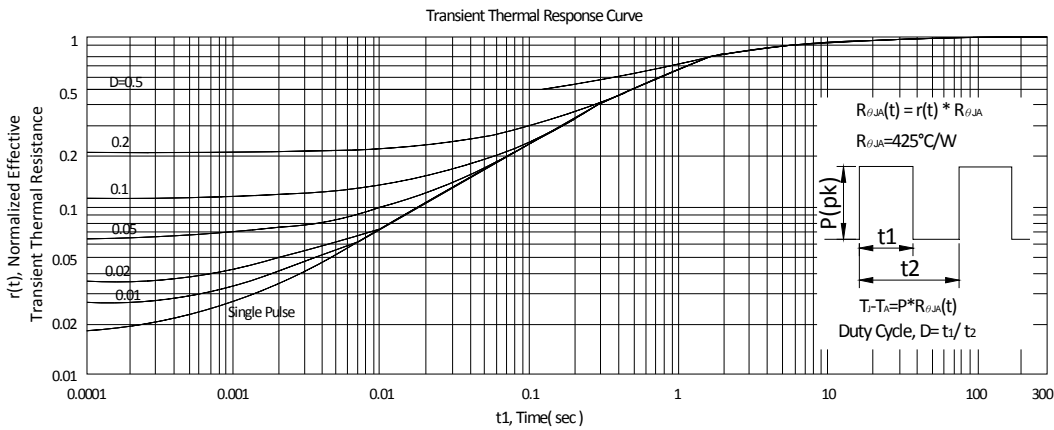
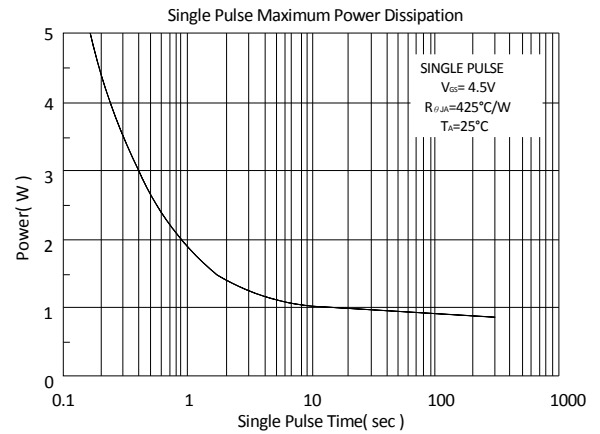
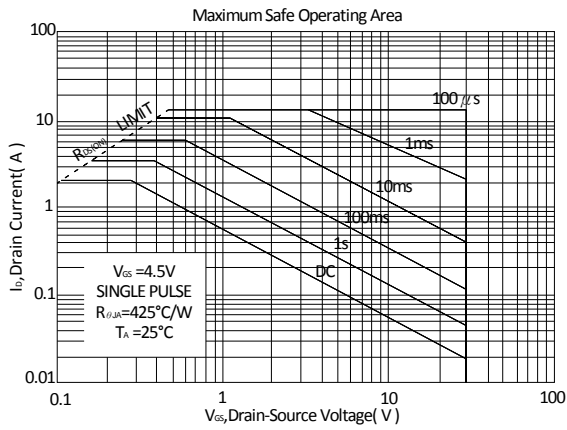
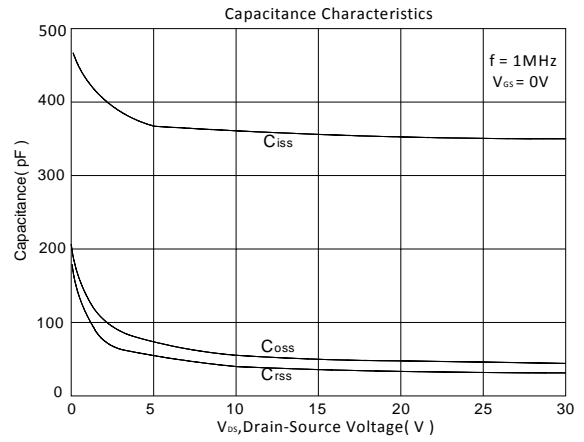
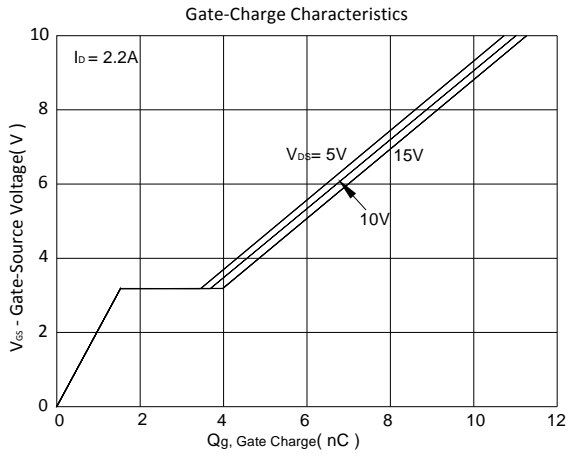
<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Pulse width limited by maximum junction temperature.



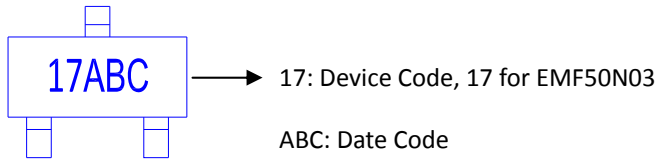
TYPICAL CHARACTERISTICS



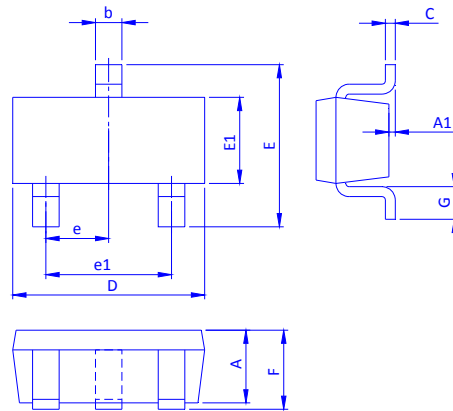


Ordering & Marking Information:

Device Name: EMF50N03M for SC-70



Outline Drawing



Dimension in mm

Dimension	A	A1	b	C	D	E	E1	e	e1	F	G
Min.	0.7	0.0	0.15	0.08						0.90	0.26
Typ.			0.22	0.127	2.1	2.3	1.3	0.65	1.3	0.95	0.4
Max.	1.0	0.1	0.30	0.20						1.10	0.46

Footprint

