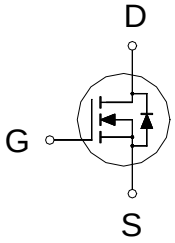
杰力科技股份有限公司
Excelliance MOS Corporation

N-Channel Logic Level Enhancement Mode Field Effect Transistor

Product Summary:

BV_{DSS}	20V
$R_{DSON\ (MAX.)}$	20mΩ
I_D	6A



Pb-Free Lead Plating & Halogen Free

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNIT
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current	T_A = 25 °C	I_D	6	A
	T_A = 70 °C		4	
Pulsed Drain Current[1]		I_{DM}	24	
Power Dissipation	T_A = 25 °C	P_D	1.25	W
	T_A = 70 °C		0.8	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNIT
Junction-to-Ambient	$R_{\theta JA}$		100	°C / W

[1]Pulse width limited by maximum junction temperature.

[2]Duty cycle ≤ 1%

ELECTRICAL CHARACTERISTICS (T_A = 25 °C, Unless Otherwise Noted)

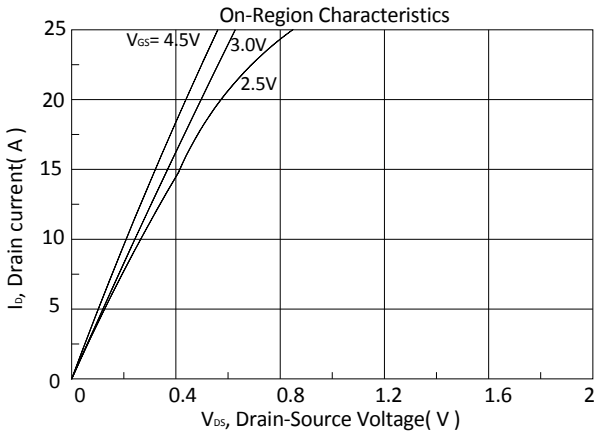
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	20			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	0.35	0.65	1.0	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±12V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 16V, V_{GS} = 0V			1	μA
		V_{DS} = 16V, V_{GS} = 0V, T_J = 125 °C			10	
On-State Drain Current[1]	$I_{D(ON)}$	V_{DS} = 5V, V_{GS} = 4.5V	6			A
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 4.5V, I_D = 6A		17	20	mΩ
		V_{GS} = 2.5V, I_D = 5A		20	25	
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D = 6A		7		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 10V, f = 1MHz		560		pF
Output Capacitance	C_{oss}			166		
Reverse Transfer Capacitance	C_{rss}			150		
Total Gate Charge[1,2]	Q_g	V_{DS} = 10V, V_{GS} = 4.5V, I_D = 6A		8.5		nC
Gate-Source Charge[1,2]	Q_{gs}			1.5		
Gate-Drain Charge[1,2]	Q_{gd}			3.5		
Turn-On Delay Time[1,2]	$t_{d(on)}$	V_{DS} = 10V, I_D = 1A, V_{GS} = 4.5V, R_{GS} = 6Ω		12		nS
Rise Time[1,2]	t_r			15		
Turn-Off Delay Time[1,2]	$t_{d(off)}$			30		
Fall Time[1,2]	t_f			15		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_C = 25 °C)						
Continuous Current	I_S				2	A
Pulsed Current[3]	I_{SM}				8	
Forward Voltage[1]	V_{SD}	I_F = I_S, V_{GS} = 0V			1.2	V

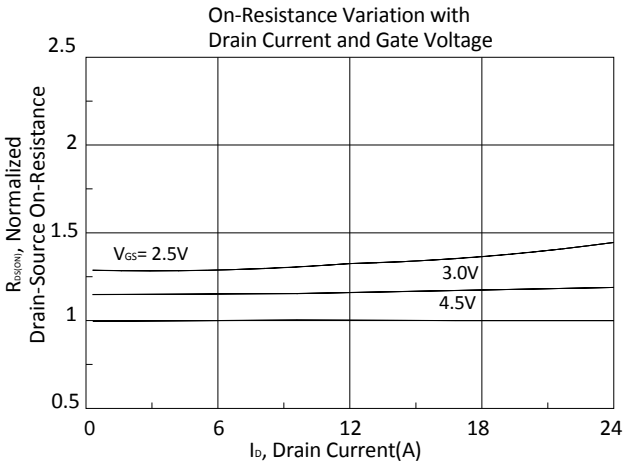
[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

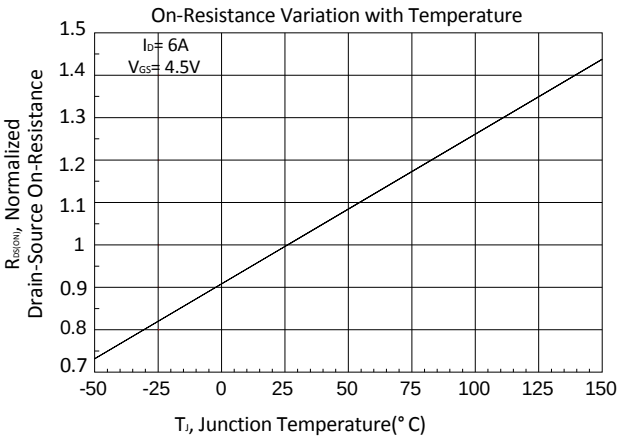
[2]Independent of operating temperature.

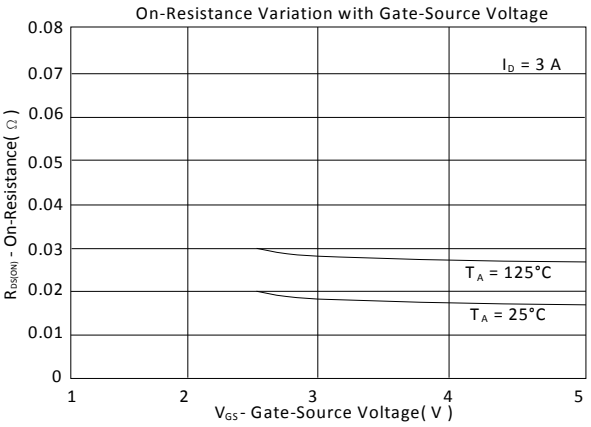
[3]Pulse width limited by maximum junction temperature.

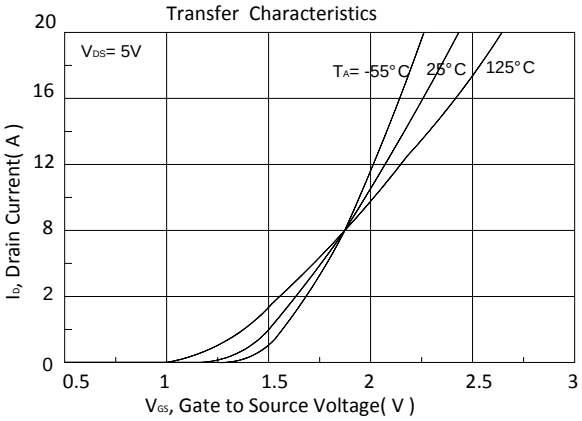
TYPICAL CHARACTERISTICS

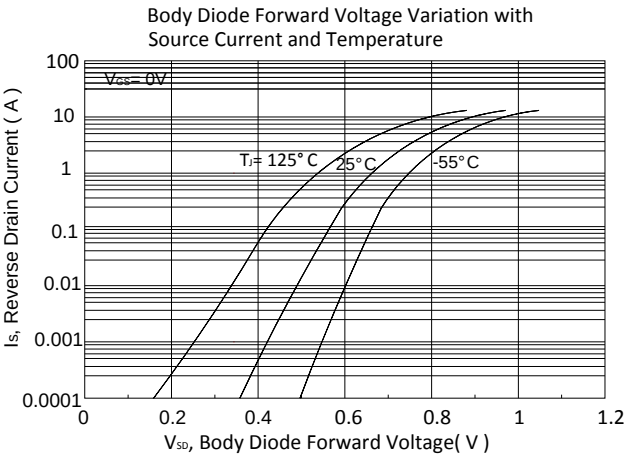


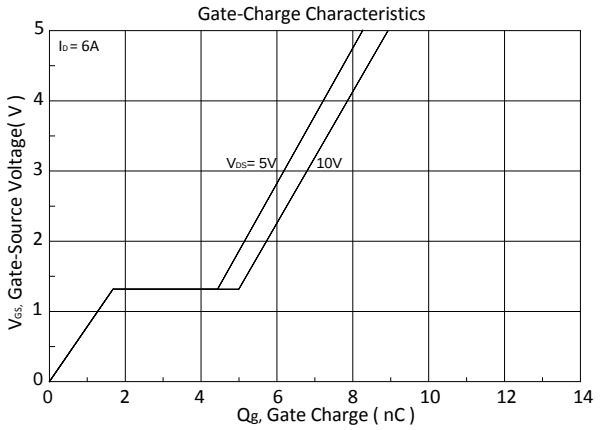


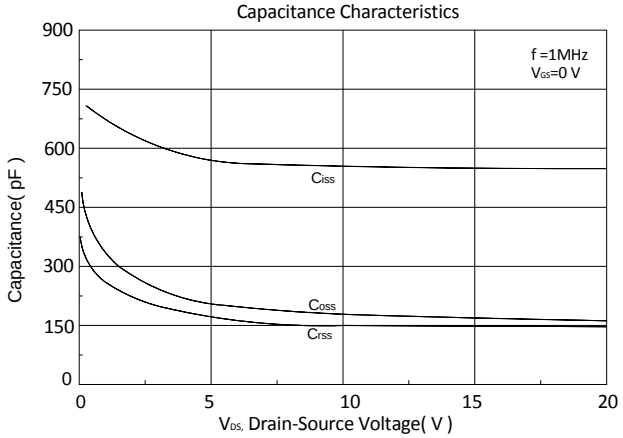


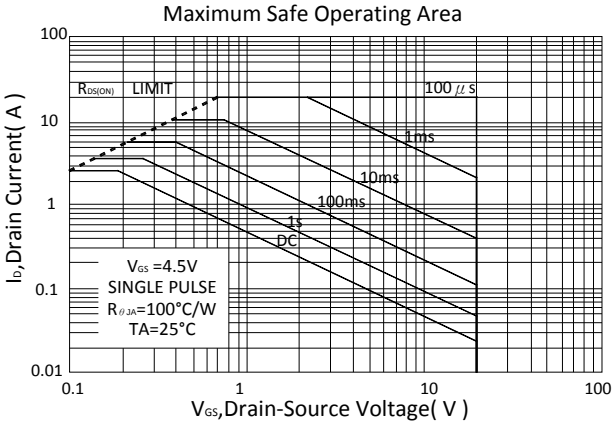


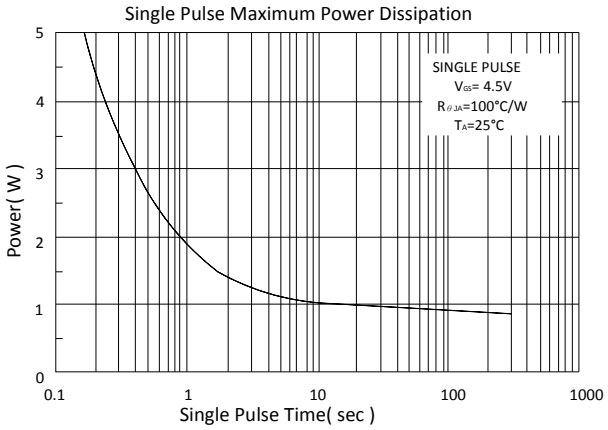


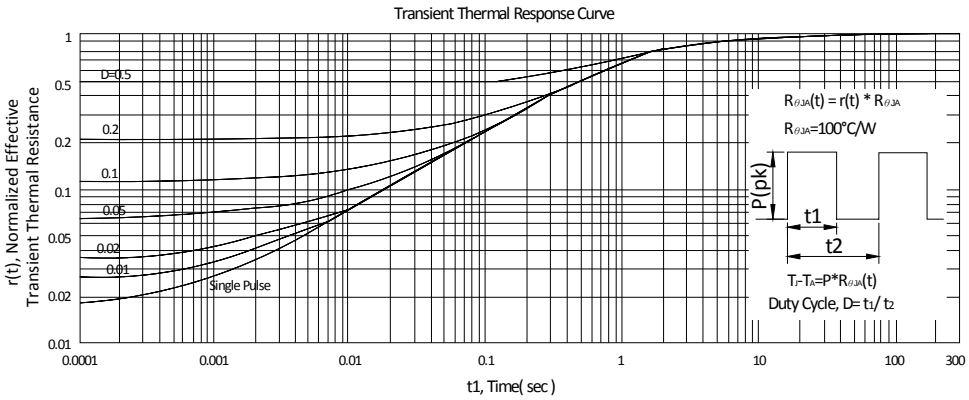



Gate-Charge Characteristics
ID = 6A
VDS= 5V
10V
VGS, Gate-Source Voltage(V)
Qg, Gate Charge (nC)


Capacitance Characteristics
f =1MHz
VGS=0 V
Ciss
Coss
Crss
Capacitance(pF)
VDS, Drain-Source Voltage(V)

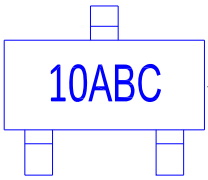

Maximum Safe Operating Area
RDS(ON) LIMIT
100 μs
1ms
10ms
100ms
1s
DC
VGS =4.5V
SINGLE PULSE
RθJA=100°C/W
TA=25°C
ID,Drain Current(A)
VGS,Drain-Source Voltage(V)


Single Pulse Maximum Power Dissipation
SINGLE PULSE
VGS= 4.5V
RθJA=100°C/W
TA=25°C
Power(W)
Single Pulse Time(sec)


Transient Thermal Response Curve
D=0.5
0.2
0.1
0.05
0.02
0.01
Single Pulse
RθJA(t) = r(t) * RθJA
RθJA=100°C/W
P(pk)
t1
t2
TJ-TA=P*RθJA(t)
Duty Cycle, D= t1/ t2
r(t), Normalized Effective Transient Thermal Resistance
t1, Time(sec)

Ordering & Marking Information:

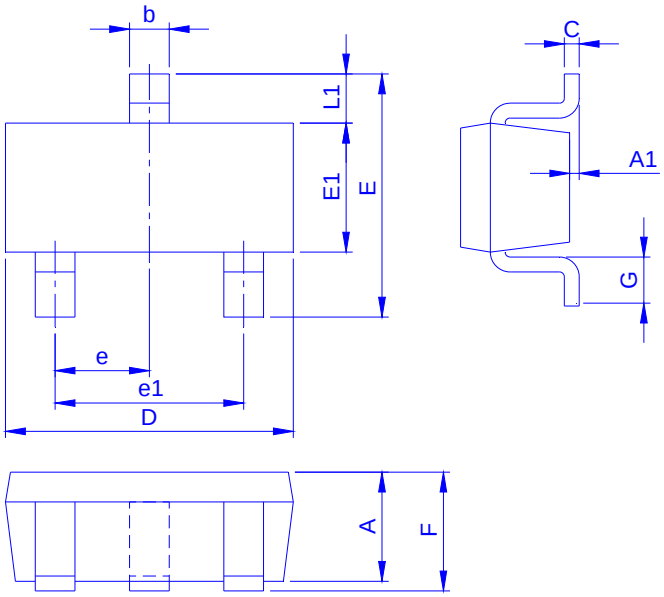
Device Name: EMF20N02J for SOT-23



10: Device Code, 10 for EMF20N02J

ABC: Date Code

Outline Drawing



Dimension in mm

Dimension	A	A1	A2	b	C	D	E	E1	e	e1	F	G	L1
Min.	0.7	0		0.35	0.1	2.8	2.6	1.5	0.9		0.8	0.3	0.55
Typ.						2.9	2.8	1.6	0.95	1.9			
Max.	1.12	0.1		0.5	0.2	3	3	1.7	1		1.2	0.6	0.65

Footprint