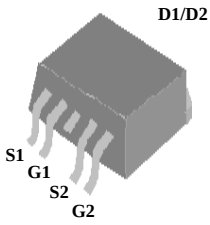
AP10C150H

Halogen-Free Product

Advanced Power Electronics Corp.

N AND P-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **Simple Drive Requirement**
- ▼ **Good Thermal Performance**
- ▼ **Fast Switching Performance**
- ▼ **RoHS Compliant & Halogen-Free**

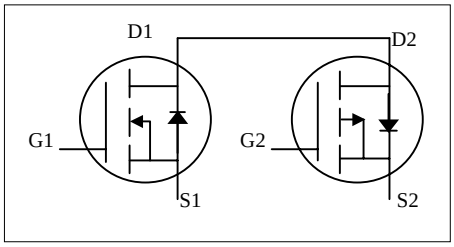


TO-252-4L

N-CH	BV_{DSS}	100V
	$R_{DS(ON)}$	150mΩ
	I_D	8A
P-CH	BV_{DSS}	-100V
	$R_{DS(ON)}$	160mΩ
	I_D	-8A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.



Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating		Units
		N-channel	P-channel	
V_{DS}	Drain-Source Voltage	100	-100	V
V_{GS}	Gate-Source Voltage	±20	±20	V
I_D@T_C=25℃	Drain Current, V_{GS} @ 10V	8	-8	A
I_D@T_C=100℃	Drain Current, V_{GS} @ 10V	5	-5	A
I_D@T_A=25℃	Drain Current, V_{GS} @ 10V[3]	3.2	-3.2	A
I_D@T_A=70℃	Drain Current, V_{GS} @ 10V[3]	2.5	-2.5	A
I_{DM}	Pulsed Drain Current[1]	20	-20	A
P_D@T_C=25℃	Total Power Dissipation	20.8		W
P_D@T_A=25℃	Total Power Dissipation[3]	3.13		W
T_{STG}	Storage Temperature Range	-55 to 150		℃
T_J	Operating Junction Temperature Range	-55 to 150		℃

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-c	Maximum Thermal Resistance, Junction-case	6	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient[3]	40	℃/W

N-CH Electrical Characteristics@ T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	100	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=2A	-	-	150	mΩ
		V_{GS}=5V, I_D=1A	-	-	250	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	1	-	3	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=2A	-	10	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=80V, V_{GS}=0V	-	-	25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Q_g	Total Gate Charge	I_D=2A	-	11	17.6	nC
Q_{gs}	Gate-Source Charge	V_{DS}=50V	-	2	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=10V	-	2	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=50V	-	7	-	ns
t_r	Rise Time	I_D=1A	-	4	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω	-	16	-	ns
t_f	Fall Time	V_{GS}=10V	-	7	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	600	960	pF
C_{oss}	Output Capacitance	V_{DS}=50V	-	35	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	25	-	pF
R_g	Gate Resistance	f=1.0MHz	-	1.8	3.6	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=2.4A, V_{GS}=0V	-	-	1.3	V
t_{rr}	Reverse Recovery Time	I_S=2A, V_{GS}=0V	-	19	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	15	-	nC

P-CH Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=-250uA	-100	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=-10V, I_D=-2A	-	-	160	mΩ
		V_{GS}=-5V, I_D=-1A	-	-	250	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=-250uA	-1	-	-3	V
g_{fs}	Forward Transconductance	V_{DS}=-10V, I_D=-2A	-	7.5	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-80V, V_{GS}=0V	-	-	-25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Q_g	Total Gate Charge	I_D=-2A	-	32	51.2	nC
Q_{gs}	Gate-Source Charge	V_{DS}=-50V	-	5.5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=-10V	-	5	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=-50V	-	12	-	ns
t_r	Rise Time	I_D=-1A	-	4	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω	-	46	-	ns
t_f	Fall Time	V_{GS}=-10V	-	17	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	1900	3040	pF
C_{oss}	Output Capacitance	V_{DS}=-50V	-	60	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	45	-	pF
R_g	Gate Resistance	f=1.0MHz	-	6	12	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=-2.4A, V_{GS}=0V	-	-	-1.3	V
t_{rr}	Reverse Recovery Time	I_S=-2A, V_{GS}=0V	-	25	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=-100A/μs	-	33	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test.

3.N-CH , P-CH are same , mounted on 1 in^2 2oz FR4 board t ≦10s.

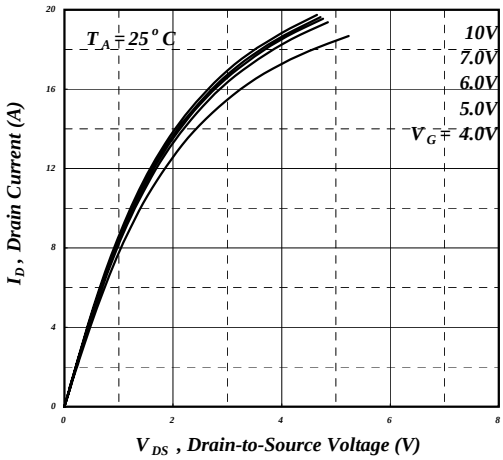


Fig 1. Typical Output Characteristics

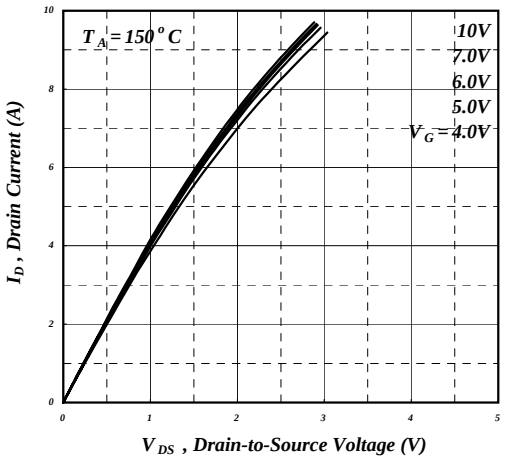


Fig 2. Typical Output Characteristics

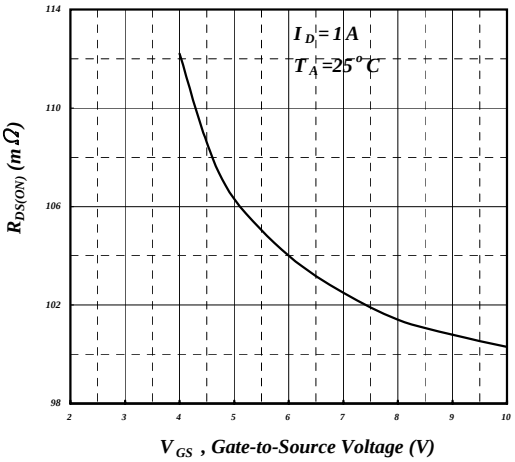


Fig 3. On-Resistance v.s. Gate Voltage

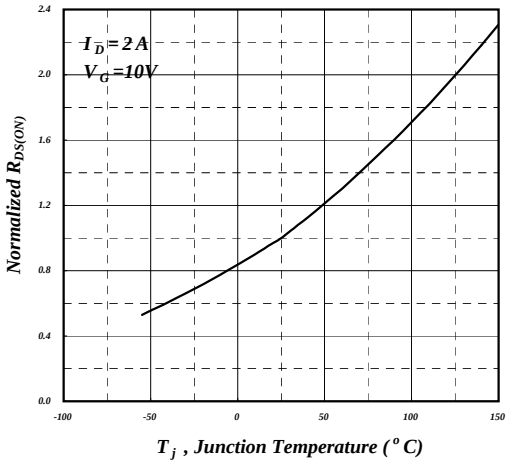


Fig 4. Normalized On-Resistance v.s. Junction Temperature

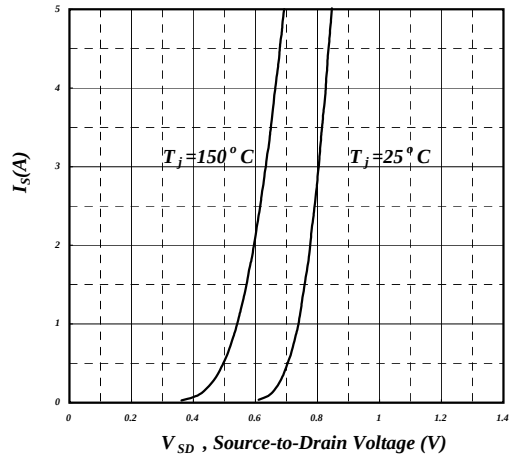


Fig 5. Forward Characteristic of Reverse Diode

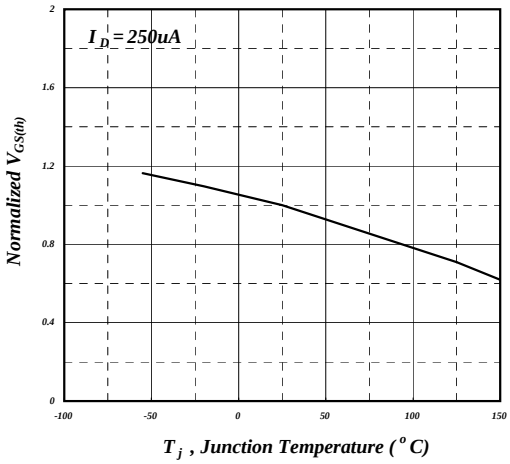


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

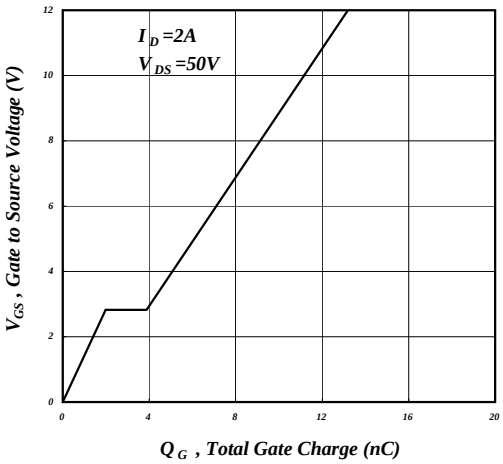


Fig 7. Gate Charge Characteristics

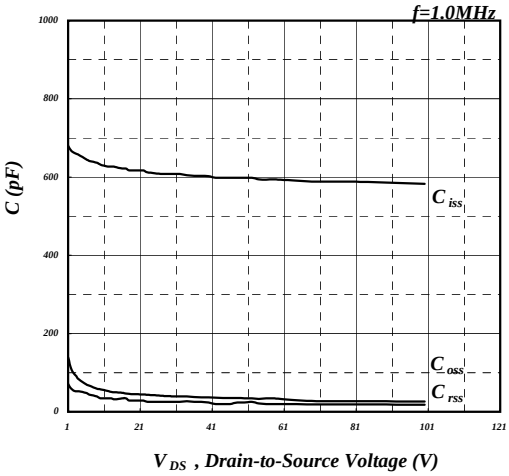


Fig 8. Typical Capacitance Characteristics

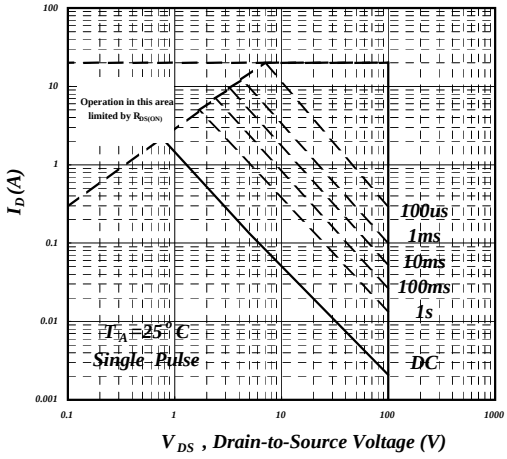


Fig 9. Maximum Safe Operating Area

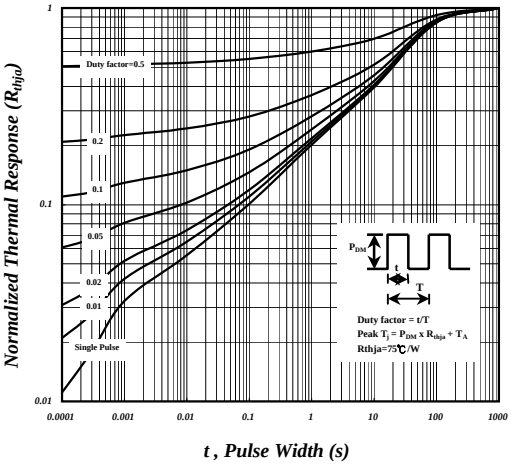


Fig 10. Effective Transient Thermal Impedance

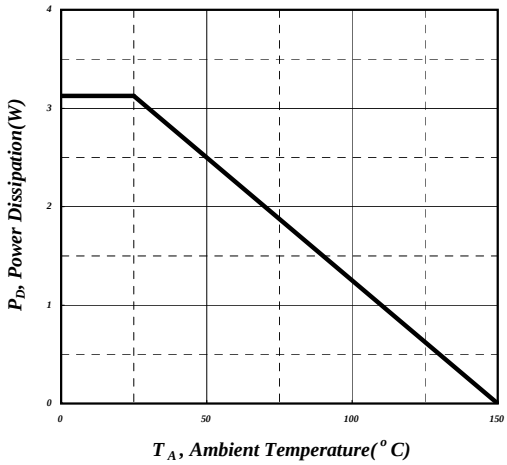


Fig 11. Total Power Dissipation

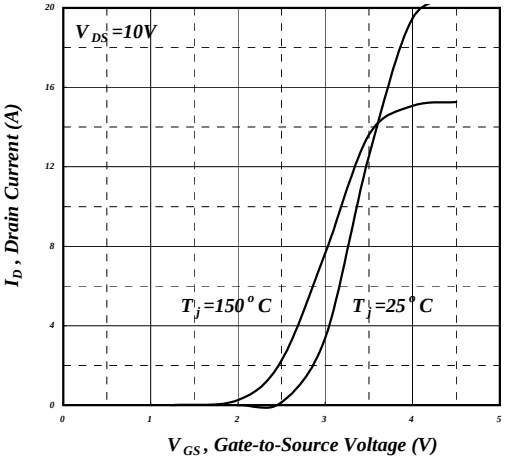


Fig 12. Transfer Characteristics

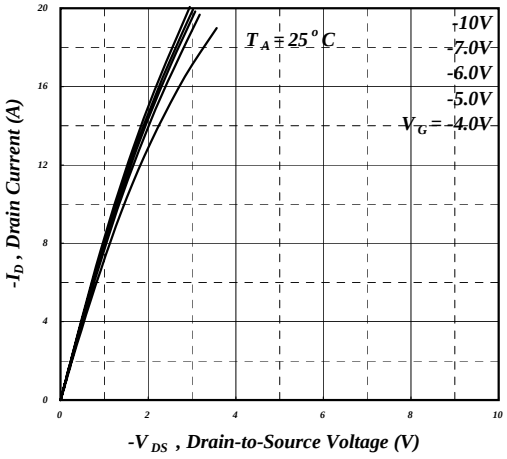


Fig 1. Typical Output Characteristics

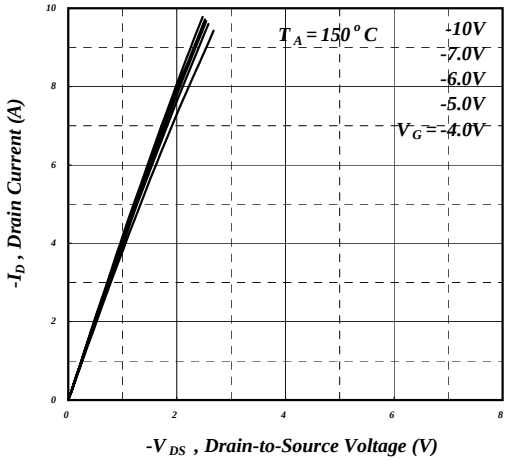


Fig 2. Typical Output Characteristics

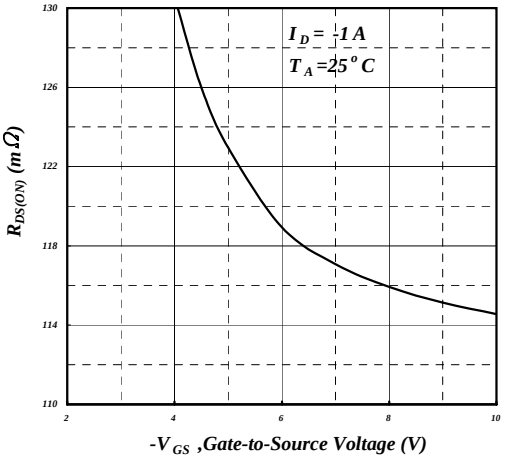


Fig 3. On-Resistance v.s. Gate Voltage

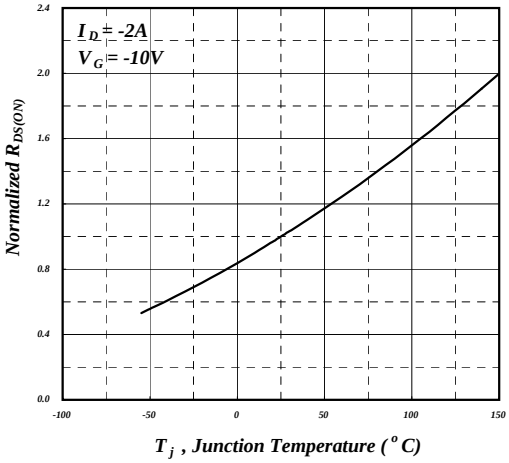


Fig 4. Normalized On-Resistance v.s. Junction Temperature

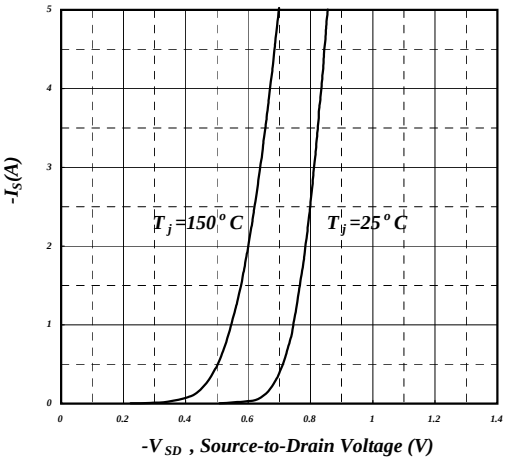


Fig 5. Forward Characteristic of Reverse Diode

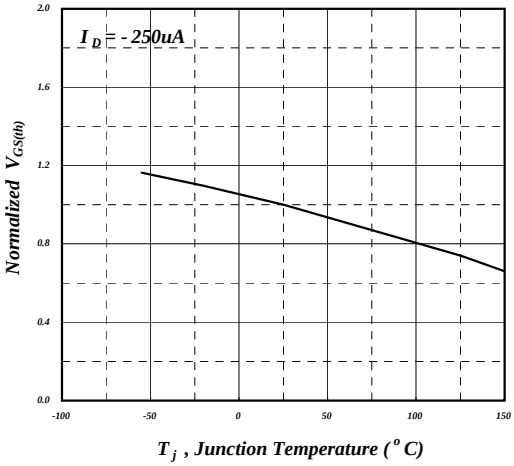


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

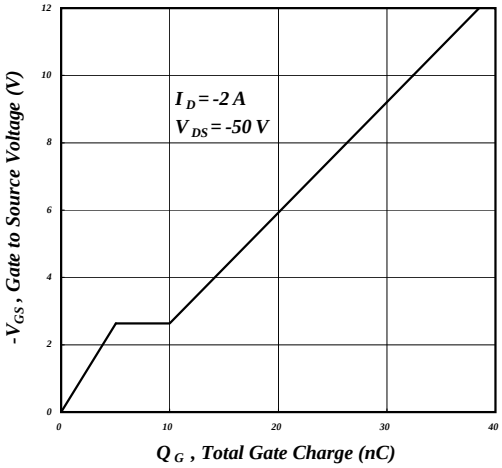
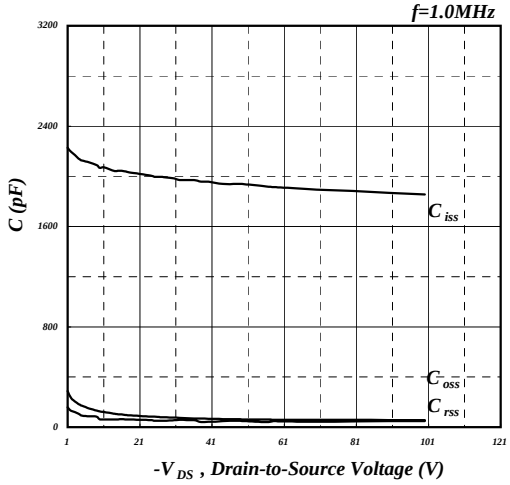


Fig 7. Gate Charge Characteristics



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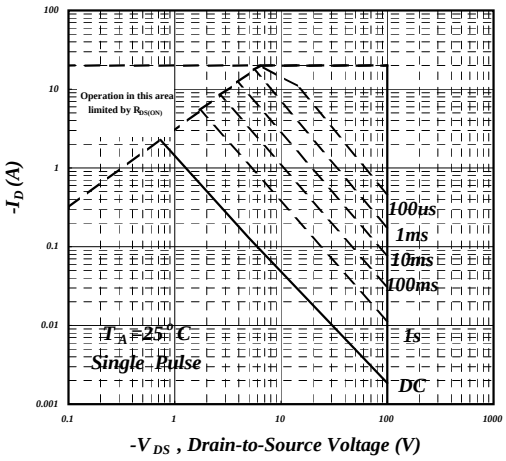

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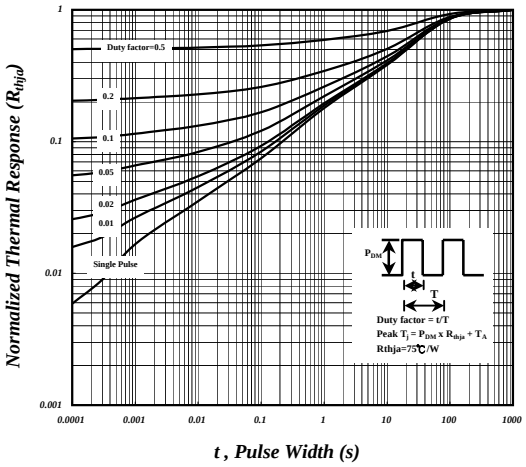


Fig 10. Effective Transient Thermal Impedance

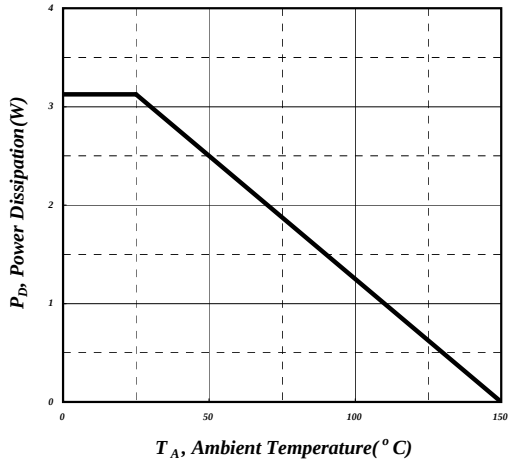


Fig 11. Total Power Dissipation

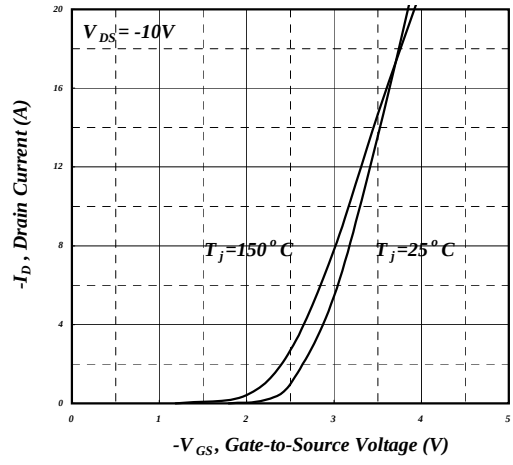
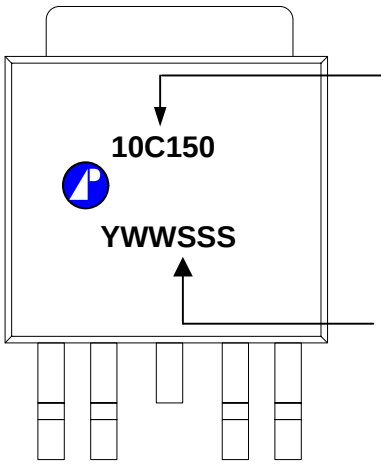


Fig 12. Transfer Characteristics

MARKING INFORMATION



Part Number

Date Code (YWWSSS)

Y：Last Digit Of The Year

WW：Week

SSS：Sequence