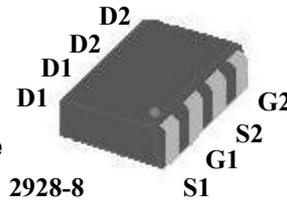




- ▼ Simple Drive Requirement
- ▼ Low Gate Charge
- ▼ Fast Switching Performance
- ▼ RoHS Compliant & Halogen-Free

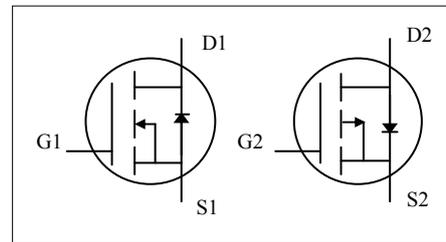


N-CH	$BV_{DSS}$	120V
	$R_{DS(ON)}$	220m $\Omega$
	$I_D$	1.7A
P-CH	$BV_{DSS}$	-120V
	$R_{DS(ON)}$	380m $\Omega$
	$I_D$	-1.3A

## Description

AP4579 series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The 2928-8 J-lead package provides good on-resistance performance and space saving like TSOP-6.



## Absolute Maximum Ratings@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating		Units
		N-channel	P-channel	
$V_{DS}$	Drain-Source Voltage	120	-120	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	$\pm 20$	V
$I_D@T_A=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10V^3$	1.7	-1.3	A
$I_D@T_A=70^\circ\text{C}$	Drain Current, $V_{GS} @ 10V^3$	1.3	-1	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	6	-5	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	1.38		W
$T_{STG}$	Storage Temperature Range	-55 to 150		$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150		$^\circ\text{C}$

## Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	90	$^\circ\text{C}/\text{W}$



# AP4579JY

## N-CH Electrical Characteristics @ $T_j=25^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	120	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=1.7A$	-	-	220	m $\Omega$
		$V_{GS}=4.5V, I_D=1A$	-	-	280	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.4	-	3	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=1.7A$	-	10	-	S
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=96V, V_{GS}=0V$	-	-	25	$\mu A$
$I_{GSS}$	Gate-Source Leakage	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 0.1$	$\mu A$
$Q_g$	Total Gate Charge	$I_D=1.7A$	-	15	24	nC
$Q_{gs}$	Gate-Source Charge	$V_{DS}=60V$	-	2.5	-	nC
$Q_{gd}$	Gate-Drain ("Miller") Charge	$V_{GS}=10V$	-	2.2	-	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=60V$	-	7	-	ns
$t_r$	Rise Time	$I_D=1A$	-	4	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	19	-	ns
$t_f$	Fall Time	$V_{GS}=10V$	-	7	-	ns
$C_{iss}$	Input Capacitance	$V_{GS}=0V$	-	720	1152	pF
$C_{oss}$	Output Capacitance	$V_{DS}=80V$	-	22	-	pF
$C_{rSS}$	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	15	-	pF

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{SD}$	Forward On Voltage <sup>2</sup>	$I_S=1.2A, V_{GS}=0V$	-	-	1.3	V
$t_{rr}$	Reverse Recovery Time	$I_S=1.7A, V_{GS}=0V$	-	24	-	ns
$Q_{rr}$	Reverse Recovery Charge	$dI/dt=100A/\mu s$	-	23	-	nC



**P-CH Electrical Characteristics@T<sub>j</sub>=25°C(unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-120	-	-	V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.2A	-	-	380	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.6A	-	-	450	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.4	-	-3	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.2A	-	6	-	S
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-96V, V <sub>GS</sub> =0V	-	-	-25	uA
I <sub>GSS</sub>	Gate-Source Leakage	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±0.1	uA
Q <sub>g</sub>	Total Gate Charge	I <sub>D</sub> =-1.2A	-	21	33.6	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-60V	-	3.5	-	nC
Q <sub>gd</sub>	Gate-Drain ("Miller") Charge	V <sub>GS</sub> =-10V	-	2.7	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> =-60V	-	10	-	ns
t <sub>r</sub>	Rise Time	I <sub>D</sub> =-1A	-	4	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time	R <sub>G</sub> =3.3Ω	-	38	-	ns
t <sub>f</sub>	Fall Time	V <sub>GS</sub> =-10V	-	10	-	ns
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V	-	1100	1760	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =-80V	-	28	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f=1.0MHz	-	20	-	pF

**Source-Drain Diode**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>SD</sub>	Forward On Voltage <sup>2</sup>	I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V	-	-	-1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> =-1.2A, V <sub>GS</sub> =0V,	-	24	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	di/dt=100A/μs	-	23	-	nC

**Notes:**

- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board, t ≤10sec ; 250 °C/W when mounted on Min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT, AUTOMOTIVE OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED. APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.



# AP4579JY

## N-Channel

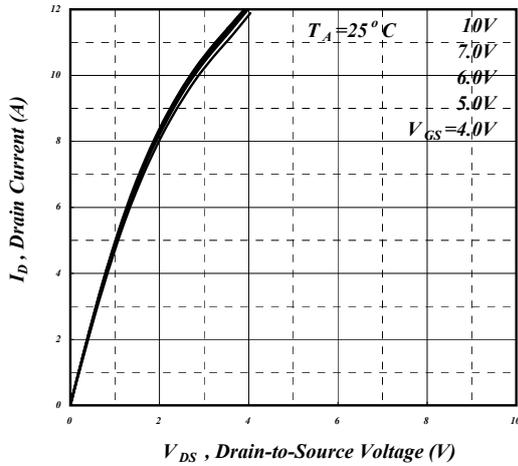


Fig 1. Typical Output Characteristics

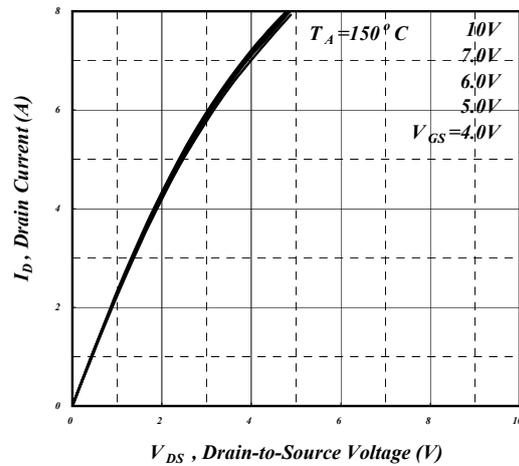


Fig 2. Typical Output Characteristics

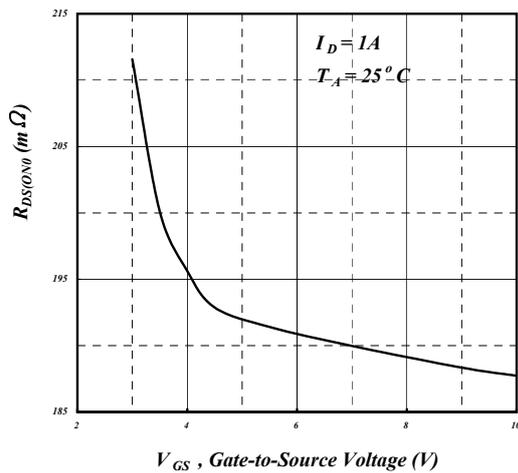


Fig 3. On-Resistance v.s. Gate Voltage

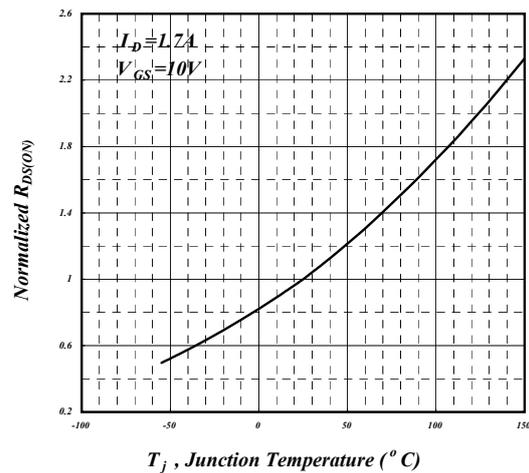


Fig 4. Normalized On-Resistance v.s. Junction Temperature

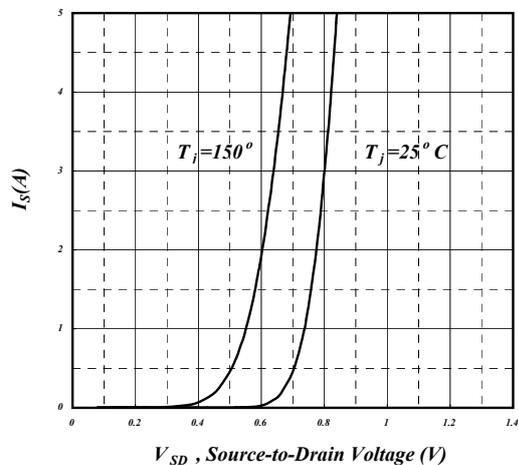


Fig 5. Forward Characteristic of Reverse Diode

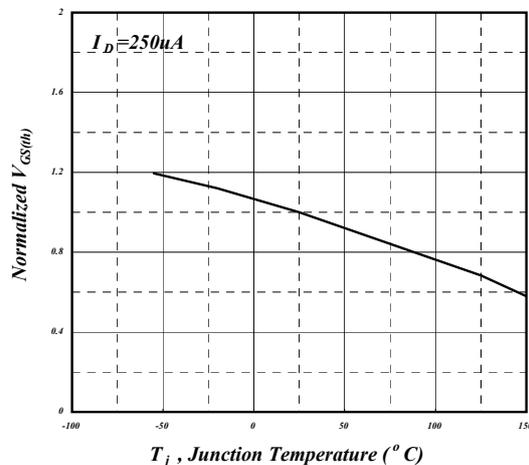


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



N-Channel

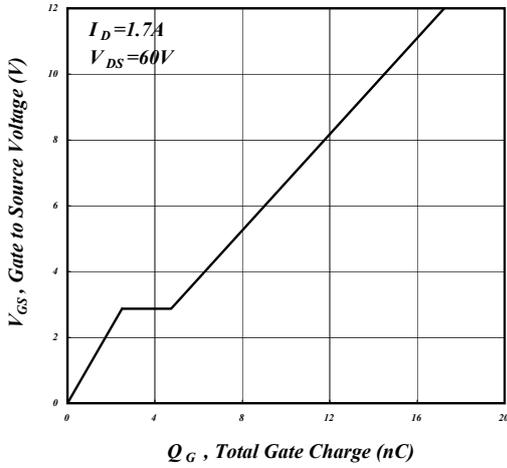


Fig 7. Gate Charge Characteristics

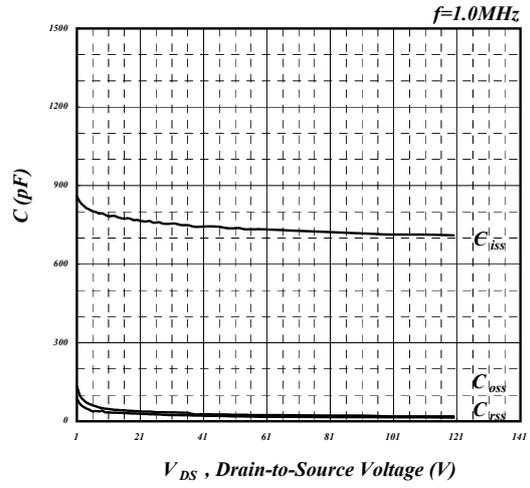


Fig 8. Typical Capacitance Characteristics

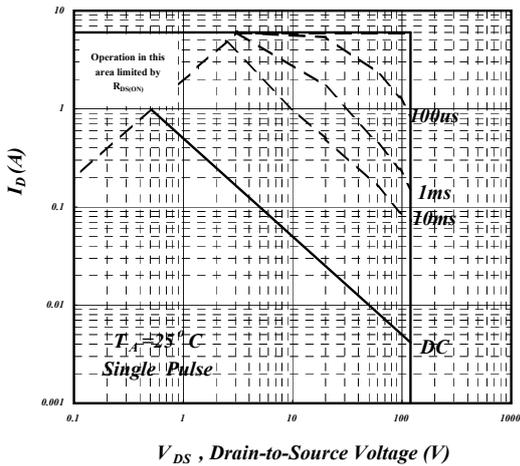


Fig 9. Maximum Safe Operating Area

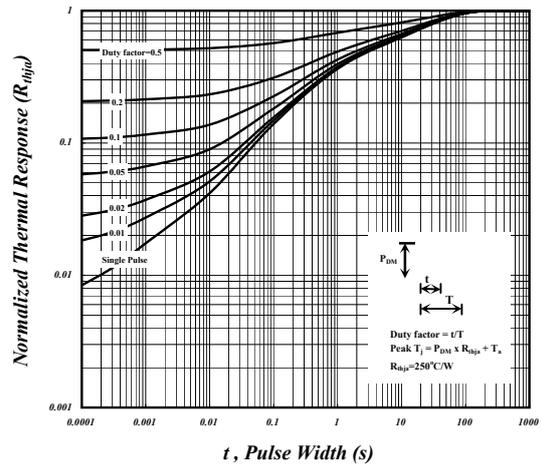


Fig 10. Effective Transient Thermal Impedance

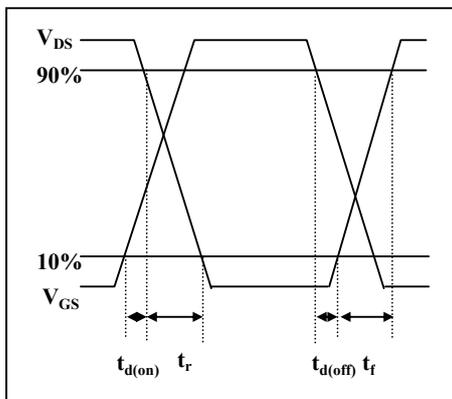


Fig 11. Switching Time Waveform

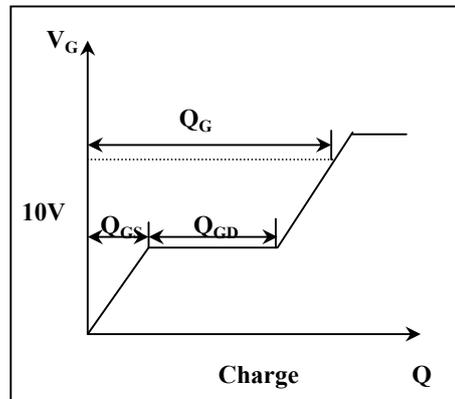
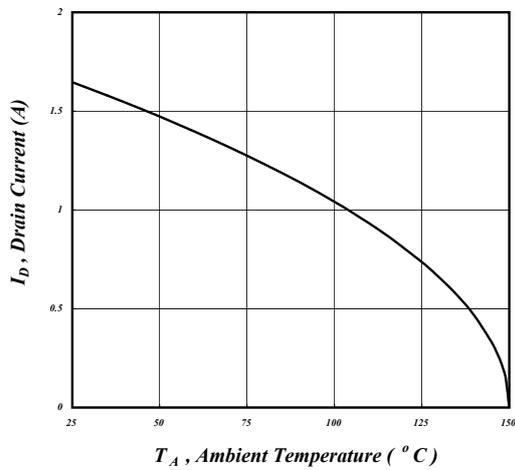


Fig 12. Gate Charge Waveform

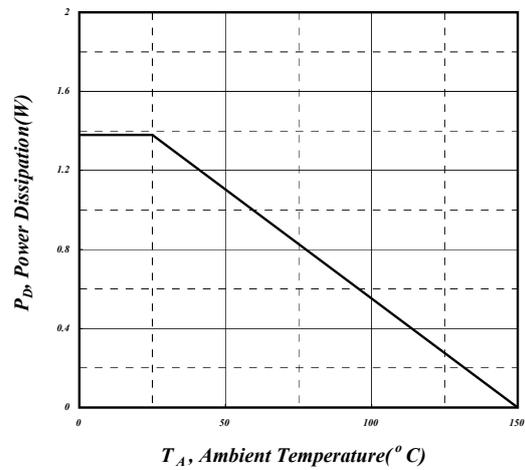


# AP4579JY

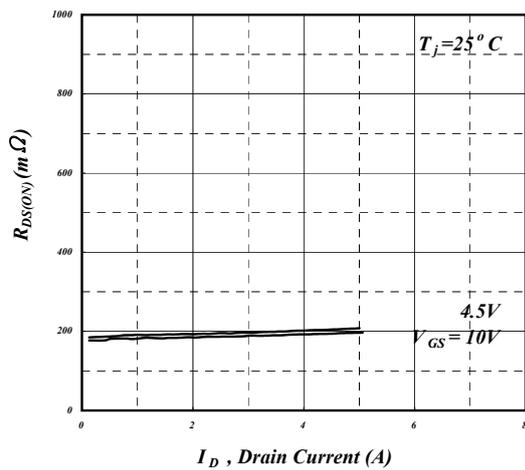
## N-Channel



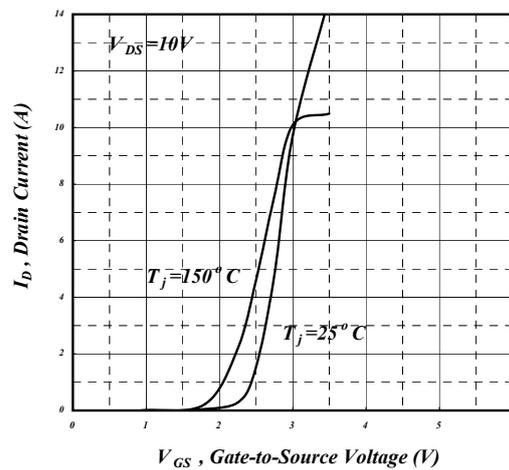
**Fig 13. Drain Current v.s. Ambient Temperature**



**Fig 14. Total Power Dissipation**



**Fig 15. Typ. Drain-Source on State Resistance**



**Fig 16. Transfer Characteristics**



P-Channel

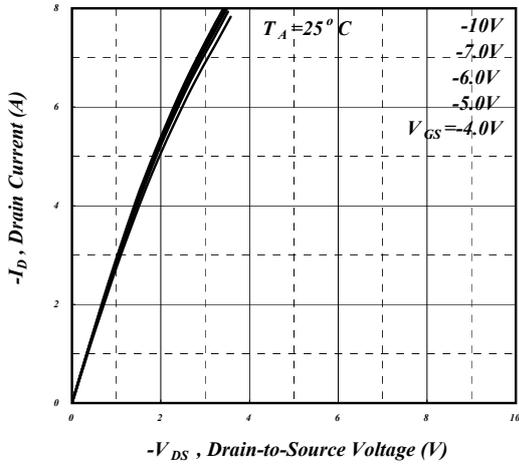


Fig 1. Typical Output Characteristics

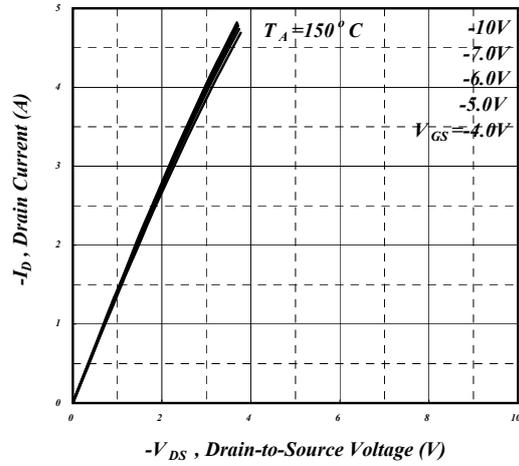


Fig 2. Typical Output Characteristics

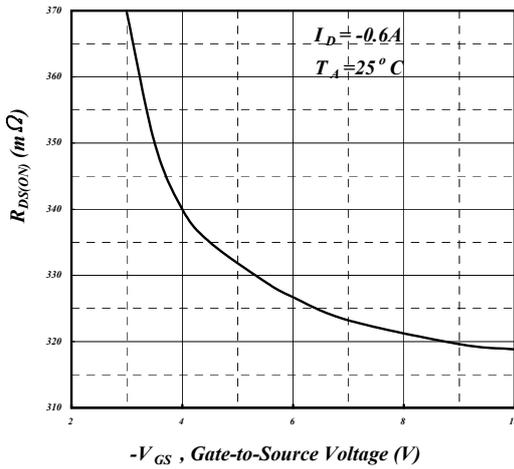


Fig 3. On-Resistance v.s. Gate Voltage

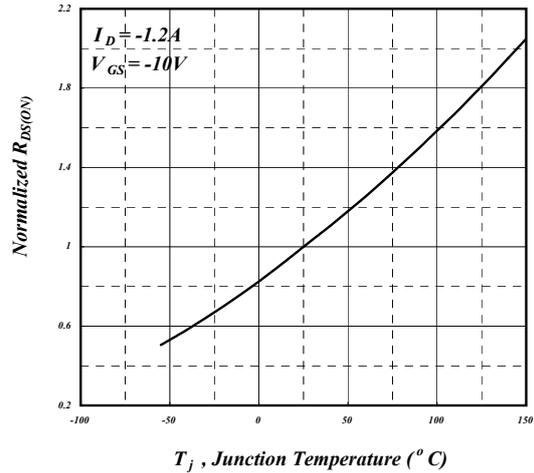


Fig 4. Normalized On-Resistance v.s. Junction Temperature

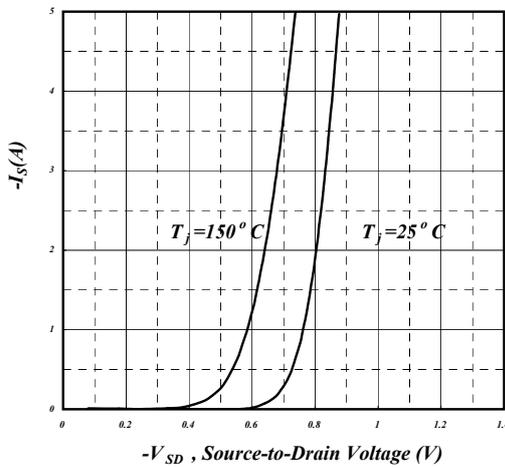


Fig 5. Forward Characteristic of Reverse Diode

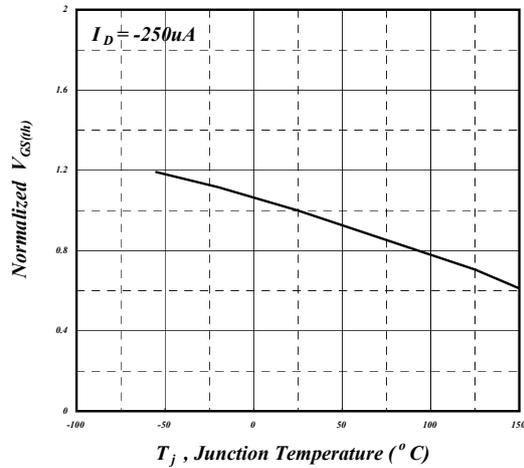


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



# AP4579JY

## P-Channel

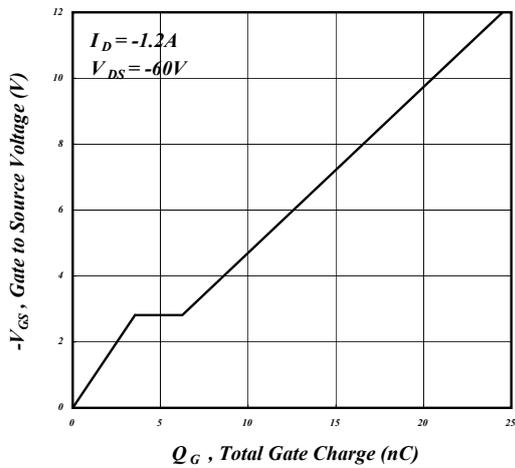


Fig 7. Gate Charge Characteristics

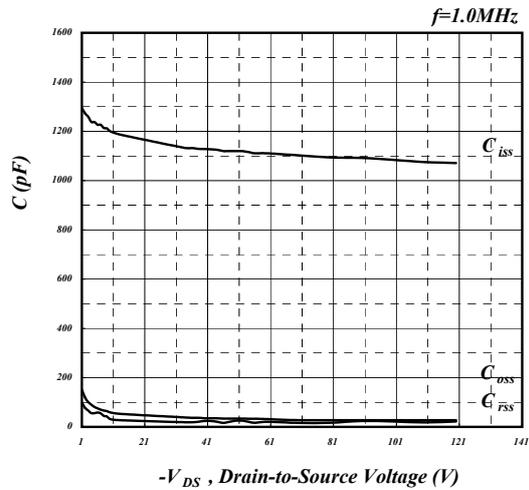


Fig 8. Typical Capacitance Characteristics

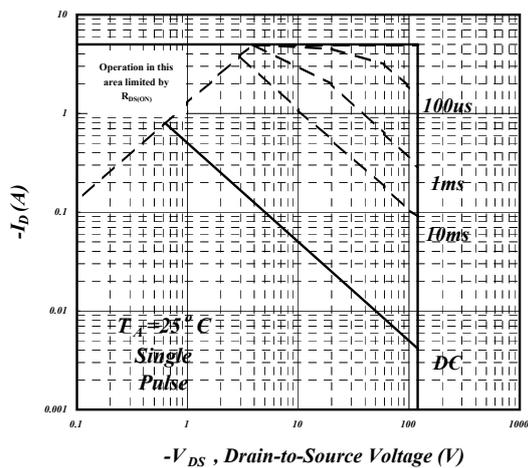


Fig 9. Maximum Safe Operating Area

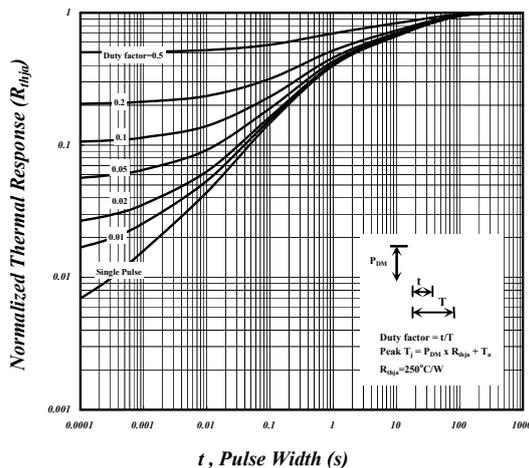


Fig 10. Effective Transient Thermal Impedance

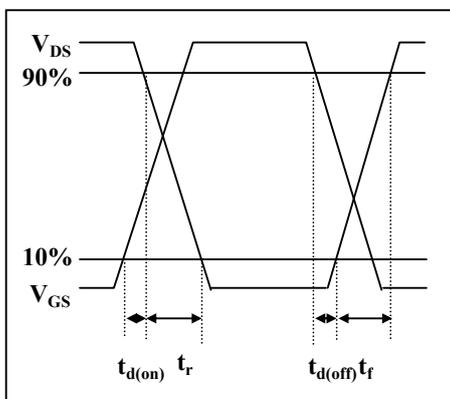


Fig 11. Switching Time Waveform

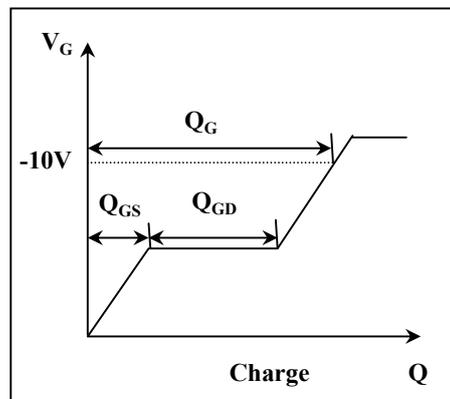


Fig 12. Gate Charge Waveform



P-Channel

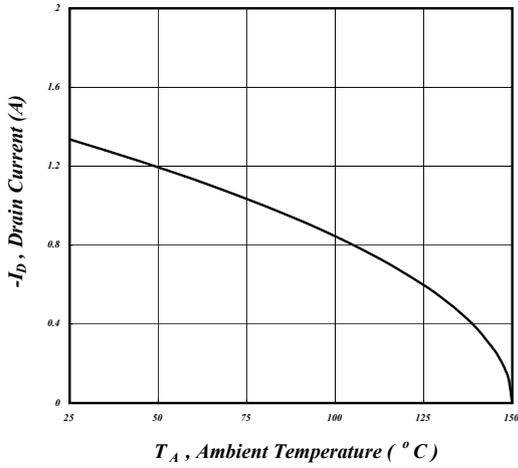


Fig 13. Drain Current v.s. Ambient Temperature

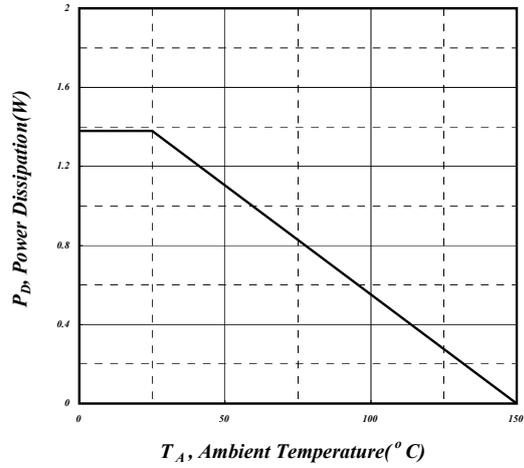


Fig 14. Total Power Dissipation

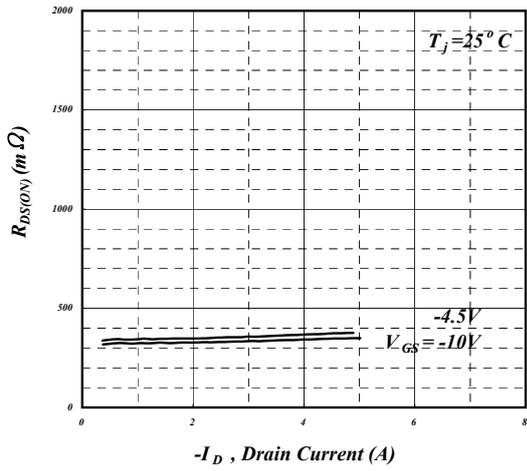


Fig 15. Typ. Drain-Source on State Resistance

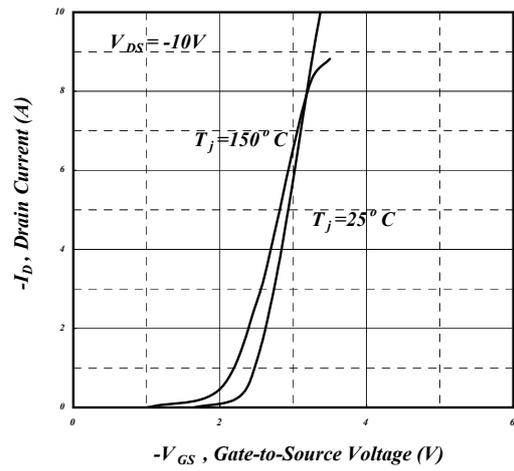


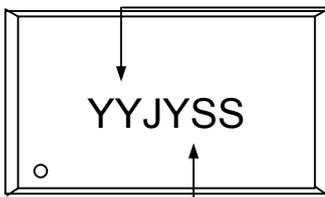
Fig 16. Transfer Characteristics



**AP4579JY**

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**MARKING INFORMATION**



Part Number : YYJ

Date Code (YSS)

Y : Last Digit Of The Year

SS : Sequence