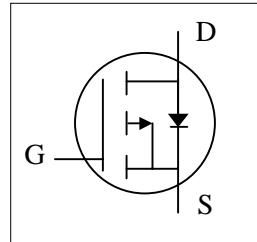
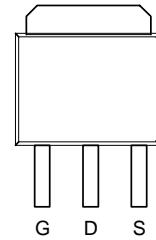




- ▼ Higher Gate-Source Voltage
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant & Halogen-Free



BV_{DSS}	-60V
$R_{DS(ON)}$	64mΩ
I_D	-17A



TO-251S(JB)

Description

AP9575A series are from Advanced Power innovative design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-251S short lead package is preferred for all commercial-industrial through-hole applications without lead-cutted.

Absolute Maximum Ratings@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D @ T_c=25^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	-17	A
$I_D @ T_c=100^\circ\text{C}$	Drain Current, $V_{GS} @ 10\text{V}$	-11	A
I_{DM}	Pulsed Drain Current ¹	-60	A
$P_D @ T_c=25^\circ\text{C}$	Total Power Dissipation	36	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Maximum Thermal Resistance, Junction-case	3.5	°C/W
R_{thj-a}	Maximum Thermal Resistance, Junction-ambient	110	°C/W



AP9575AGJB

Electrical Characteristics@ $T_j=25^\circ C$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-10V, I_D=-12A$	-	-	64	$m\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-	-3	V
g_{fs}	Forward Transconductance	$V_{DS}=-10V, I_D=-9A$	-	12	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-48V, V_{GS}=0V$	-	-	-25	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 30V, V_{DS}=0V$	-	-	± 100	nA
Q_g	Total Gate Charge	$I_D=-12A$	-	35	56	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=-48V$	-	5	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=-10V$	-	12	-	nC
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=-30V$	-	12	-	ns
t_r	Rise Time	$I_D=-12A$	-	23	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega$	-	45	-	ns
t_f	Fall Time	$V_{GS}=-10V$	-	60	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	1440	2300	pF
C_{oss}	Output Capacitance	$V_{DS}=-25V$	-	160	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	120	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_S=-12A, V_{GS}=0V$	-	-	-1.3	V
t_{rr}	Reverse Recovery Time	$I_S=-12A, V_{GS}=0V,$	-	43	-	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=-100A/\mu s$	-	75	-	nC

Notes:

1.Pulse width limited Max. junction temperature.

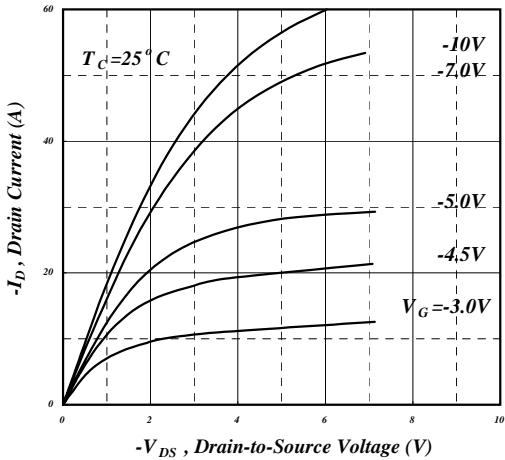
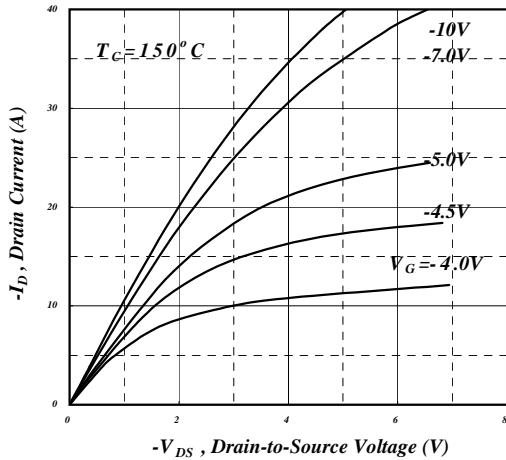
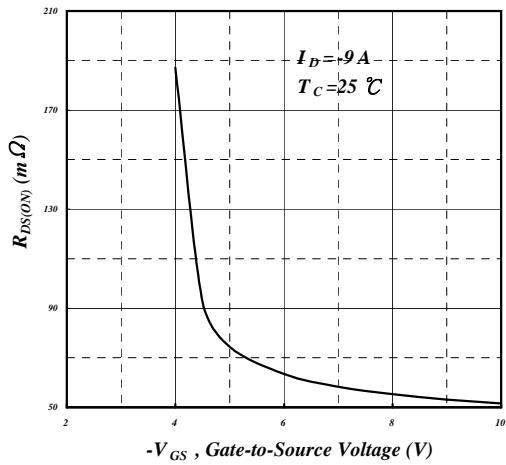
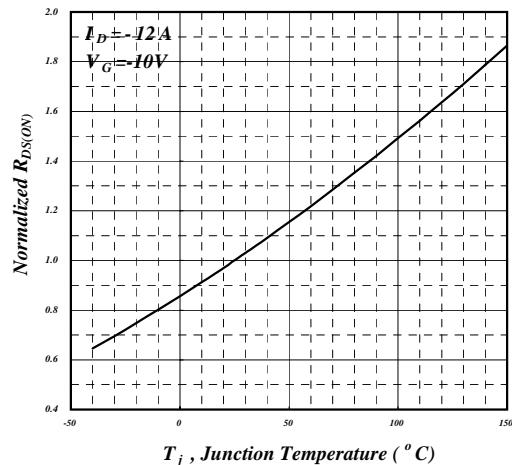
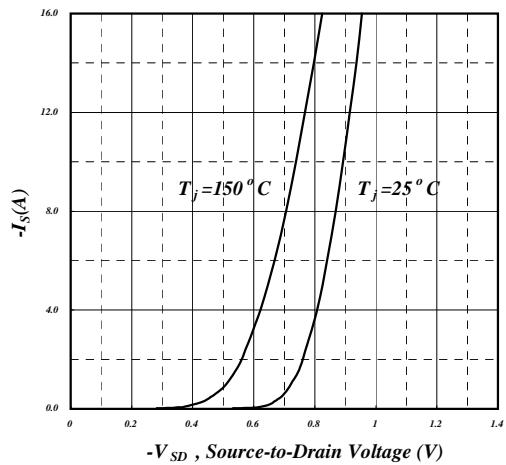
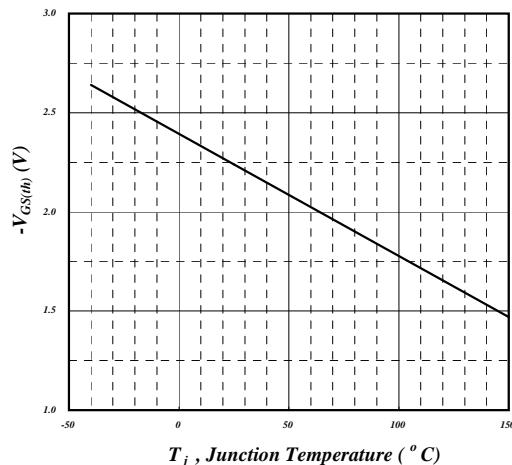
2.Pulse test

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

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APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.


Fig 1. Typical Output Characteristics

Fig 2. Typical Output Characteristics

Fig 3. On-Resistance v.s. Gate Voltage

Fig 4. Normalized On-Resistance v.s. Junction Temperature

Fig 5. Forward Characteristic of Reverse Diode

Fig 6. Gate Threshold Voltage v.s. Junction Temperature

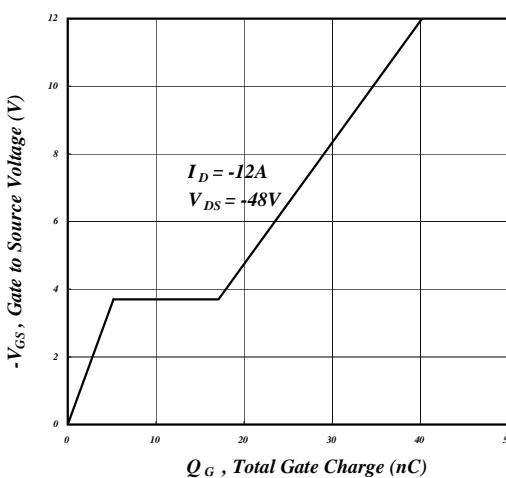


Fig 7. Gate Charge Characteristics

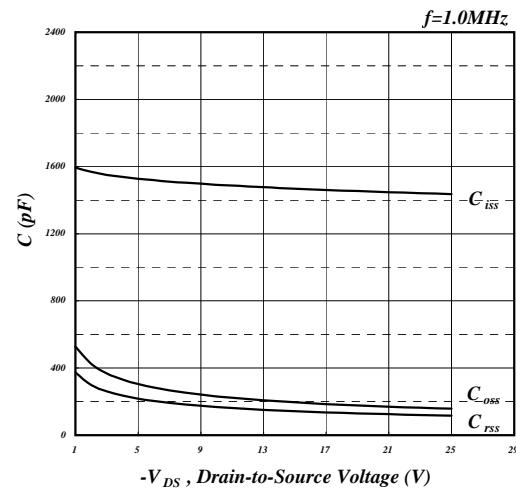


Fig 8. Typical Capacitance Characteristics

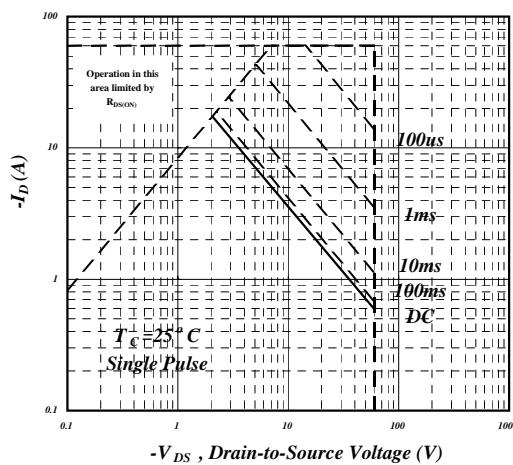


Fig 9. Maximum Safe Operating Area

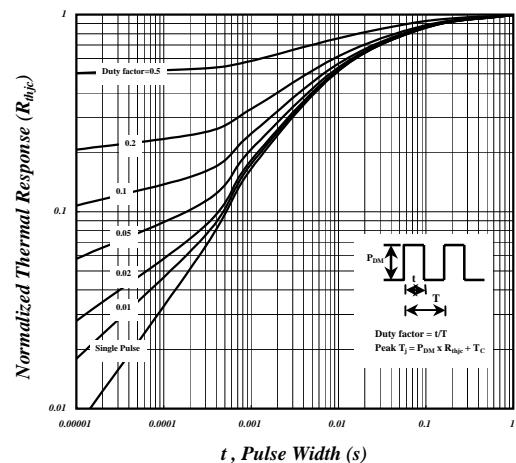


Fig 10. Effective Transient Thermal Impedance

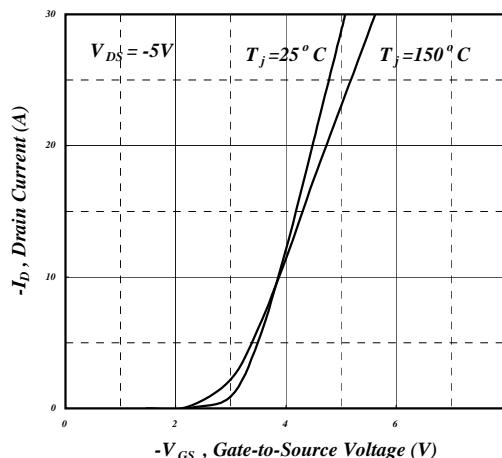


Fig 11. Transfer Characteristics

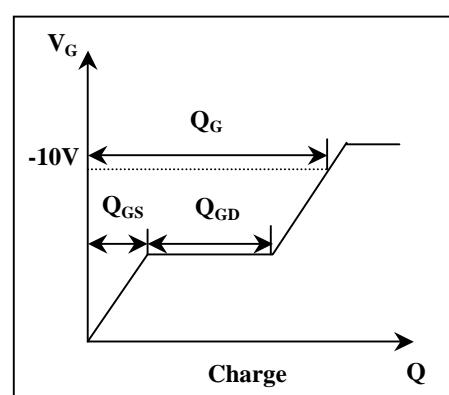


Fig 12. Gate Charge Waveform



AP9575AGJB

MARKING INFORMATION

