

P-Channel 30-V (D-S) MOSFET

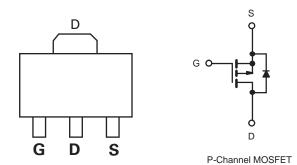
| PRODUCT SUMMARY | | | | | | |
|---------------------|------------------------------------|--------------------|-----------------------|--|--|--|
| V _{DS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) | Q _g (Typ.) | | | |
| - 30 | 0.049 at $V_{GS} = -10 \text{ V}$ | -6.6 ^a | 20 nC | | | |
| - 30 | 0.070 at V _{GS} = - 4.5 V | -5 ^a | 20110 | | | |

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested

APPLICATIONS

- DC/DC Converter
 - Load Switch
 - Adaptor Switch



| Parameter | Symbol | Limit | Unit | | |
|--|---|----------------|--|---|--|
| Drain-Source Voltage | V _{DS} | - 30 | V | | |
| Gate-Source Voltage | V_{GS} | ± 20 | V | | |
| Continuous Drain Current (T _J = 150 °C) | $T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 85 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 85 ^{\circ}\text{C}$ | I _D | - 6.6 ^a - 4.8 - 5 ^{a, b, c} -4.2 ^{b, c} | A | |
| Pulsed Drain Current | I _{DM} | - 18 | | | |
| Continuous Source-Drain Diode Current | $T_C = 25 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$ | I _S | - 5.3 - 2.1 ^{b, c} |] | |
| Maximum Power Dissipation | $T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 85 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 85 ^{\circ}\text{C}$ | P _D | 5.5 3 2.3 ^{b, c} | W | |
| Operating Junction and Storage Temperature Ra | T _J , T _{stg} | - 55 to 150 | °C | | |
| Soldering Recommendations (Peak Temperature | | 260 | | | |

| THERMAL RESISTANCE RATINGS | | | | | | | |
|----------------------------------|--------------|------------|---------|---------|------|--|--|
| Parameter | | Symbol | Typical | Maximum | Unit | | |
| Maximum Junction-to-Ambient | t ≤ 5 s | R_{thJA} | 50 | 60 | °C/W | | |
| Maximum Junction-to-Foot (Drain) | Steady State | R_{thJF} | 16 | 25 | C/VV | | |

Notes:

- a. Package limited.b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.



| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|---|-----------------------------------|--|------|-------|-------|-------|
| Static | ·I. | , | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$ | - 30 | | | V |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = - 250 μA | | - 30 | | mV/°C |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | η = - 250 μΑ | | 5 | | |
| Gate-Source Threshold Voltage | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | - 1 | | - 3 | V |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA |
| Zana Cata Valta na Duain Commant | I _{DSS} | V _{DS} = - 30 V, V _{GS} = 0 V | | | - 1 | μΑ |
| Zero Gate Voltage Drain Current | | V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 85 °C | | | - 5 | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$ | - 18 | | | Α |
| | Р | V _{GS} = - 10 V, I _D = - 5A | | 0.049 | 0.055 | Ω |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | V _{GS} = - 4.5 V, I _D = - 4A | | 0.070 | 0.079 | |
| Forward Transconductance ^a | g _{fs} | V _{DS} = - 15 V, I _D = - 4A | | 18 | | S |
| Dynamic ^b | | | | | | |
| Input Capacitance | C _{iss} | | | 1120 | | pF |
| Output Capacitance | C _{oss} | V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz | | 205 | | |
| Reverse Transfer Capacitance | C _{rss} | 1 | | 115 | | |
| Total Cata Charge | Q _g Q _{gs} | V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 5 A | | 30 | 42 | nC |
| Total Gate Charge | | | | 20 | 33 | |
| Gate-Source Charge | | $V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4 \text{ A}$ | | 5.5 | | |
| Gate-Drain Charge | Q_{gd} | | | 6 | | |
| Gate Resistance | R_{g} | f = 1 MHz | | 8 | 12 | Ω |
| Turn-On Delay Time | t _{d(on)} | | | 75 | | |
| Rise Time | t _r | $V_{DD} = -15 \text{ V}, R_{L} = 2.6 \Omega$ | | 160 | | ns |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong -4 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$ | | 38 | | |
| Fall Time | t _f |] | | 27 | | |
| Turn-On Delay Time | t _{d(on)} | | | 16 | | |
| Rise Time | t _r | $V_{DD} = -15 \text{ V}, R_{L} = 2.6 \Omega$ | | 12 | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong -5 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$ | | 35 | | |
| Fall Time | t _f |] | | 13 | | |
| Drain-Source Body Diode Characteristic | s | | | | | |
| Continuous Source-Drain Diode Current | I _S | T _C = 25 °C | | | -6.6 | A |
| Pulse Diode Forward Current | I _{SM} | | | | -18 | |
| Body Diode Voltage | V_{SD} | I _S = - 5.8 A, V _{GS} = 0 V | | - 0.8 | - 1.2 | V |
| Body Diode Reverse Recovery Time | t _{rr} | | | 22 | 33 | ns |
| Body Diode Reverse Recovery Charge | Q _{rr} | I _F = - 5.8 A, dl/dt = - 100 A/μs, T _J = 25 °C | | 16 | 25 | nC |
| Reverse Recovery Fall Time | t _a | 1 i _F = - 3.6 A, αι/αι = - 100 A/μS, 1 j = 25 °C | | 15 | | |
| Reverse Recovery Rise Time | t _b | 1 | | 9 | | ns |

Notes:

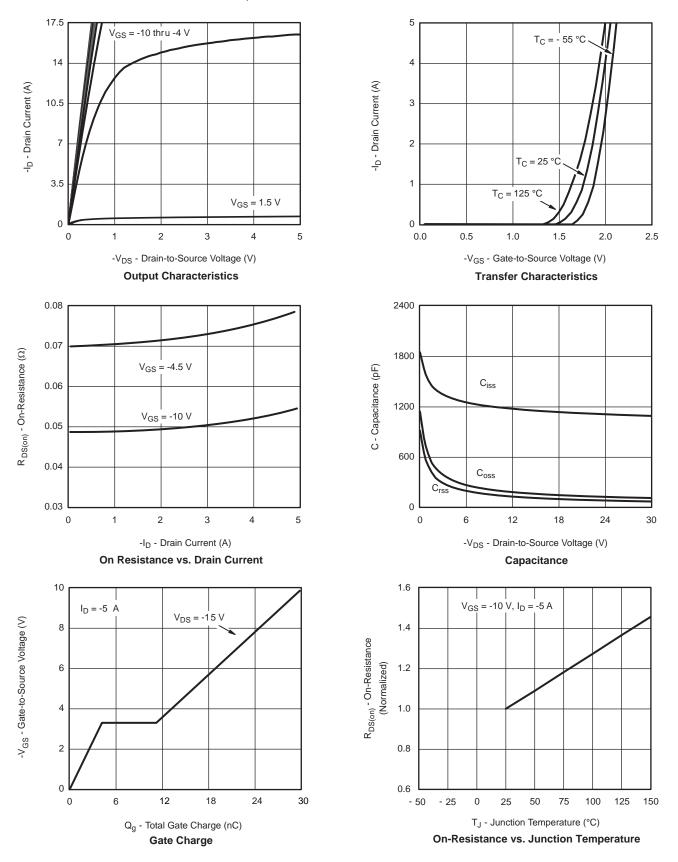
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

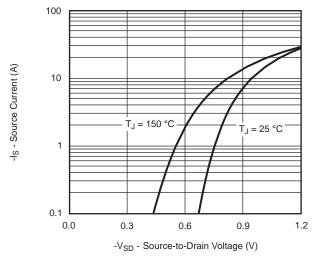


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

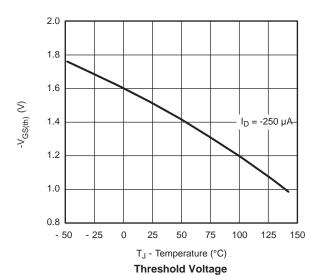




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



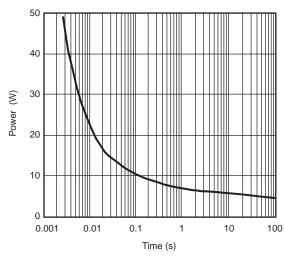
Forward Diode Voltage vs. Temp.



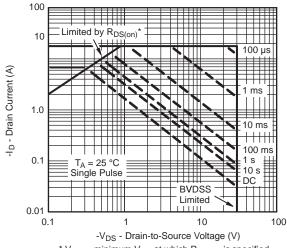
 $C_{\text{O}} = 0.08$ $C_{\text{O}} = 0.07$ $C_{\text{O}} = 0.06$ $C_{\text{O}} =$

-V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



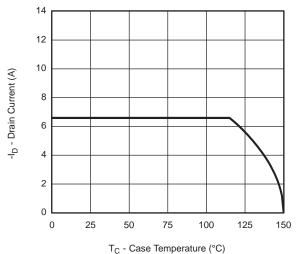
Single Pulse Power



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

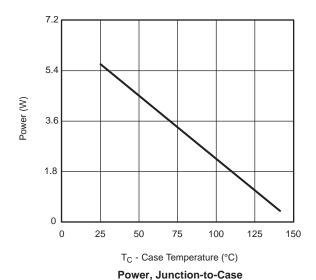
Safe Operating Area, Junction-to-Ambient

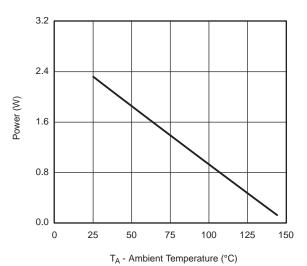
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



C - Case remperature (C

Current Derating*



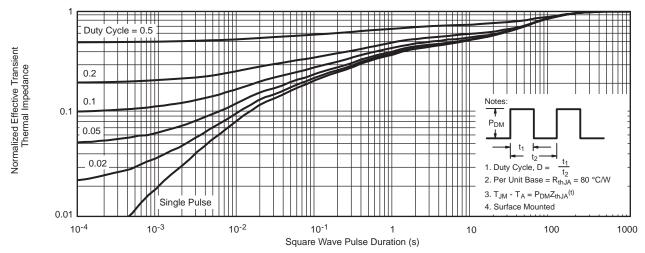


Power, Junction-to-Ambient

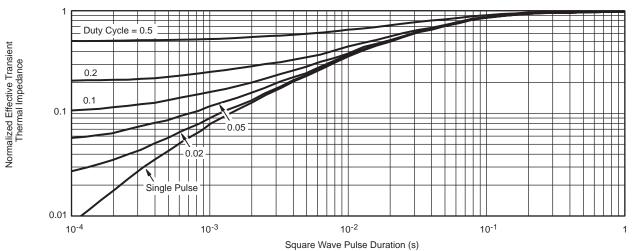
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



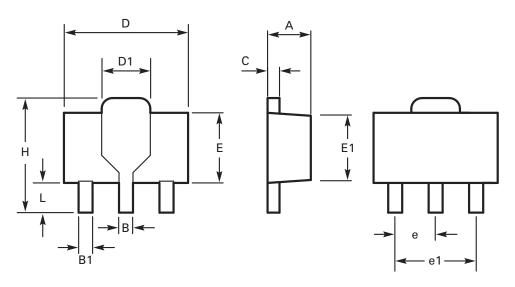
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



Package outline - SOT89



| DIM | Millin | neters | Inc | hes | DIM | Millimeters | | ters Inches | |
|-----|--------|--------|-------|-------|-----|-------------|------|-------------|-------|
| | Min | Max | Min | Max | | Min | Max | Min | Max |
| Α | 1.40 | 1.60 | 0.550 | 0.630 | Е | 2.29 | 2.60 | 0.090 | 0.102 |
| В | 0.44 | 0.56 | 0.017 | 0.022 | E1 | 2.13 | 2.29 | 0.084 | 0.090 |
| B1 | 0.36 | 0.48 | 0.014 | 0.019 | е | 1.50 BSC | | 0.059 BSC | |
| С | 0.35 | 0.44 | 0.014 | 0.017 | e1 | 3.00 BSC | | 0.118 BSC | |
| D | 4.40 | 4.60 | 0.173 | 0.181 | Н | 3.94 | 4.25 | 0.155 | 0.167 |
| D1 | 1.62 | 1.83 | 0.064 | 0.072 | L | 0.89 | 1.20 | 0.035 | 0.047 |

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches





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