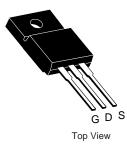
# P-Channel 60-V (D-S) MOSFET

PRODU	CT SUMMARY		
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
- 60	0.022 at V <sub>GS</sub> = - 10 V	- 55	147 nC
- 00	0.032 at $V_{GS}$ = - 4.5 V	- 40	147 110

#### TO-220 FULLPAK

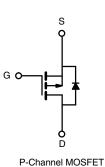


# FEATURES

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested

## **APPLICATIONS**

Load Switch



<b>ABSOLUTE MAXIMUM RATING</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise no	ted)	
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 60	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	v
	T <sub>C</sub> = 25 °C		- 55 <sup>a</sup>	
Continuous Drain Current (T <sub>1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 35	
Continuous Drain Current $(T_j = 150 \text{ C})$	T <sub>A</sub> = 25 °C	I <sub>D</sub>	10 <sup>b</sup>	A
	T <sub>A</sub> = 70 °C		- 6.2 <sup>b</sup>	
Pulsed Drain Current		I <sub>DM</sub>	- 220	
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	- 55	
Single Pulse Avalanche Energy	L = 0.1 min	E <sub>AS</sub>	255	mJ
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	L.	55 <sup>a</sup>	Α
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	۱ <sub>s</sub>	3.1 <sup>b</sup>	A
	T <sub>C</sub> = 25 °C		134 <sup>a</sup>	
	T <sub>C</sub> = 70 °C	D D	81 <sup>a</sup>	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	4.2 <sup>b</sup>	W
	T <sub>A</sub> = 70 °C		1.9 <sup>b</sup>	
Operating Junction and Storage Temperature Ra	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	R <sub>thJA</sub>	40	65	°C/W
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.38	0.62	0/11

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, $I_D$ = - 250 $\mu$ A	- 60			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	$\frac{\Delta V_{DS}/T_{J}}{I_{D}} = -250 \ \mu A$		38		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η - 200 μλ		- 5.2			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 1		- 3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current	1	$V_{DS} = -48 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = - 48 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 55			Α	
	P	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 30 A		0.022	0.03	- Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 20 A		0.032	0.042		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 50 A		17		S	
Dynamic <sup>b</sup>	•						
Input Capacitance	C <sub>iss</sub>			7750		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = 0 V, f = 1 MHz		480			
Reverse Transfer Capacitance	C <sub>rss</sub>			281			
	Qg	$V_{DS} = -48$ V, $V_{GS} = -10$ V, $I_{D} = -30$ A	147			1	
Total Gate Charge				37		nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 48 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 20 A		15			
Gate-Drain Charge	Q <sub>gd</sub>			21			
Gate Resistance	Rg	f = 1 MHz		4.4		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			73			
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 48 V, $R_L$ = 2 $\Omega$		158		- ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN}$ = - 10 V, $R_g$ = 1 $\Omega$		212			
Fall Time	t <sub>f</sub>			165			
Drain-Source Body Diode Characteristic	s				<u> </u>		
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			- 55	_	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			- 220	A		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 30 A		- 0.7	- 1.5	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			47		ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			57		nC	
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = - 50 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		27			
Reverse Recovery Rise Time	t <sub>b</sub>	—-		15		ns	

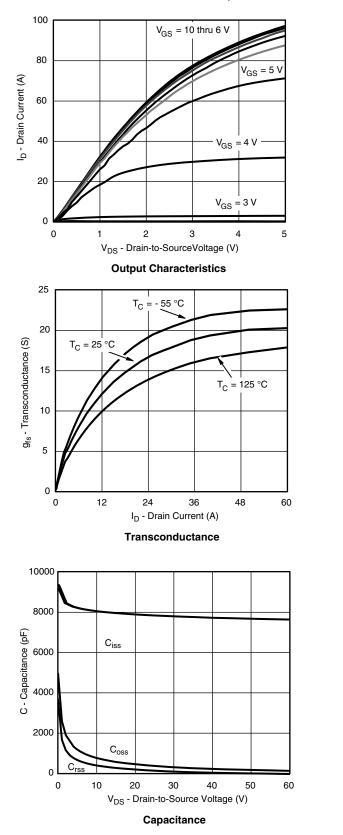
Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

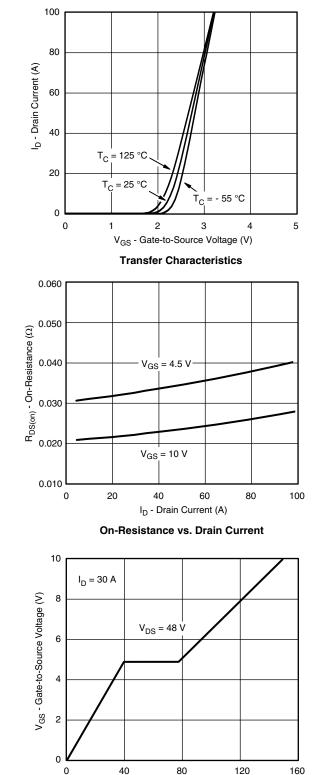
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





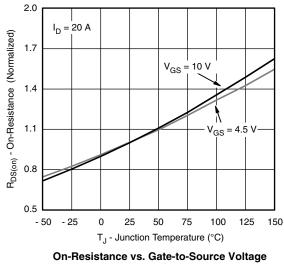
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

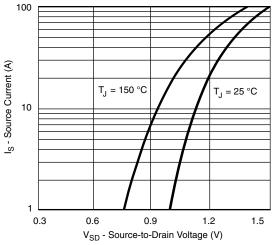


Q<sub>g</sub> - Total Gate Charge (nC) Gate Charge

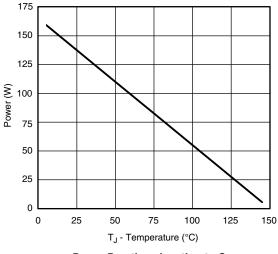


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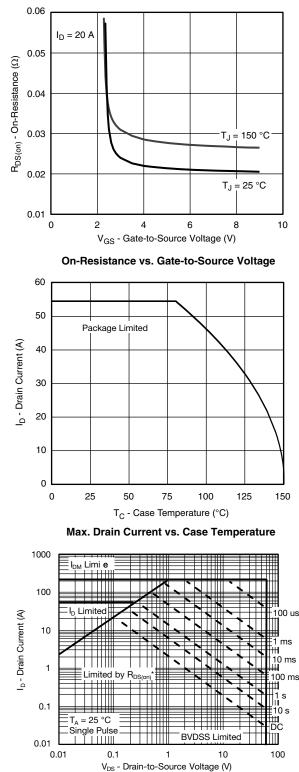




Source-Drain Diode Forward Voltage



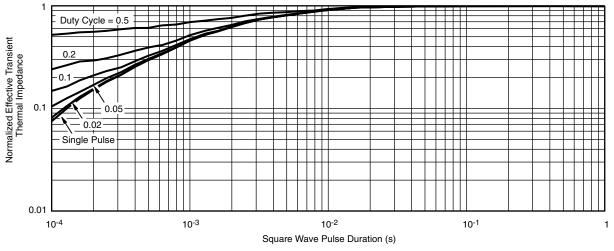
Power Derating, Junction-to-Case



Safe Operating Area, Junction-to-Ambient



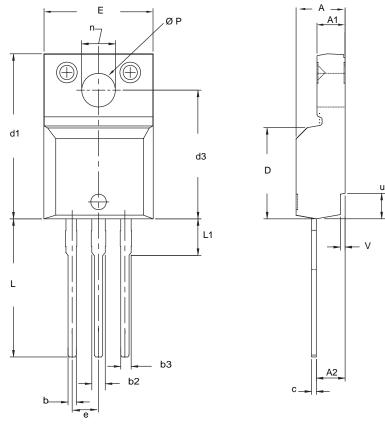
# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220 FULLPAK (HIGH VOLTAGE)**



DIM.	MILLIN	METERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
А	4.570	4.830	0.180	0.190	
A1	2.570	2.830	0.101	0.111	
A2	2.510	2.850	0.099	0.112	
b	0.622	0.890	0.024	0.035	
b2	1.229	1.400	0.048	0.055	
b3	1.229	1.400	0.048	0.055	
С	0.440	0.629	0.017	0.025	
D	8.650	9.800	0.341	0.386	
d1	15.88	16.120	0.622	0.635	
d3	12.300	12.920	0.484	0.509	
E	10.360	10.630	0.408	0.419	
е	2.54	BSC	0.100 BSC		
L	13.200	13.730	0.520	0.541	
L1	3.100	3.500	0.122	0.138	
n	6.050	6.150	0.238	0.242	
ØP	3.050	3.450	0.120	0.136	
u	2.400	2.500	0.094	0.098	
V	0.400	0.500	0.016	0.020	

Notes

1. To be used only for process drawing. 2. These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads. 3. All critical dimensions should C meet  $C_{pk} > 1.33$ . 4. All dimensions include burrs and plating thickness. 5. No chipping or package damage.



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