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P-Channel 20 V (D-S) MOSFET

| PRODUCT SUMMARY | | | | | | |
|---------------------|-------------------------------------|--------------------|-----------------------|--|--|--|
| V _{DS} (V) | $R_{DS(on)}$ (Ω) (Max.) | I _D (A) | Q _g (Typ.) | | | |
| - 20 | 0.0091 at V _{GS} = - 4.5 V | - 40 ^a | 58 nC | | | |
| 20 | 0.0115 at V _{GS} = - 2.5 V | - 30 ^a | 36 NC | | | |

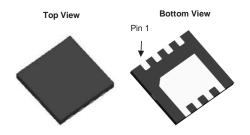
FEATURES

- DT-Trench Power MOSFET
- Thermally Enhanced DFN3X3 Package
 - Small Footprint Area
 - Low On-Resistance



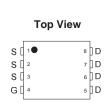
RoHS

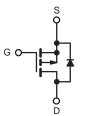
DFN 3x3 EP



APPLICATIONS

 Load Switch, PA Switch, and Battery Switch for Portable Devices





P-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted) | | | | | | |
|---|--|-----------------------------------|---|------|--|--|
| Parameter | | Symbol | Limit | Unit | | |
| Drain-Source Voltage | | V _{DS} | - 20 | V | | |
| Gate-Source Voltage | | V _{GS} ± 12 | | | | |
| | T _C = 25 °C | | - 40 ^a | | | |
| Continuous Drain Current (T _J = 150 °C) | $T_C = 70 ^{\circ}C$ $T_A = 25 ^{\circ}C$ | - I _D | - 30 ^a - 25 ^{b, c} | | | |
| | T _A = 70 °C | | - 18 ^{b, c} | Α | | |
| Pulsed Drain Current (t = 300 μs) | | I _{DM} | - 160 | | | |
| Continuous Source-Drain Diode Current | $T_C = 25 ^{\circ}\text{C}$ $T_A = 25 ^{\circ}\text{C}$ | I _S | - 40 ^a - 28 ^{b, c} | | | |
| M · · · · · · · · · · · · · · · · · · · | $T_C = 25 ^{\circ}\text{C}$ $T_C = 70 ^{\circ}\text{C}$ | D | 60 25 | 107 | | |
| Maximum Power Dissipation | $T_A = 25 ^{\circ}\text{C}$ $T_A = 70 ^{\circ}\text{C}$ | P _D - | 3 ^{b, c} | W | | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | °C | | |
| Soldering Recommendations (Peak Temperature) ^{d, e} | | , | 260 | | | |

| THERMAL RESISTANCE RATINGS | | | | | | |
|---|--------------|--------------------|---------|---------|--------|--|
| Parameter | | Symbol | Typical | Maximum | Unit | |
| Maximum Junction-to-Ambient ^{b, f} | t ≤ 5 s | R _{thJA} | 30 | 40 | °C/W | |
| Maximum Junction-to-Case (Drain) | Steady State | R _{th.IC} | 2.3 | 3.5 | C/ V V | |

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. See solder profile The DFN3X3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under steady state conditions is 80 °C/W.



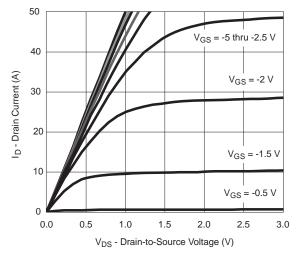
| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit | |
|--|-------------------------|--|-------|--------|-------|---------|--|
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V _{DS} | $V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$ | - 20 | | | V | |
| V _{DS} Temperature Coefficient | $\Delta V_{DS}/T_{J}$ | I _D = - 250 μA | | - 11 | | mV/°C | |
| V _{GS(th)} Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | 1 _D = - 230 μΑ | | 2.7 | | | |
| Gate-Source Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$ | - 0.5 | | - 1.5 | V | |
| Gate-Source Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$ | | | ± 100 | nA | |
| | | V _{DS} = - 12 V, V _{GS} = 0 V | | | - 1 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = - 12 V, V _{GS} = 0 V, T _J = 55 °C | | | - 10 | - 10 µA | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$ | - 40 | | | Α | |
| Davis Ossans Os Otata Basista sa | Ь | V _{GS} = - 4.5 V, I _D = - 5 A | | 0.0091 | 0.011 | Ω | |
| Drain-Source On-State Resistance ^a | R _{DS(on)} | $V_{GS} = -2.5 \text{ V}, I_D = -5 \text{ A}$ | | 0.0115 | 0.013 | | |
| Forward Transconductance ^a | g _{fs} | V _{DS} = - 12 V, I _D = - 10 A | | 50 | | S | |
| Dynamic ^b | <u>'</u> | | | ' | ' | | |
| Input Capacitance | C _{iss} | | | 10800 | | pF | |
| Output Capacitance | C _{oss} | V _{DS} = - 12 V, V _{GS} = 0 V, f = 1 MHz | | 3050 | | | |
| Reverse Transfer Capacitance | C _{rss} | | | 520 | | | |
| Total Gate Charge | Qg | V _{DS} = - 12 V, V _{GS} = - 8 V, I _D = - 10 A | | 58 | 97 | | |
| | | | | 33 | 65 | | |
| Gate-Source Charge | Q _{gs} | $V_{DS} = -12V$, $V_{GS} = -4.5 V$, $I_{D} = -10 A$ | | 7 | | nC | |
| Gate-Drain Charge | Q _{gd} | | | 15.5 | | | |
| Gate Resistance | R _g | f = 1 MHz | | 5 | | Ω | |
| Turn-On Delay Time | t _{d(on)} | | | 11 | | | |
| Rise Time | t _r | $V_{DS} = -12 \text{ V}, R_{L} = 0.75 \Omega$ | | 12 | | 1 | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong -8 \text{ A}, V_{GS} = -4.5 \text{ V}, R_g = 1 \Omega$ | | 72 | | | |
| Fall Time | t _f | | | 40 | | | |
| Turn-On Delay Time | t _{d(on)} | | | 21 | | ns | |
| Rise Time | t _r | $V_{DS} = -12V, R_{L} = 0.75 \Omega$ | | 40 | | | |
| Turn-Off Delay Time | t _{d(off)} | $I_D \cong -8 \text{ A}, V_{GS} = -2.5 \text{ V}, R_g = 1 \Omega$ | | 64 | | | |
| Fall Time | t _f | | | 40 | | 1 | |
| Drain-Source Body Diode Characterist | ics | | | • | | | |
| Continuous Source-Drain Diode Current | I _S | T _C = 25 °C | | | - 40 | А | |
| Pulse Diode Forward Current | I _{SM} | | | | 160 | | |
| Body Diode Voltage | V _{SD} | I _S = -8 A, V _{GS} = 0 V | | - 0.7 | - 1.2 | V | |
| Body Diode Reverse Recovery Time | t _{rr} | | | 41 | | ns | |
| Body Diode Reverse Recovery Charge Q _{rr} | | I _F = - 8 A, di/dt = 100 A/μs, T _{.I} = 25 °C | | 22 | | nC | |
| Reverse Recovery Fall Time | t _a | $_{1F} = -6 \text{ A}, \text{ u/u} = 100 \text{ A/}\mu\text{s}, \text{I}_{J} = 25 ^{3}\text{C}$ | | 14 | | ns | |
| Reverse Recovery Rise Time | t _b | | | 26 | | | |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

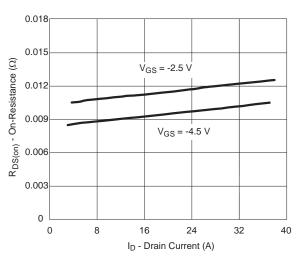
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



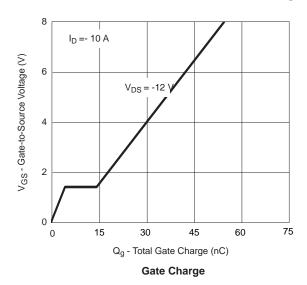
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

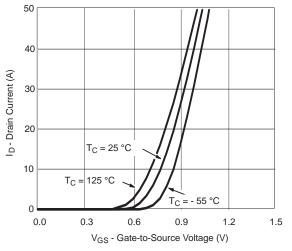


Output Characteristics

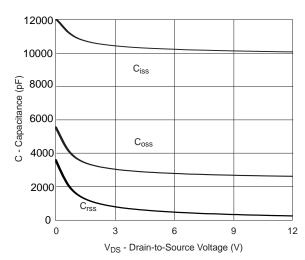


On-Resistance vs. Drain Current and Gate Voltage

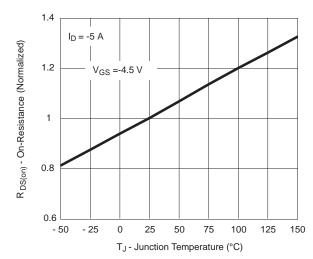




Transfer Characteristics



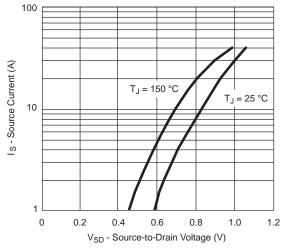
Capacitance



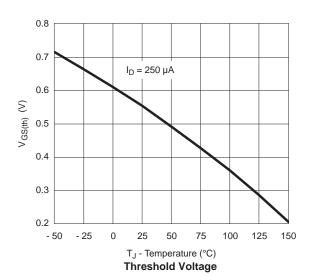
On-Resistance vs. Junction Temperature

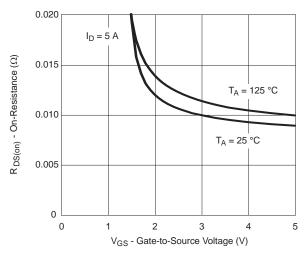


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

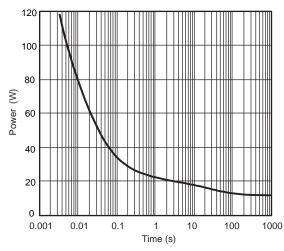


Soure-Drain Diode Forward Voltage

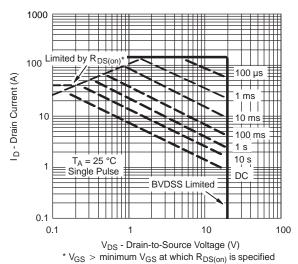




On-Resistance vs. Gate-to-Source Voltage



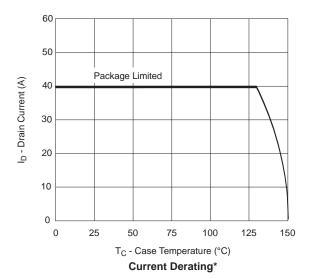
Single Pulse Power, Junction-to-Ambient

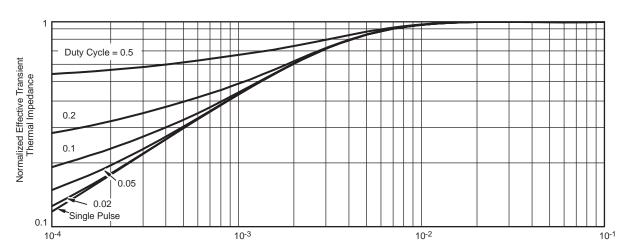


Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

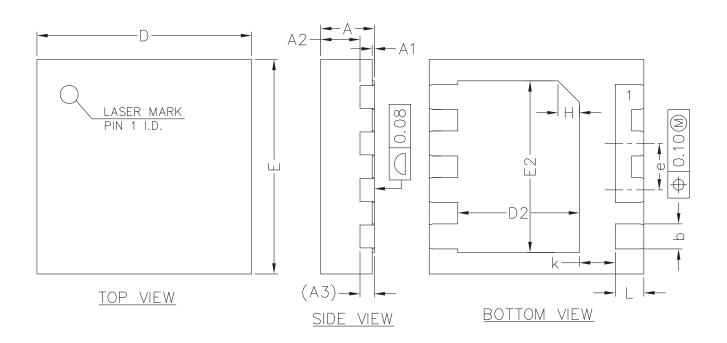




Normalized Thermal Transient Impedance, Junction-to-Case

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN | NOM | MAX | |
|--------|---------|------|------|--|
| А | 0.70 | 0.75 | 0.80 | |
| A1 | 0.00 | 0.02 | 0.05 | |
| A2 | 0.50 | 0.55 | 0.60 | |
| А3 | 0.20REF | | | |
| Ь | 0.30 | 0.35 | 0.40 | |
| D | 2.90 | 3.00 | 3.10 | |
| Е | 2.90 | 3.00 | 3.10 | |
| D2 | 1.60 | 1.70 | 1.80 | |
| E2 | 2.30 | 2.40 | 2.50 | |
| е | 0.55 | 0.65 | 0.75 | |
| K | 0.40 | 0.50 | 0.60 | |
| L | 0.35 | 0.40 | 0.45 | |



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