

# P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY							
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ ) Max.	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)				
- 30	0.0123 at V <sub>GS</sub> = - 10 V	- 45 <sup>d</sup>	40 nC				
- 30	0.0179 at V <sub>GS</sub> = -4.5 V	- 30 <sup>d</sup>	40 NC				

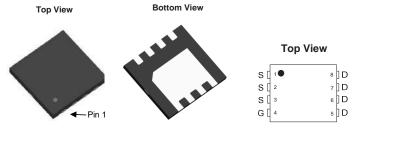
#### DFN 3x3 EP

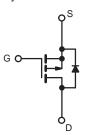
#### **FEATURES**

- DT-Trench Power MOSFET
- Low On-Resistance for Low Voltage Drop
- 100 % R<sub>g</sub> and UIS Tested

#### **APPLICATIONS**

- Battery, Load and Adaptor Switches
  - Notebook Computers
  - Notebook Battery Packs





P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		- 45 <sup>d</sup>	A	
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 35 <sup>d</sup>		
Continuous Diam Curient (1) = 130 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 18 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		- 9 <sup>a, b</sup>		
Pulsed Drain Current (t = 100 μs)	I <sub>DM</sub>	- 180	_ A		
Continuous Course Prais Diade Current	T <sub>C</sub> = 25 °C	I.	- 45 <sup>d</sup>		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 125 °C	l <sub>S</sub> –	- 3.8 <sup>a, b</sup>		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 25		
Single-Pulse Avalanche Energy	L = 0.1 mn	E <sub>AS</sub>	35	mJ	
	T <sub>C</sub> = 25 °C		43		
Manianum Danian Dissipation	T <sub>C</sub> = 70 °C		26	W	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	4.8 <sup>a, b</sup>		
	T <sub>A</sub> = 70 °C		3 <sup>a, b</sup>	1	
Operating Junction and Storage Temperature Range	•	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature) <sup>e, f</sup>		260	٦		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	25	30	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	2.5	3	C/VV	

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under steady state conditions is 70 °C/W.
- d. Package limited.
- e. The DFN3X3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- f. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.



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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0$ , $I_D = -250 \mu A$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	/T <sub>J</sub>		- 22		m\//oC	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		4.1		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	- 1		- 3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta na Duain Comunit	1	V <sub>DS</sub> = - 24 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 180			Α	
	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 15 A		0.0123	0.014	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 10 A		0.0179	0.02		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 15 A		60	0.02	S	
Dynamic <sup>b</sup>						<u> </u>	
Input Capacitance	C <sub>iss</sub>			3150			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 24 V, V <sub>GS</sub> = 0 V, f = 1 MHz		975		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	1		384		† .	
<u>·</u>	Q <sub>g</sub> _	V <sub>DS</sub> = - 24 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		92		+	
Total Gate Charge				41			
Gate-Source Charge		V <sub>DS</sub> = - 24 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 10 A		14		nC	
Gate-Drain Charge	Q <sub>gd</sub>	1		29		1	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.9	5.8	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			16			
Rise Time	t <sub>r</sub>	$V_{DS} = -15 \text{ V}, R_{L} = 1.5 \Omega$		14		1	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GS} = -10 \text{ V}, R_g = 1 \Omega$		60		1	
Fall Time	t <sub>f</sub>	1		12		1	
Turn-On Delay Time	t <sub>d(on)</sub>			60		ns	
Rise Time	t <sub>r</sub>	$V_{DS} = -15 \text{ V}, R_{L} = 1.5 \Omega$		59			
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -10 \text{ A}, V_{GS} = -4.5 \text{ V}, R_g = 1 \Omega$		50			
Fall Time	t <sub>f</sub>	1		22		1	
<b>Drain-Source Body Diode Characteris</b>	tics						
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 45	А	
Pulse Diode Forward Current (100 μs)	I <sub>SM</sub>				- 180		
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = -3 A, V <sub>GS</sub> = 0		- 0.7	- 1.20	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	]		22		ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 10 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		12		nC	
Reverse Recovery Fall Time	t <sub>a</sub>			9		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			13			

#### Notes:

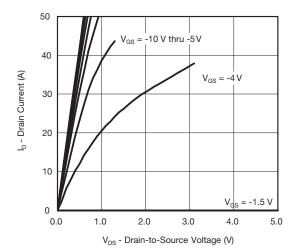
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

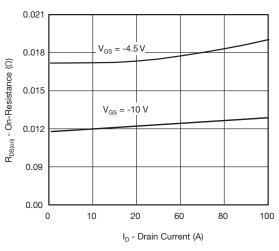
b. Guaranteed by design, not subject to production testing.



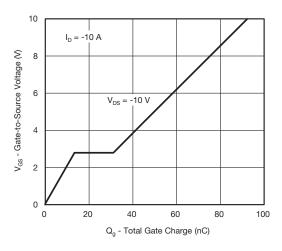
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



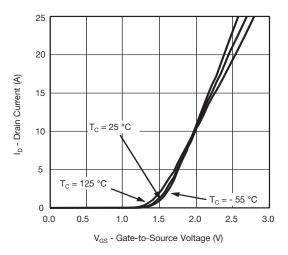
#### **Output Characteristics**



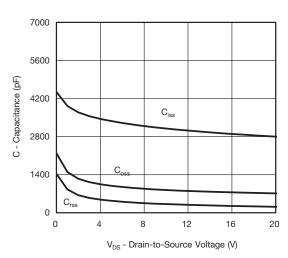
On-Resistance vs. Drain Current



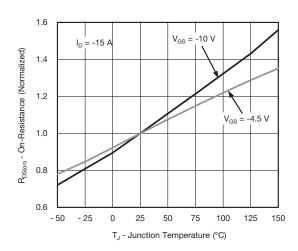
**Gate Charge** 



**Transfer Characteristics** 



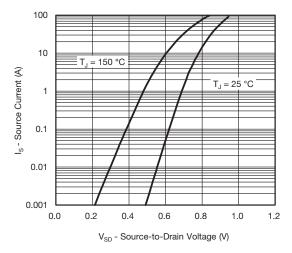
Capacitance



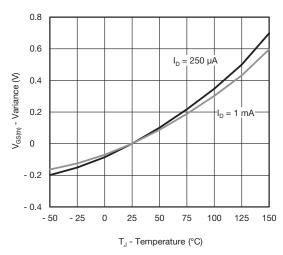
On-Resistance vs. Junction Temperature



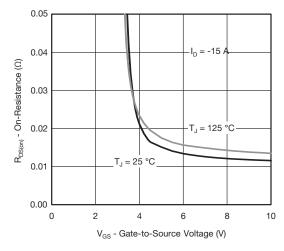
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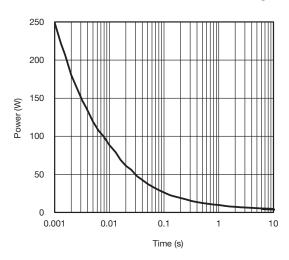
#### Source-Drain Diode Forward Voltage



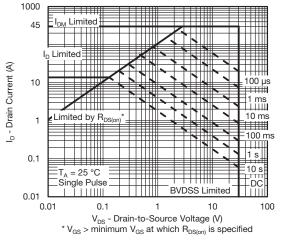
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



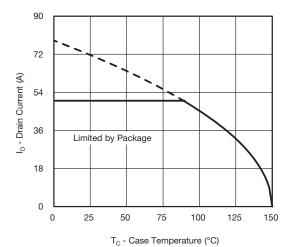
Single Pulse Power, Junction-to-Ambient



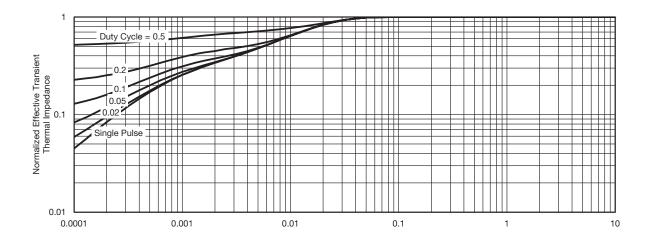
Safe Operating Area



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

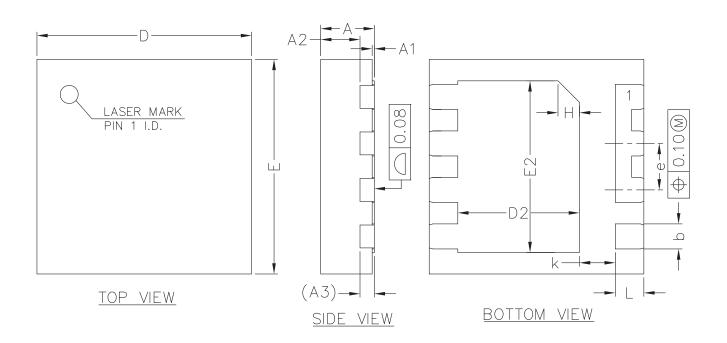


**Current Derating\*** 



 ${\bf Normalized\ Thermal\ Transient\ Impedance,\ Junction-to-Case}$ 







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
Α	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
А3		0.20REF	
Ь	0.30	0.35	0.40
D	2.90	3.00	3.10
Е	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
е	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45



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