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# P-Channel &0-V (D-S) MOSFET

PRODUCT SUMMARY	
V <sub>DS</sub> (V)	- 20
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.058
$R_{DS(on)}(\Omega)$ at $V_{GS} = -2.5 \text{ V}$	0.082
$R_{DS(on)}(\Omega)$ at $V_{GS} = -1.8 \text{ V}$	0.117
I <sub>D</sub> (A)	- 5
Configuration	Single

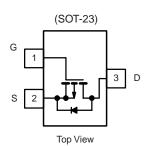
#### **FEATURES**

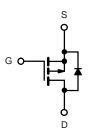
- DT-Trench Power MOSFET 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

• Load Switch for Portable Devices





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	(T <sub>C</sub> = 25 °C, unles	s otherwise noted	)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		$V_{DS}$	- 20	V
Gate-Source Voltage		$V_{GS}$	± 8	V
Continuous Drain Current	T <sub>C</sub> = 25 °C	1	- 5	
Continuous Drain Current	T <sub>C</sub> = 125 °C	I <sub>D</sub>	- 3	
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	- 2.5	Α
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	- 20	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 11	
Single Pulse Avalanche Energy	L = 0.1 MH	E <sub>AS</sub>	6	mJ
Maximum Power Dissipationa	T <sub>C</sub> = 25 °C	Pn	2	W
Maximum Fower Dissipations	T <sub>C</sub> = 125 °C	r <sub>D</sub>	0.67	VV
Operating Junction and Storage Temperature I	Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>b</sup>	$R_{thJA}$	175	°C/W
Junction-to-Foot (Drain)		$R_{thJF}$	75	C/VV



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PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static					L	l	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		- 20	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 0.45	-	- 2	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =	= 0 V, V <sub>GS</sub> = ± 8 V	-	-	± 100	nA
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	V <sub>DS</sub> = - 12 V	ı	-	- 1	
	I <sub>DSS</sub>	$V_{GS} = 0 V$	V <sub>DS</sub> = - 12 V, T <sub>J</sub> = 125 °C	ı	-	- 50	μΑ
		$V_{GS} = 0 V$	V <sub>DS</sub> = - 12 V, T <sub>J</sub> = 175 °C	ı	-	- 150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = - 4.5 V	$V_{DS} \le -5 V$	- 10	-	-	Α
		$V_{GS} = -4.5 \text{ V}$	I <sub>D</sub> = - 3.5 A	ı	0.058	0.064	Ω
		$V_{GS} = -4.5 \text{ V}$	I <sub>D</sub> = - 3.5 A, T <sub>J</sub> = 125 °C	ı	0.066	0.071	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}$	I <sub>D</sub> = - 3.5 A, T <sub>J</sub> = 175 °C	ı	0.075	0.079	
		$V_{GS} = -2.5 \text{ V}$	I <sub>D</sub> = - 3 A	ı	0.082	0.091	
		V <sub>GS</sub> = - 1.8 V	I <sub>D</sub> = - 2 A	ı	0.117	0.129	
Forward Transconductance <sup>b</sup>	9fs	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 1.6 A		ı	7	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			ı	695	-	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	$V V_{DS} = -6 V, f = 1 MHz$	ı	265	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>			ı	190	-	
Total Gate Charge <sup>c</sup>	Qg			ı	8.4		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	V <sub>GS</sub> = - 4.5 V	$V_{DS} = -6 \text{ V}, I_{D} = -3.85 \text{ A}$	ı	1	-	nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			ı	2.4	-	
Gate Resistance	$R_{g}$	f = 1 MHz		4.1	8.2	12.3	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			-	17	-	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -6 \text{ V}, R_L = 1.6 \Omega$ $I_D \cong -3.85 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		-	19	-	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			1	28	-	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	13	_	
Source-Drain Diode Ratings and Chara	acteristics <sup>b</sup>						
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	- 20	Α
Forward Voltage	$V_{SD}$	I <sub>F</sub> =	- 2 A, V <sub>GS</sub> = 0 V	-	- 0.8	- 1.2	V

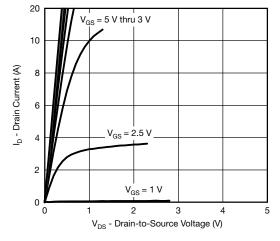
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

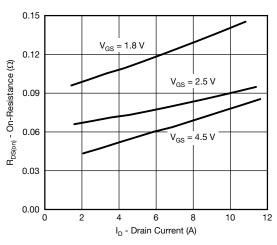
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



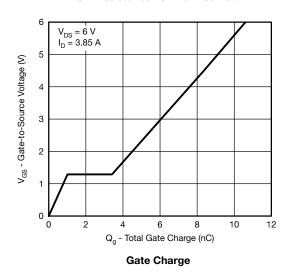
# **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### **Output Characteristics**



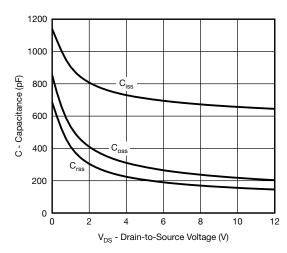
On-Resistance vs. Drain Current



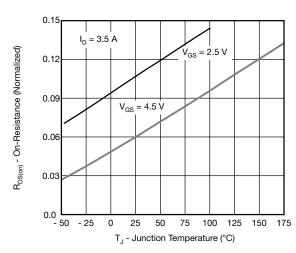
T<sub>C</sub> = 25 °C

T<sub>C</sub> = 125 °C

**Transfer Characteristics** 



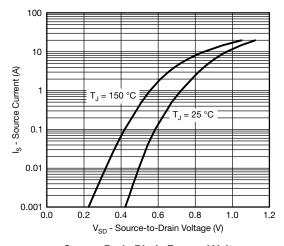
#### Capacitance



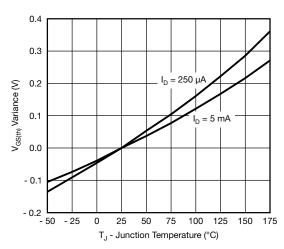
On-Resistance vs. Junction Temperature



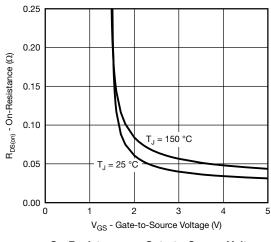
# **TYPICAL CHARACTERISTICS** ( $T_A = 25 \, ^{\circ}\text{C}$ , unless otherwise noted)



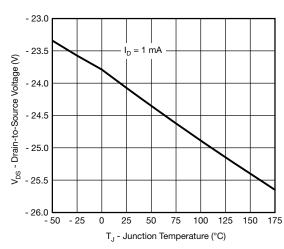
#### Source-Drain Diode Forward Voltage



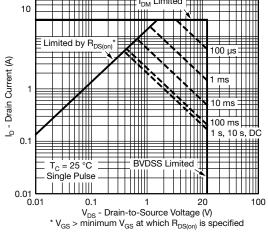
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



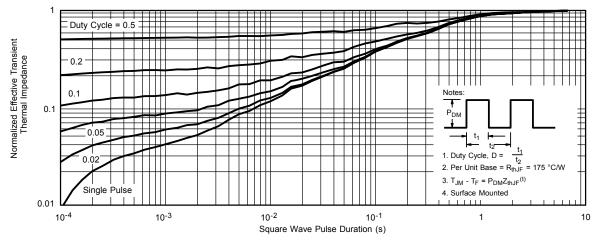
Drain Source Breakdown vs. Junction Temperature



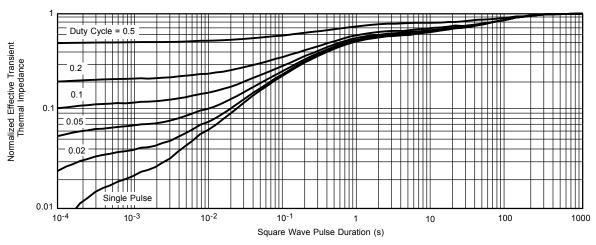
Safe Operating Area

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## **THERMAL RATINGS** ( $T_A = 25 \, ^{\circ}\text{C}$ , unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Foot



Normalized Thermal Transient Impedance, Junction-to-Ambient

#### Note

- · The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 C)

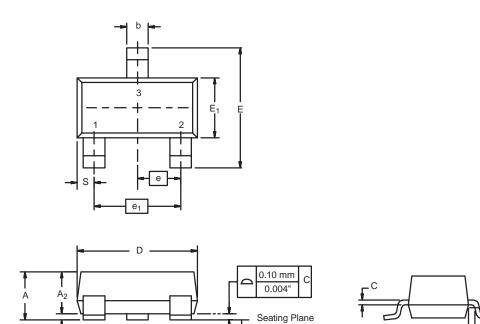
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

0.25 mm Gauge Plane

Seating Plane



### **SOT-23: 3-LEAD**



C

Dim -	MILLIM	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.037	4 Ref	
e <sub>1</sub>	1.90	1.90 BSC		8 Ref	
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025	Ref	
S	0.50 Ref		0.020	) Ref	
q	3°	8°	3°	8°	

DWG: 5479

#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)





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