

## P-Channel 60 V (D-S) MOSFET

### PRODUCT SUMMARY

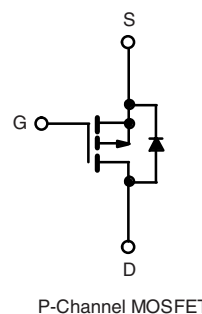
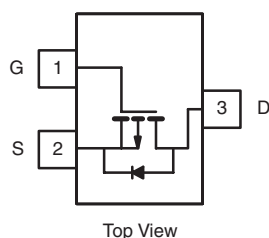
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>a</sup>	$Q_g$ (Typ.)
- 60	0.135 at $V_{GS} = - 10$ V	- 3.9	18 nC
	0.165 at $V_{GS} = - 4.5$ V	- 3.0	

### FEATURES

- DT-Trench Power MOSFET
- 100 %  $R_g$  Tested

### APPLICATIONS

- Load Switch
- DC/DC Converter


**RoHS**  
 COMPLIANT


### ABSOLUTE MAXIMUM RATINGS $T_A = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	- 60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150\text{ }^{\circ}\text{C}$ )	$T_C = 25\text{ }^{\circ}\text{C}$	- 3.9	A
	$T_C = 70\text{ }^{\circ}\text{C}$	- 2.8	
	$T_A = 25\text{ }^{\circ}\text{C}$	- 2.6 <sup>b, c</sup>	
	$T_A = 70\text{ }^{\circ}\text{C}$	- 2.0 <sup>b, c</sup>	
Pulsed Drain Current	$I_{DM}$	- 15	A
Continuous Source-Drain Diode Current	$T_C = 25\text{ }^{\circ}\text{C}$	- 3.9	
	$T_A = 25\text{ }^{\circ}\text{C}$	- 1.6 <sup>b, c</sup>	
Maximum Power Dissipation	$T_C = 25\text{ }^{\circ}\text{C}$	8	W
	$T_C = 70\text{ }^{\circ}\text{C}$	4.7	
	$T_A = 25\text{ }^{\circ}\text{C}$	1.75 <sup>b, c</sup>	
	$T_A = 70\text{ }^{\circ}\text{C}$	1.20 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	$^{\circ}\text{C}$

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \leq 5$ s	$R_{thJA}$	80	$^{\circ}\text{C/W}$
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	45	
			55	

Notes:

 a. Based on  $T_C = 25\text{ }^{\circ}\text{C}$ .

b. Surface mounted on 1" x 1" FR4 board.

 c.  $t = 5$  s.

 d. Maximum under steady state conditions is 166  $^{\circ}\text{C/W}$ .

SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 60			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = - 250 μA		- 40		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			4.8		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 1.2		- 2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = 0 V			- 1	μA
		V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ - 5 V, V <sub>GS</sub> = - 10 V	- 3.9			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 2.0 A		135	160	mΩ
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.0 A		165	200	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 48 V, I <sub>D</sub> = - 2.0 A		10		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = 0 V, f = 1 MHz		705		pF
Output Capacitance	C <sub>oss</sub>			106		
Reverse Transfer Capacitance	C <sub>rss</sub>			68		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 2.0 A		18		nC
		V <sub>DS</sub> = - 48 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.0 A		10		
Gate-Source Charge	Q <sub>gs</sub>			2.5		
Gate-Drain Charge	Q <sub>gd</sub>			3.2		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		4.3		Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 48 V, R <sub>L</sub> = 8 Ω I <sub>D</sub> ≡ - 1.0 A, V <sub>GEN</sub> = - 4.5 V, R <sub>g</sub> = 1 Ω		18		ns
Rise Time	t <sub>r</sub>			10		
Turn-Off Delay Time	t <sub>d(off)</sub>			41		
Fall Time	t <sub>f</sub>			13		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 48 V, R <sub>L</sub> = 8 Ω I <sub>D</sub> ≡ - 2.0 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		10		
Rise Time	t <sub>r</sub>			6		
Turn-Off Delay Time	t <sub>d(off)</sub>			33		
Fall Time	t <sub>f</sub>			9		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 3.9	A
Pulse Diode Forward Current	I <sub>SM</sub>				- 15	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 2.0 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 2.0 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C		26		ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			33		nC
Reverse Recovery Fall Time	t <sub>a</sub>			10		ns
Reverse Recovery Rise Time	t <sub>b</sub>			8		

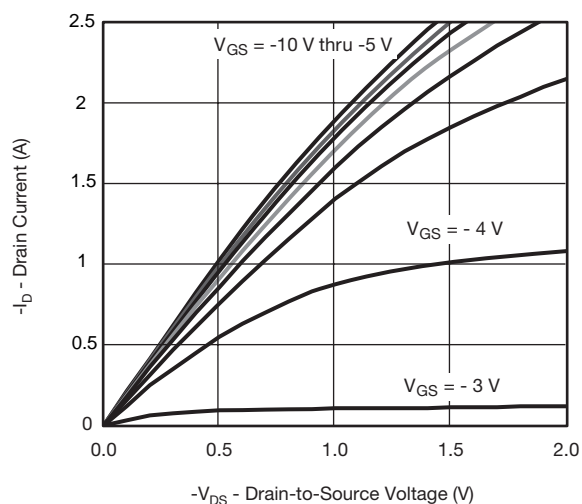
Notes:

 a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

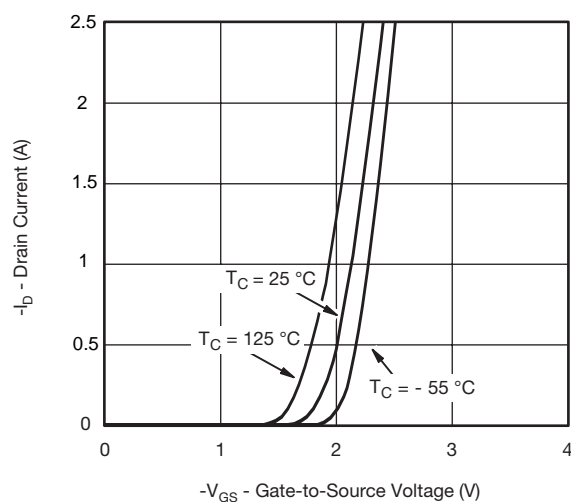
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

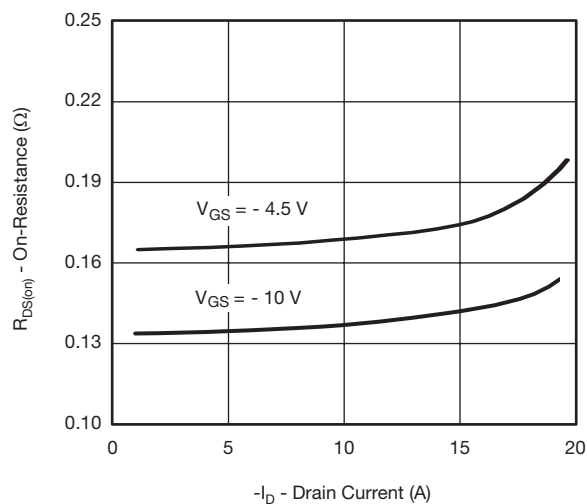
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



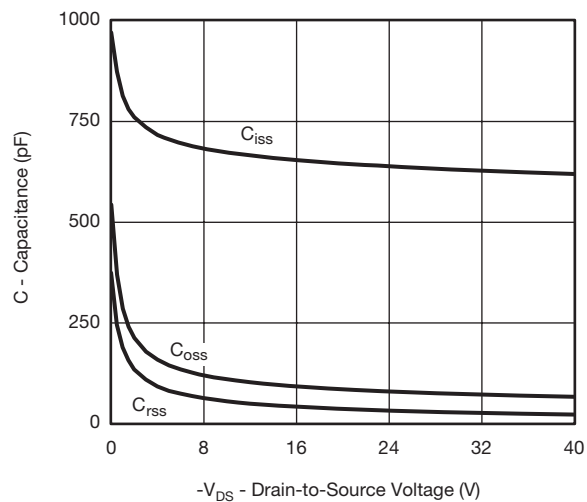
**Output Characteristics**



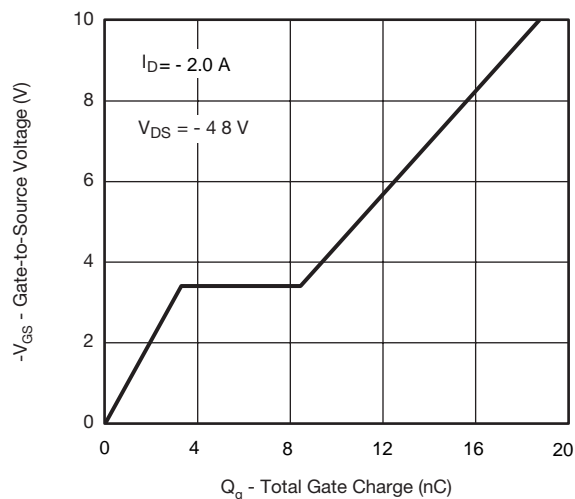
**Transfer Characteristics**



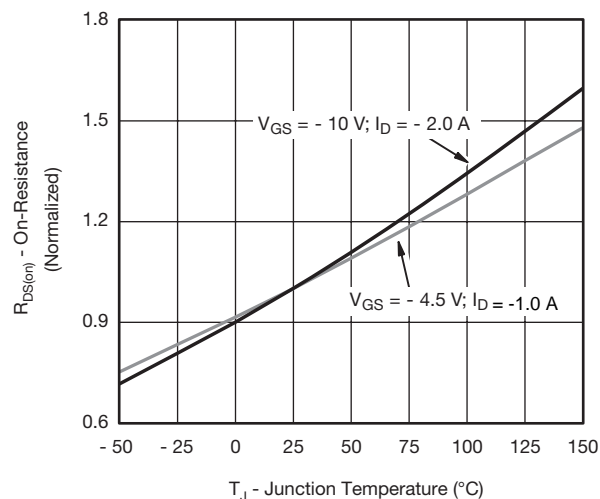
**On-Resistance vs. Drain Current**



**Capacitance**

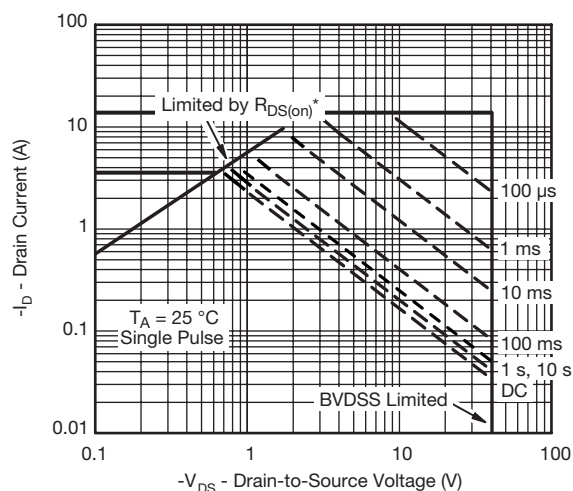
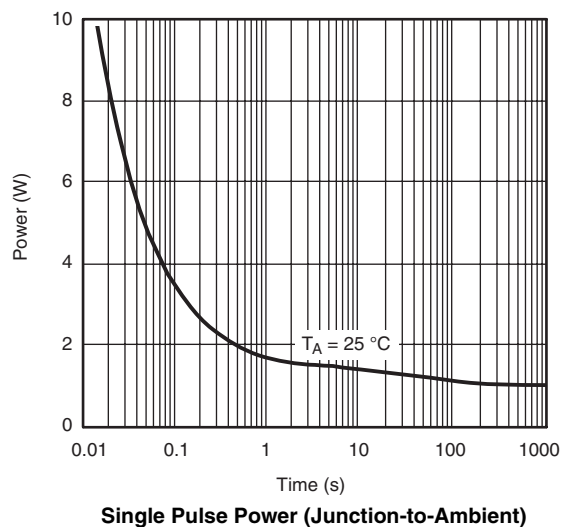
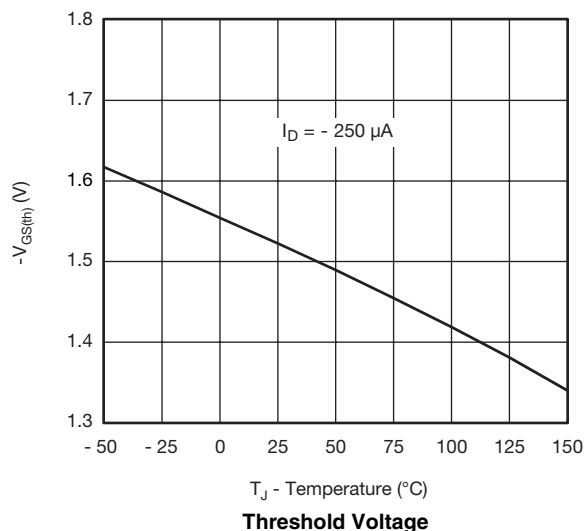
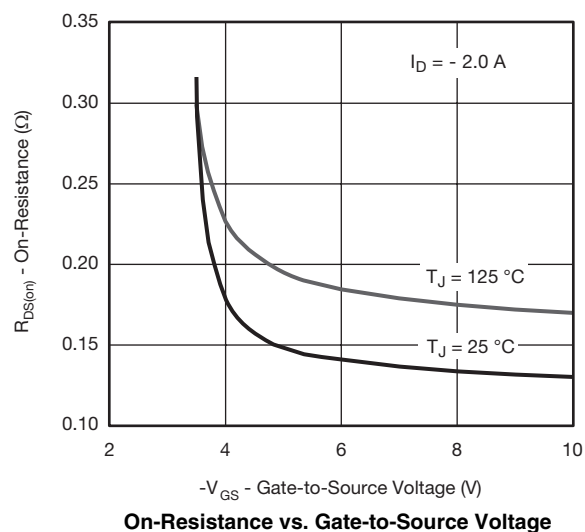
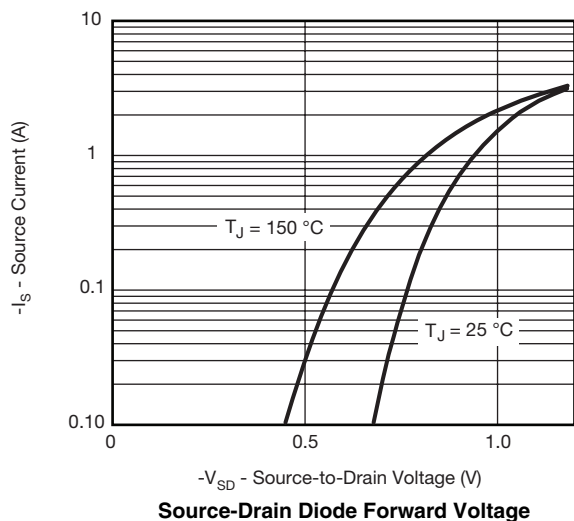


**Gate Charge**



**On-Resistance vs. Junction Temperature**

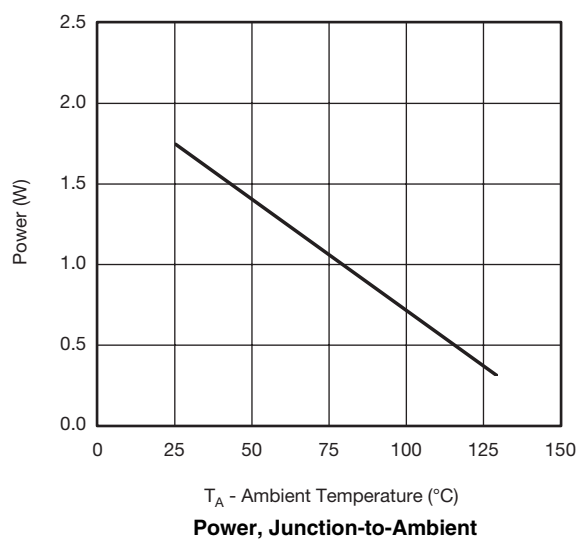
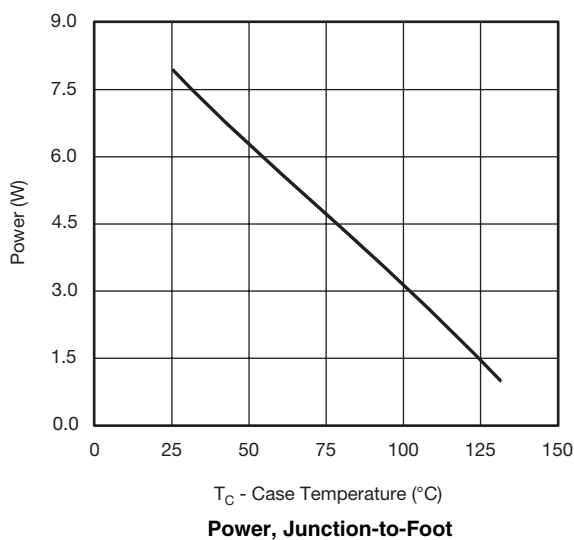
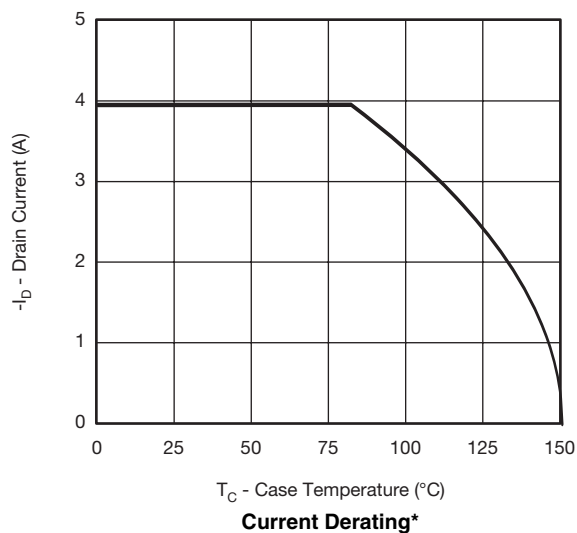
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



\*  $-V_{GS} >$  minimum  $-V_{GS}$  at which  $R_{DS(on)}$  is specified

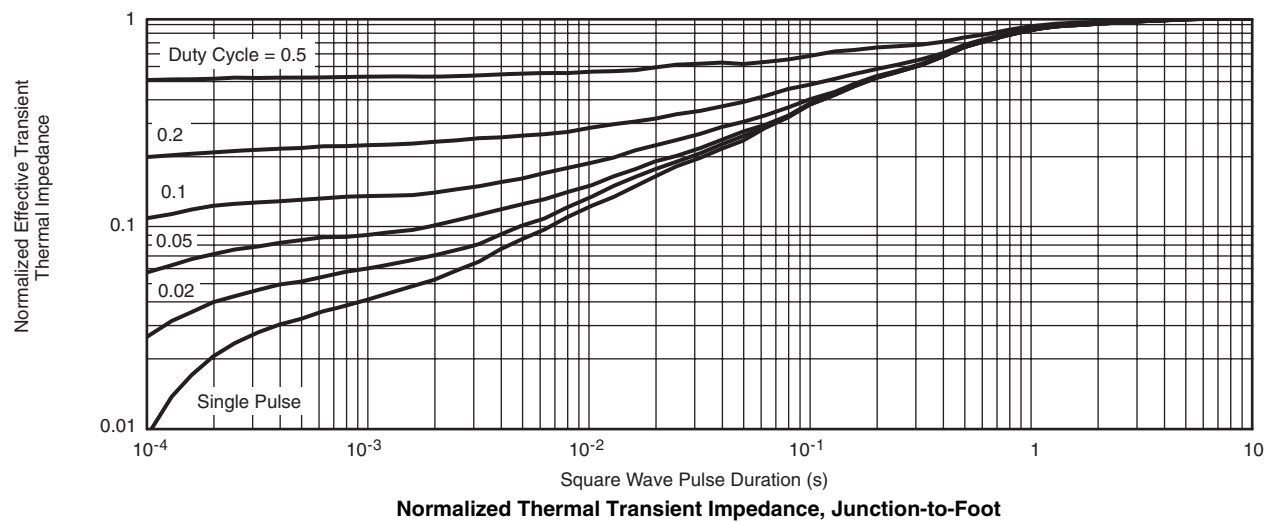
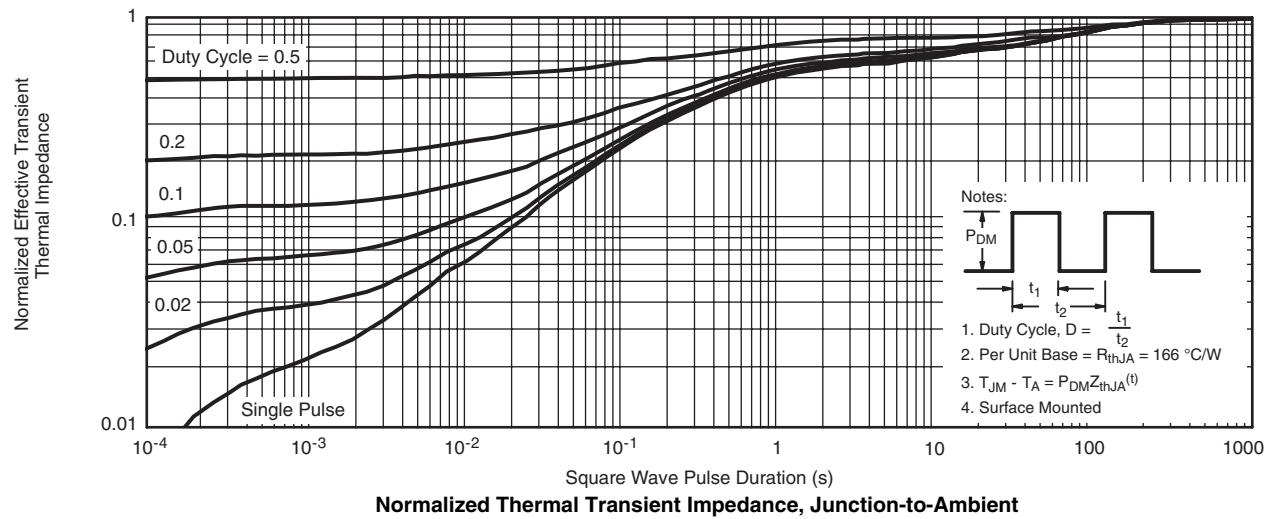
**Safe Operating Area, Junction-to-Ambient**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

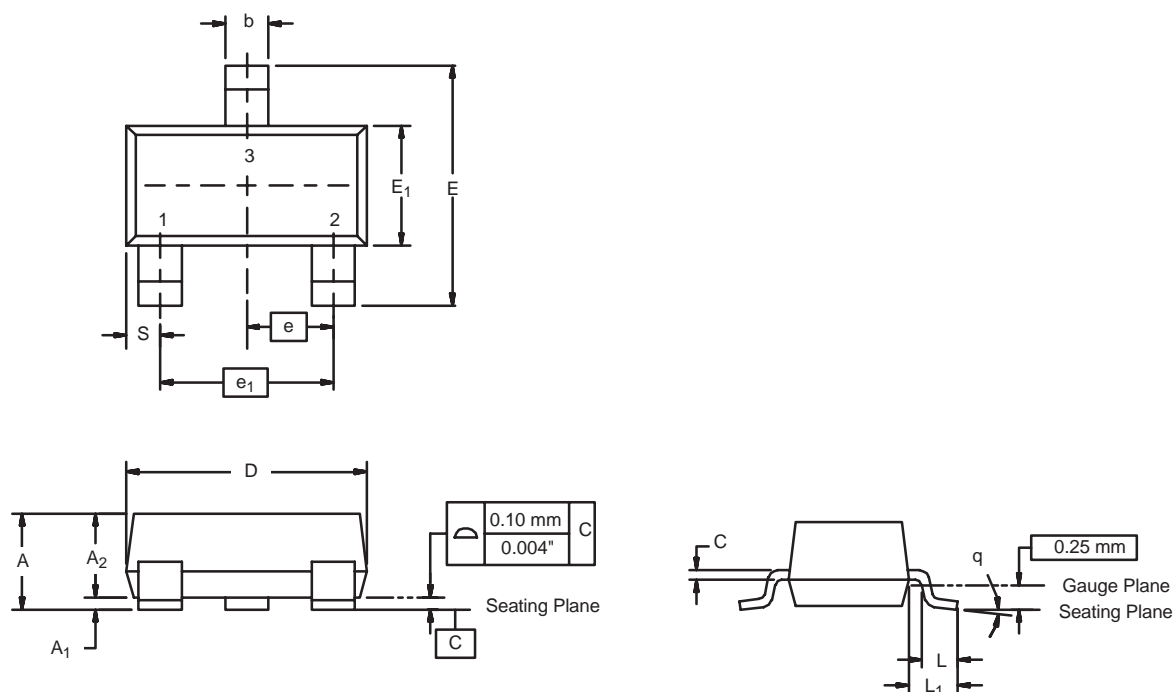


\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

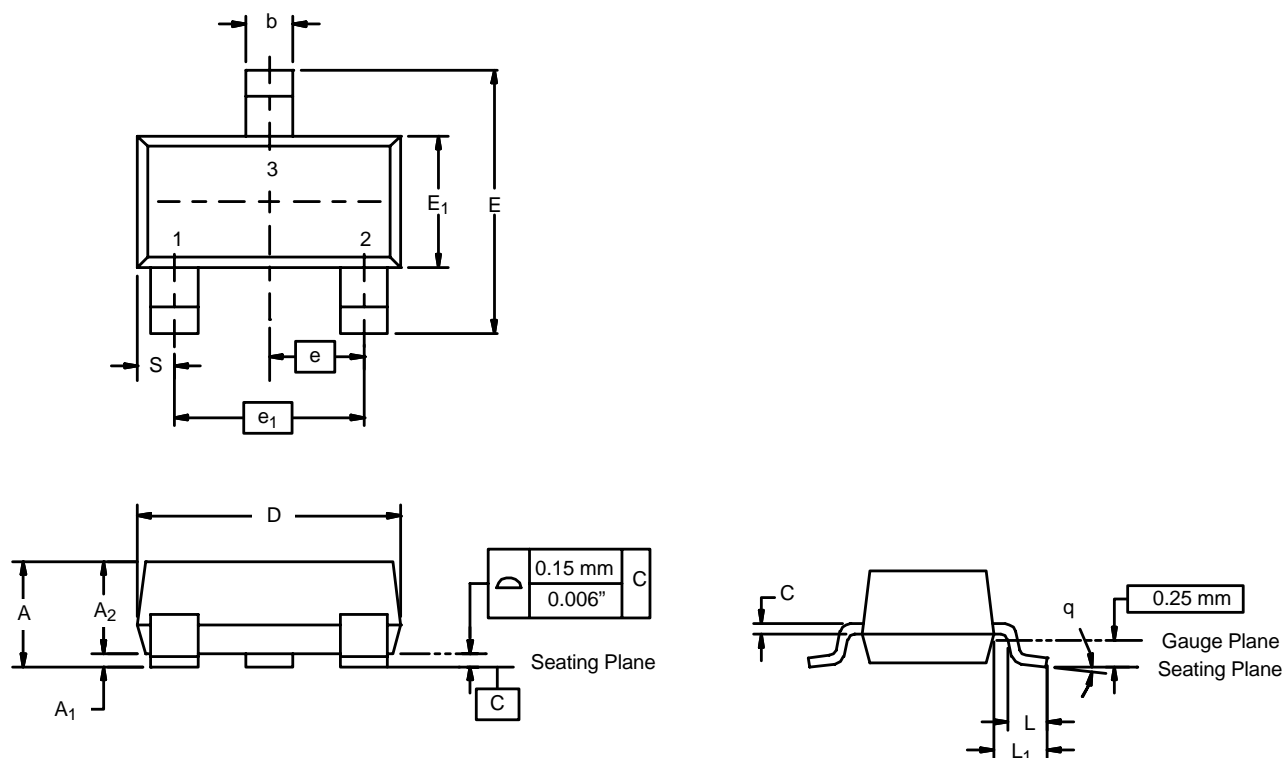


**SOT-23 (TO-236): 3-LEAD**



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A <sub>1</sub>	0.01	0.10	0.0004	0.004
A <sub>2</sub>	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E <sub>1</sub>	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e <sub>1</sub>	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L <sub>1</sub>	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°
ECN: S-03946-Rev. K, 09-Jul-01 DWG: 5479				

**SOT-23-3L (TO-236-3L): 3-LEAD**

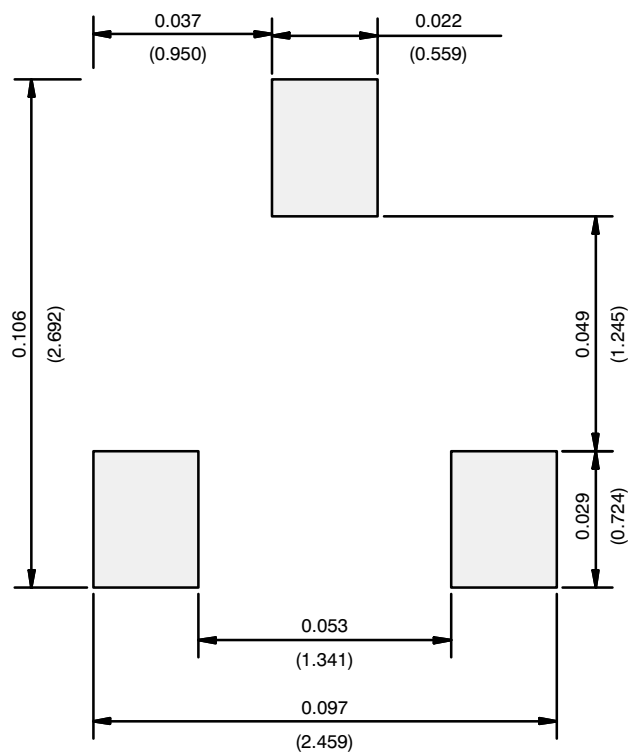


Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
<b>A</b>	—	1.45	—	0.057
<b>A<sub>1</sub></b>	0	0.15	0	0.006
<b>A<sub>2</sub></b>	0.90	1.30	0.035	0.052
<b>b</b>	0.30	0.50	0.012	0.020
<b>c</b>	0.08	0.25	0.003	0.010
<b>D</b>	2.70	3.10	0.100	0.122
<b>E</b>	2.40	3.00	0.090	0.120
<b>E<sub>1</sub></b>	1.40	1.80	0.055	0.071
<b>e</b>	0.95 TYP		0.037 TYP	
<b>e<sub>1</sub></b>	1.90 TYP		0.075 TYP	
<b>L</b>	0.35	0.55	0.013	0.022
<b>L<sub>1</sub></b>	0.64 TYP		0.025 TYP	
<b>S</b>	0.50 TYP		0.020 TYP	
<b>q</b>	0°	10°	0°	10°
ECN: S-31068—Rev. J, 26-May-13 DWG: 5901				

NOTE: Dimensions are in mm converted to inches.



**RECOMMENDED MINIMUM PADS FOR SOT-23 & SOT-23-3L**



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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