

P-Channel 30-V (D-S) MOSFET

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PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
- 30	0.019 at V _{GS} = - 10 V	- 7.5	20 nC			
	$0.029 \text{ at V}_{GS} = -4.5 \text{ V}$	- 6.3	20110			

FEATURES

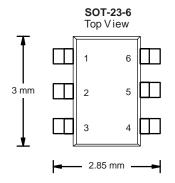
- DT-Trench Power MOSFET
- 100 % $\rm R_{\rm g}$ and UIS tested

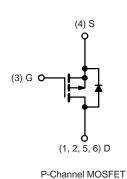
APPLICATIONS

· Load Switch



COMPLIANT





ABSOLUTE MAXIMUM RATIN Parameter	Α .	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 30		
Gate-Source Voltage		V _{GS}	± 20	V	
-	T _C = 25 °C		- 7.5		
Continuous Drain Current (T _{.1} = 150 °C)	$T_C = 70 ^{\circ}C$	I _D	- 6.3		
Communication (19 100 0)	T _A = 25 °C		- 4.1 ^{b, c}		
	T _A = 70 °C		- 3.3 ^{b, c}	Α	
Pulsed Drain Current		I _{DM}	- 30		
	T _C = 25 °C		- 7.5		
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	- 1.87 ^{b, c}		
	T _C = 25 °C		3.5		
Maximum Dawar Dissination	T _C = 70 °C	P _D	2.2	w	
Maximum Power Dissipation	T _A = 25 °C		1.5 ^{b, c}	¬	
	T _A = 70 °C]	0.9 ^{b, c}	7	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R_{thJA}	45	55	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	34	41	5/ ۷ ۷		

Notes:

- a. Based on T_C = 25 °C.
 b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under Steady State conditions is 110 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 31		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	10 = - 230 μΛ		4.5		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zoro Coto Voltago Drain Current		V _{DS} = - 30 V, V _{GS} = 0 V			- 1	μA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 7.5			Α
	D	$V_{GS} = -10 \text{ V}, I_D = -3.0 \text{ A}$		0.019	0.027	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -2.0 \text{ A}$		0.029	0.039	
Dynamic ^b				•		
Input Capacitance	C _{iss}			1450		
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		180		pF
Reverse Transfer Capacitance	C _{rss}			153		1
Total Gate Charge	Q_g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 3.0 A		20		nC
Gate-Source Charge	Q _{gs}			4		
Gate-Drain Charge	Q_{gd}			3.5		1
Gate Resistance	R_g	f = 1 MHz		7		Ω
Turn-On Delay Time	t _{d(on)}			7		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 4.6 Ω		23		ns
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 3.0 A, V_{GS} = - 10 V, R_g = 1 Ω		40		
Fall Time	t _f			20		
Drain-Source Body Diode Characteristic	s			•		
ontinuous Source-Drain Diode Current I _S		T _C = 25 °C			- 7.5	^
Pulse Diode Forward Current ^a	I _{SM}				- 30	A
Body Diode Voltage	V _{SD}	I _S = - 3.3 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}			15		ns
Body Diode Reverse Recovery Charge	Q _{rr}	$I_F = -3.0 \text{ A, di/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 \text{ °C}$		4.7		nC

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

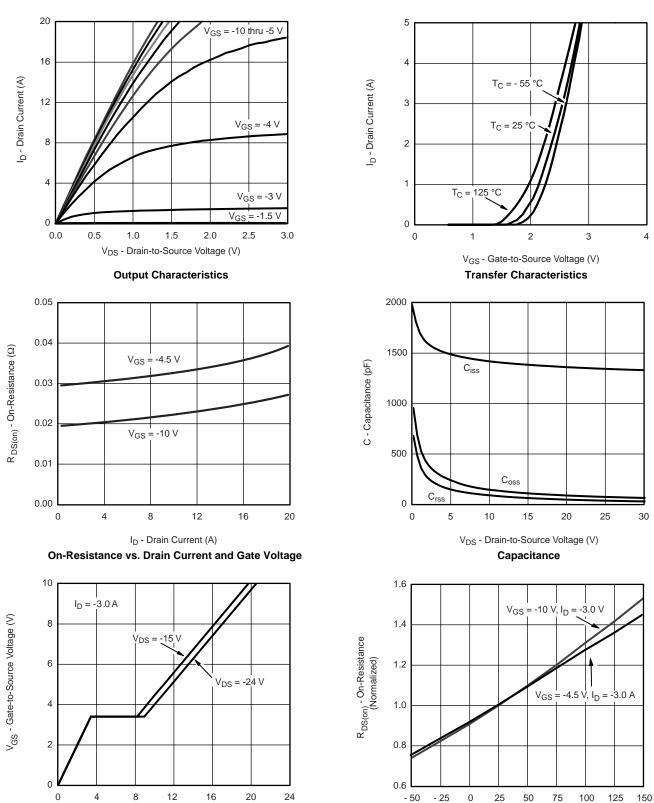
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

Q_q - Total Gate Charge (nC)

Gate Charge

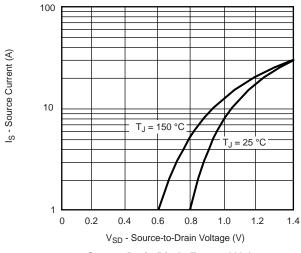


T_J - Junction Temperature (°C)

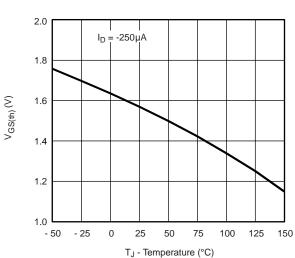
On-Resistance vs. Junction Temperature



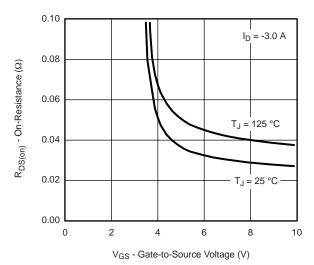
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



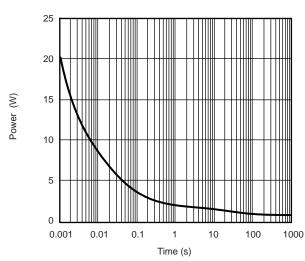
Source-Drain Diode Forward Voltage



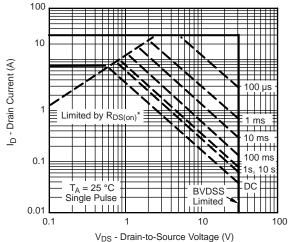
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power

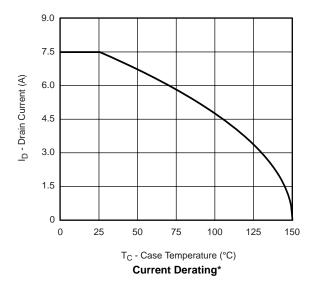


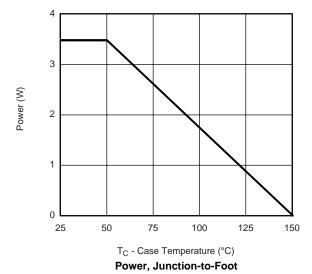
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

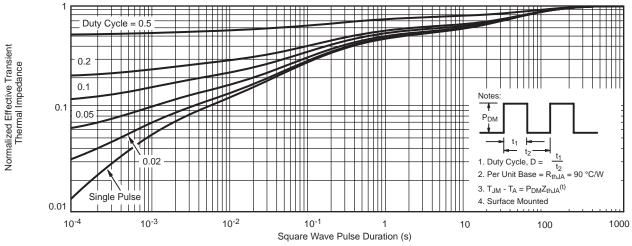




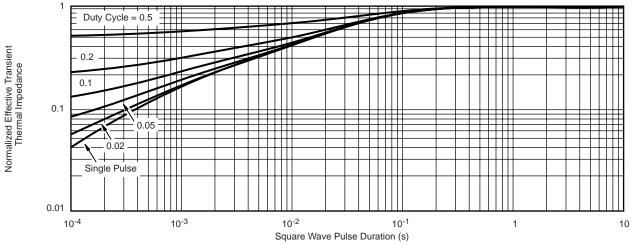
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



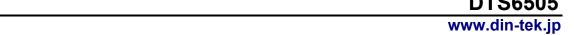
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



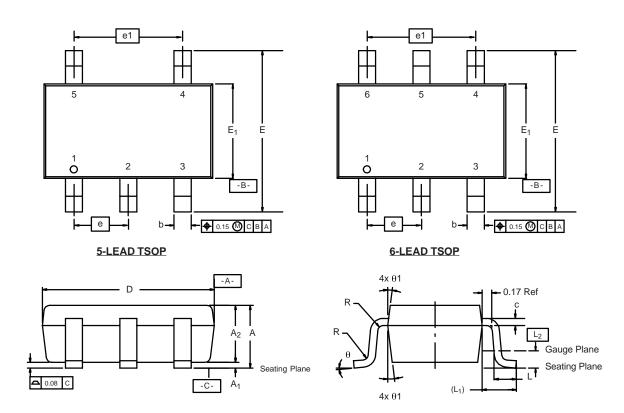
Normalized Thermal Transient Impedance, Junction-to-Foot



TSOP: 5/6-LEAD

Din-Tek SEMICONDUCTOR

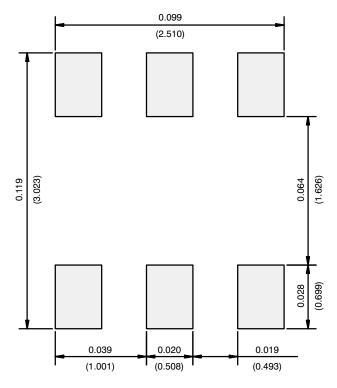
JEDEC Part Number: MO-193C



	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е	0.95 BSC			0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							



RECOMMENDED MINIMUM PADS FOR TSOP-6



Recommended Minimum Pads Dimensions in Inches/(mm)

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