

DTS6672 www.din-tek.jp

N- and P-Channel 60 V (D-S) MOSFET

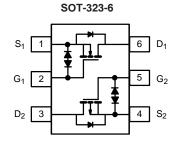
PRODUCT SUMMARY						
	V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
N-Channel	60	1.6 at V _{GS} = 10 V	0.45			
		1.8 at V _{GS} = 4.5 V	0.4			
P-Channel	- 60	4.5 at V _{GS} = - 10 V	- 0.25			
		6.3 at V _{GS} = - 4.5 V	- 0.2			

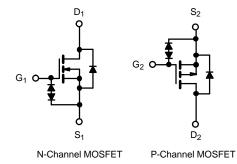
FEATURES

- DT-Trench Power MOSFET
- Very Small Footprint
- 100 % Rg and UIS Tested
- Gate-Source ESD Protected

APPLICATIONS

- LED Inverter Circuits
- DC/DC Conversion Circuits
- Motor drives





ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter		Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage		V _{DS}	60	- 60	v	
Gate-Source Voltage		V _{GS}	± 20		v	
Continuous Drain Current $(T_J = 150 \text{ °C})^a$	T _A = 25 °C	I _D	0.45	- 0.25		
	T _A = 85 °C		0.36	- 0.19		
Pulsed Drain Current ^b		I _{DM}	1.8	- 1	A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	0.45	- 0.25		
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	0.33	0.3	w	
	T _A = 85 °C		0.17	0.16	vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

Notes:

a. Surface mounted on FR4 board.

b. Pulse width limited by maximum junction temperature.





Parameter	Symbol Test Conditions				Тур.	Max.	Uni	
Static								
		V _{GS} = 0 V, I _D = 10 μA	N-Ch	60				
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = -10 \mu A$	P-Ch	- 60			l	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	N-Ch	1		3.0	- V	
		$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	P-Ch	- 1		- 3.0		
Gate-Body Leakage		$V_{DS} = 0 V, V_{GS} = \pm 5 V$	N-Ch			± 50		
	I _{GSS}	$v_{\rm DS} = 0$ V, $v_{\rm GS} = \pm 3$ V	P-Ch			± 100	-	
		<u> </u>	N-Ch			± 150		
		$V_{DS} = 0 V, V_{GS} = \pm 10 V$	P-Ch			± 200	- ^	
		$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	N-Ch			10	nA	
Zava Cata Valtana Duain Current		$V_{DS} = -50 \text{ V}, V_{GS} = 0 \text{ V}$	P-Ch			- 25	_	
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 50 V, V _{GS} = 0 V, T _J = 85 °C	N-Ch			100		
		$V_{DS} = -50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 85 ^{\circ}\text{C}$	P-Ch			- 250		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	N-Ch	1.5				
On-State Drain Current	'D(on)	V _{DS} = - 10 V, V _{GS} = - 4.5 V	P-Ch	- 0.85			A	
Drain-Source On-State Resistance ^a		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.25 \text{ A}$	N-Ch		1.8	3.9		
		V _{GS} = - 4.5 V, I _D = - 0.16 A	P-Ch		6.3	9.2		
	Б	V _{GS} = 10 V, I _D = 0.3 A	N-Ch		1.6	2.2	Ω	
	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 0.2 A	P-Ch		4.5	5.3		
Forward Transconductance ^a	a .	V _{DS} = 10 V, I _D = 0.2 A	N-Ch		12			
	9 _{fs}	V _{DS} = - 10 V, I _D = - 0.1A	P-Ch		10		ms	
	V _{SD}	I _S = 0.2 A, V _{GS} = 0 V	N-Ch			1.4	v	
Diode Forward Voltage ^a	♥ SD	I _S = - 0.2 A, V _{GS} = 0 V	P-Ch			- 1.4	v	
Dynamic ^b								
Tatal Cata Charge	0		N-Ch		1.2			
Total Gate Charge	Qg	N-Channel V _{DS} = 30 V, V _{GS} = 4.5 V, I _D = 0.25 A	P-Ch		1.2		- nC	
Gate-Source Charge	Q _{gs}		N-Ch		0.2			
Cale Course Charge	⊶gs	P-Channel	P-Ch		0.4			
Gate-Drain Charge	Q _{gd}	$V_{DS} = -30$ V, $V_{GS} = -4.5$ V, $I_{D} = -0.2$ A	N-Ch		0.8			
eare Drain enange	gu		P-Ch		0.5			
Input Capacitance	C _{iss}	N-Channel	N-Ch		60		– – pF	
put Cuputitairee	100	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	P-Ch		83			
Output Capacitance	Coss		N-Ch		18			
		P-Channel	P-Ch		15			
Reverse Transfer Capacitance	C _{rss}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	N-Ch		8		-	
-		N-Channel	P-Ch		9		+	
Turn-On Time ^c	t _{ON}	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 150 \Omega$	N-Ch		5			
	SON	$I_D \cong 0.2 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, \text{R}_{\text{g}} = 10 \Omega$	P-Ch		8		ns	
	te	P-Channel V _{DD} = - 25 V, R _L = 150 Ω	N-Ch		15		115	
Turn-Off Time ^c	t _{OFF}	$V_{DD} = -2.5 \text{ V}, \text{ H}_{L} = -100 \Omega_{2}$ $I_{D} \cong -0.2 \text{ A}, \text{ V}_{\text{GEN}} = -10 \text{ V}, \text{ H}_{\text{g}} = 10 \Omega_{2}$	P-Ch		13		1	

Notes:

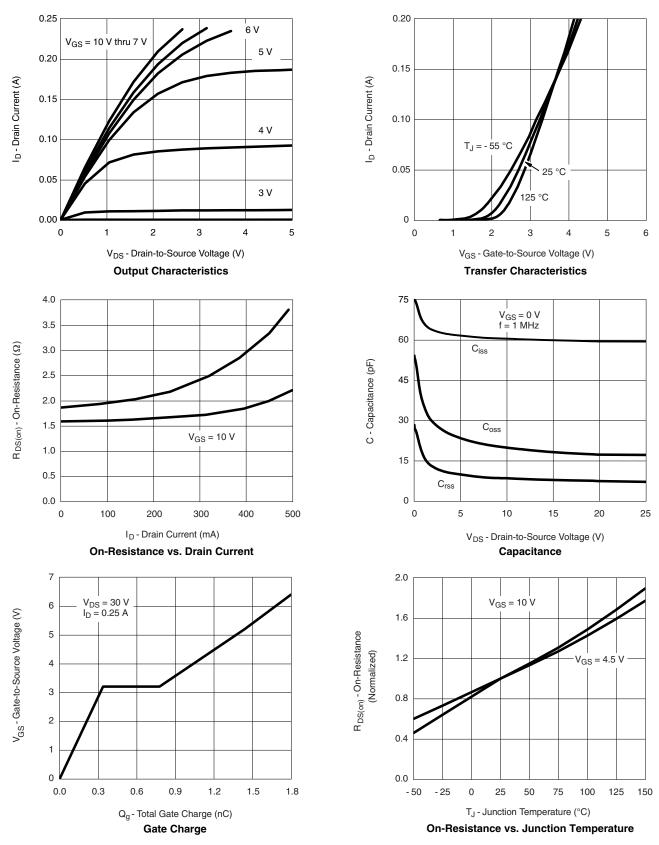
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

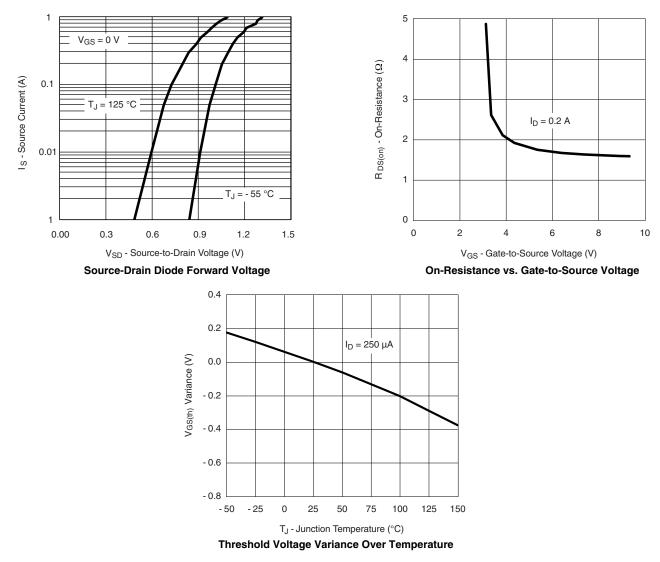






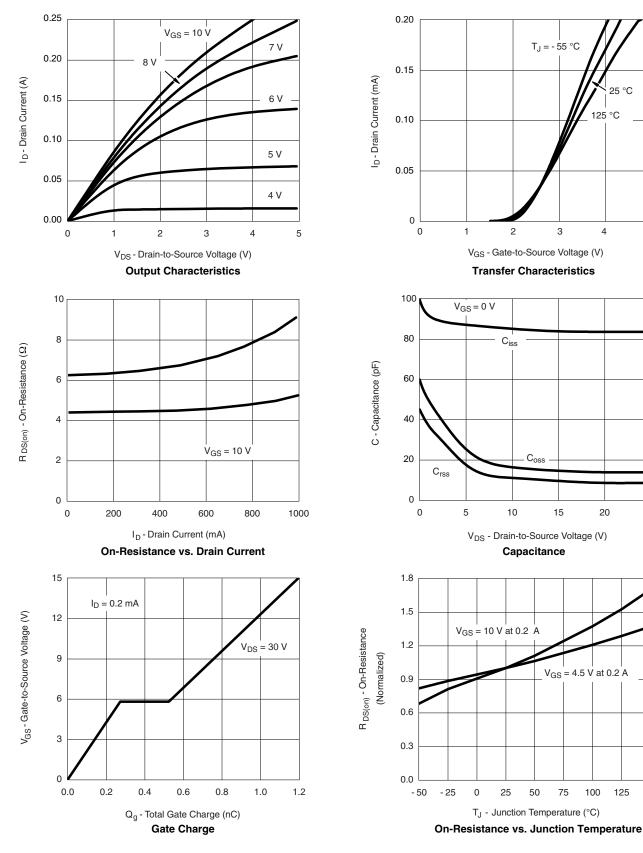


N-CHANNEL TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



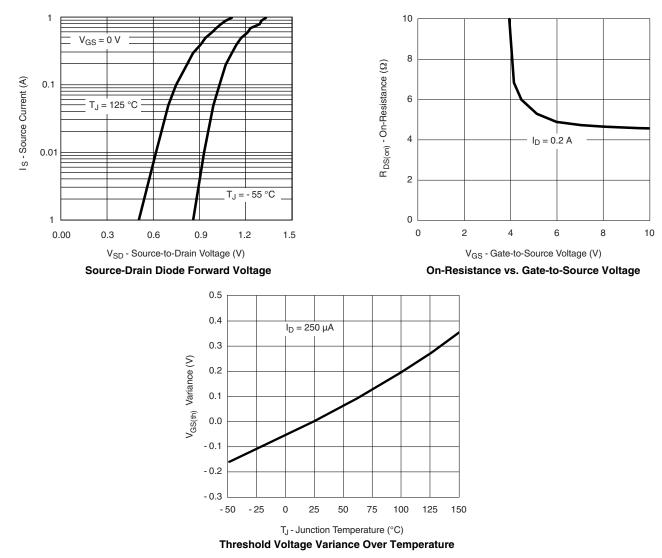


P-CHANNEL TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)

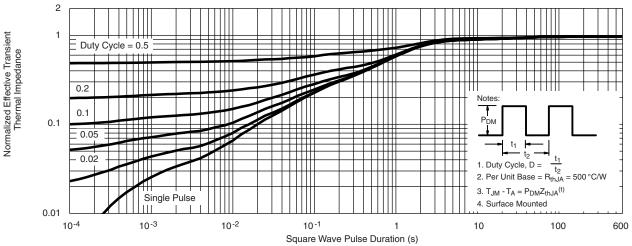




P-CHANNEL TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)





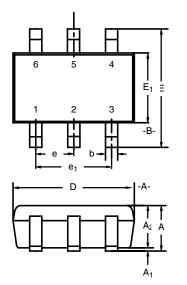


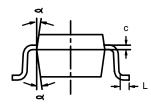
N- OR P-CHANNEL TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)





Package Information www.din-tek.jp

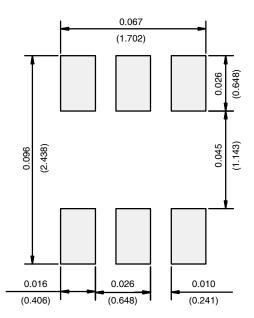




	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	-	0.043	
A ₁	-	-	0.10	-	-	0.004	
A ₂	0.80	-	1.00	0.031	-	0.039	
b	0.15	-	0.30	0.006	-	0.012	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Е	1.80	2.10	2.40	0.071	0.083	0.094	
E ₁	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65BSC			0.026BSC			
e ₁	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
م	7°Nom			7°Nom			



RECOMMENDED MINIMUM PADS FOR SOT323: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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