

Features

- Low Supply Current ~ 55uA (Typ.)
- Low Shutdown Current ~0.1uA (Typ.)
- Output Current ~300mA
- High Power Supply Rejection Ratio ~78dB@1KHz
- 1.7~5.5V Operation
- ±2% Initial Voltage Accuracy
- Low Temperature Drift Coefficient ~50ppm
- Line Regulation ~0.02%/V(Typ.)
- Low ESR Capacitor ~1.0uF ceramic capacitor
- WDFN4-1.2x1.6、SOT-23-5、SC-82、MSOT-23 and SOT-353 package
- Green Product (RoHS, Lead-Free, Halogen-Free Compliant)

Applications

- Portable communication equipment
- Notebook Computer
- Battery Powered Systems

Typical Application

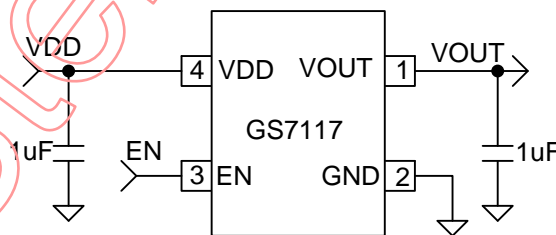


Figure.1 Typical Application of GS7117

General Description

The GS7117 is a CMOS linear regulator. It is featuring ultra-high power supply rejection ratio, low output voltage noise, low dropout voltage, low quiescent current and fast transient response. It guarantees delivery of 300mA output current, and supports preset 1.0V, 1.05V, 1.1V, 1.2V, 1.3V, 1.5V, 1.7V, 1.8V, 2.5V, 2.8V output voltage versions.

Based on its low quiescent current consumption and its less than 1uA shutdown mode, the GS7117 is ideal for battery- powered applications. The high power supply rejection ratio of the GS7117 holds well for low input voltages typically encountered in battery- operated systems. The regulator is stable with small ceramic capacitive loads (1μF typical).

This document is GStek's confidential information. Anyone having confidential obligation to GStek shall keep this document confidential. Any unauthorized disclosure or use beyond authorized purpose will be considered as violation of confidentiality and criminal and civil liability will be asserted.