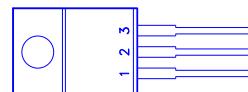
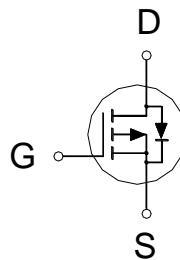


NIKO-SEM
**P-Channel Enhancement Mode
Field Effect Transistor**
P3506ET
TO-220
Halogen-Free & Lead-Free
PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-60V	35mΩ	-36A



1. GATE
-
2. DRAIN
-
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ²	I_D	-36	A
		-23	
Pulsed Drain Current ¹	I_{DM}	-86	A
Avalanche Current	I_{AS}	-28	
Avalanche Energy	E_{AS}	39	mJ
Power Dissipation	P_D	87	W
		35	
Junction & Storage Temperature Range	T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$	1.43	62.5	°C / W
Junction-to-Ambient	$R_{\theta JA}$			

¹Pulse width limited by maximum junction temperature.
ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu\text{A}$	-60			V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1.3	-1.8	-2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 25V$			±100	nA

NIKO-SEM
**P-Channel Enhancement Mode
Field Effect Transistor**
P3506ET
TO-220
Halogen-Free & Lead-Free

Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -48V, V_{GS} = 0V$			-1	μA
		$V_{DS} = -40V, V_{GS} = 0V, T_J = 125^\circ C$			-10	
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = -4.5V, I_D = -15A$		42	55	$m\Omega$
		$V_{GS} = -10V, I_D = -20A$		31	35	
Forward Transconductance ¹	g_{fs}	$V_{DS} = -5V, I_D = -20A$		20		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = -25V, f = 1MHz$		1451		pF
Output Capacitance	C_{oss}			152		
Reverse Transfer Capacitance	C_{rss}			104		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		3.6		Ω
Total Gate Charge ²	$Q_g (V_{GS}=-10V)$	$V_{DS} = -30V, V_{GS} = -10V, I_D = -20A$		30		nC
	$Q_g (V_{GS}=-4.5V)$			15		
Gate-Source Charge ²	Q_{gs}			4.4		
Gate-Drain Charge ²	Q_{gd}			8.3		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DS} = -30V, I_D \approx -20A, V_{GS} = -10V, R_{GEN} = 6\Omega$		13		nS
Rise Time ²	t_r			49		
Turn-Off Delay Time ²	$t_{d(off)}$			57		
Fall Time ²	t_f			95		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ C$)						
Continuous Current ³	I_s				-36	A
Forward Voltage ¹	V_{SD}	$I_F = -20A, V_{GS} = 0V$			-1.3	V
Reverse Recovery Time	t_{rr}	$I_F = -20A, dI_F/dt = 100A/\mu S$		20		nS
Reverse Recovery Charge	Q_{rr}			14		

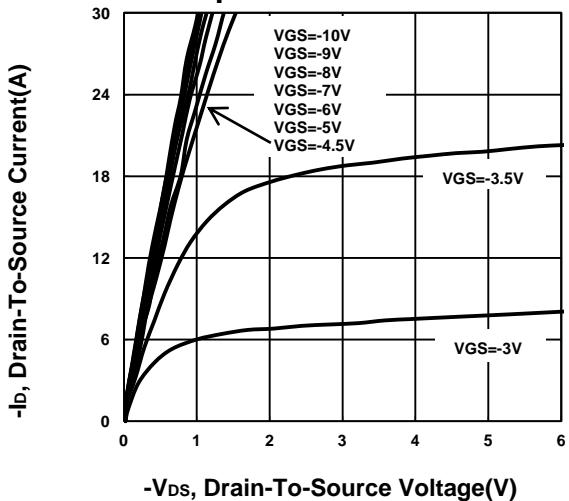
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.

NIKO-SEM

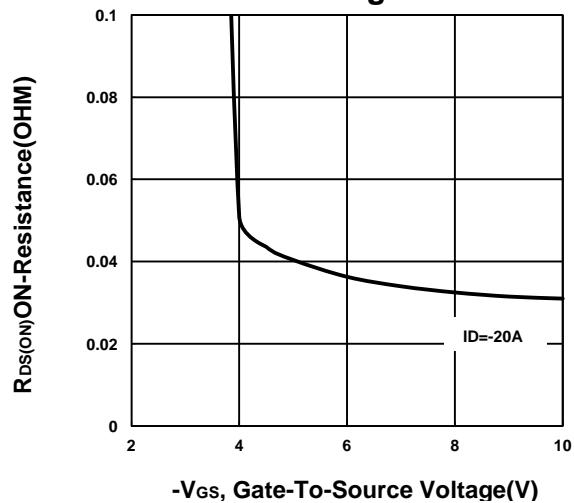
**P-Channel Enhancement Mode
Field Effect Transistor**

P3506ET
TO-220
Halogen-Free & Lead-Free

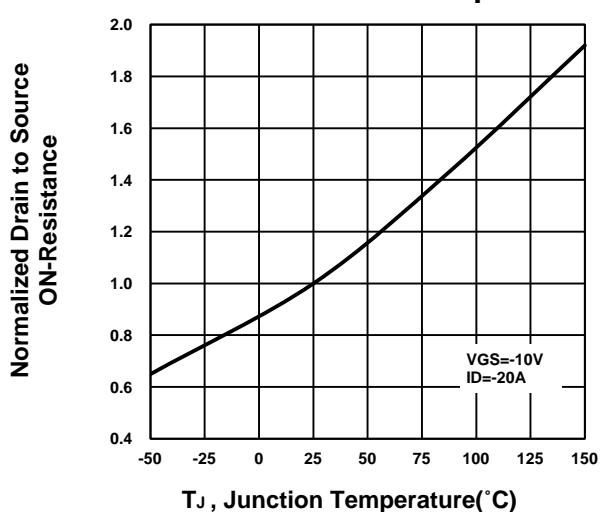
Output Characteristics



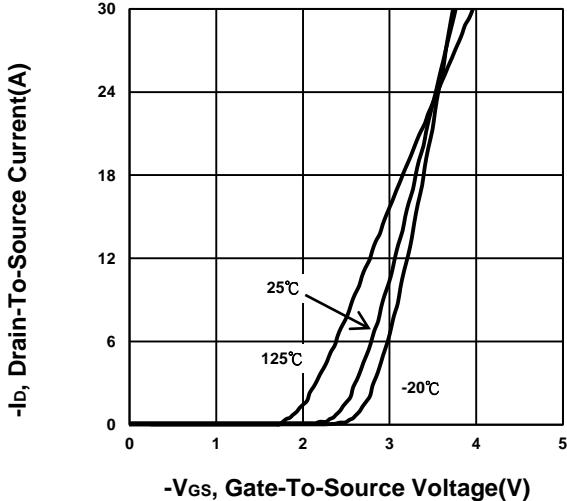
On-Resistance VS Gate-To-Source Voltage



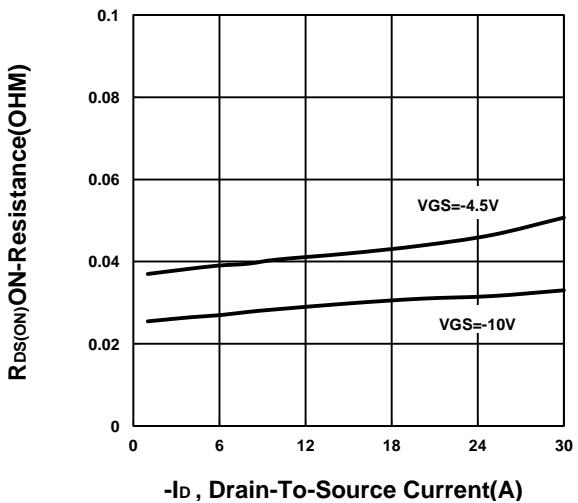
On-Resistance VS Temperature



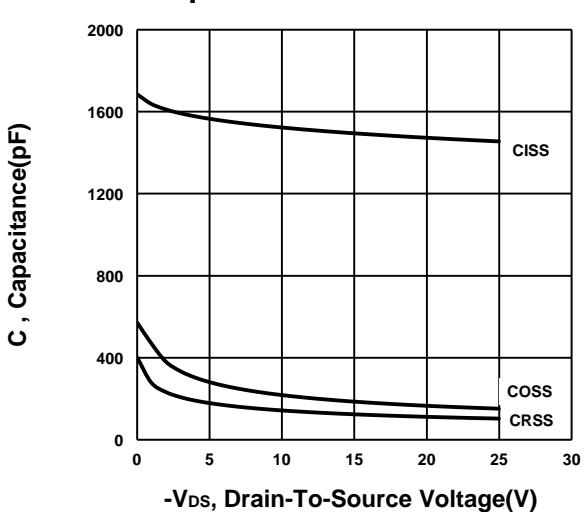
Transfer Characteristics



On-Resistance VS Drain Current



Capacitance Characteristic

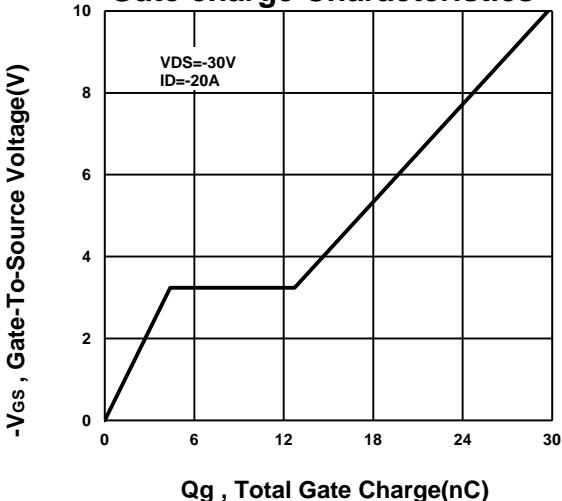


NIKO-SEM

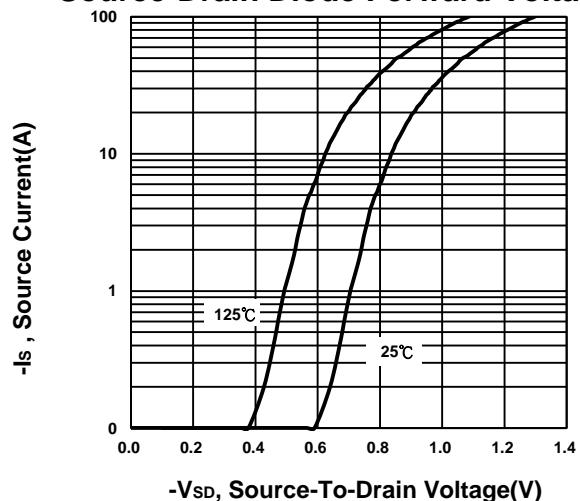
**P-Channel Enhancement Mode
Field Effect Transistor**

P3506ET
TO-220
Halogen-Free & Lead-Free

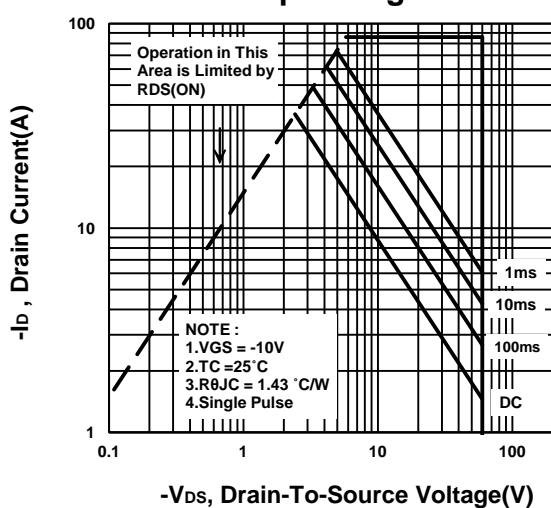
Gate charge Characteristics



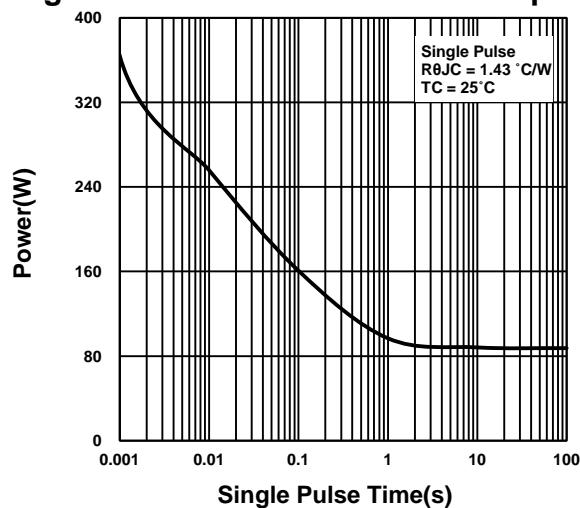
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

