NIKO-SEM

N-Channel Enhancement Mode Field Effect Transistor

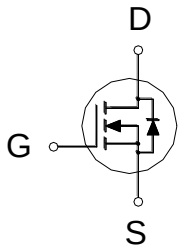
P2610BTF

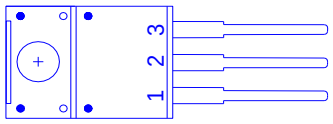
TO-220F

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	26.8mΩ	24A





RoHS Compliant

1: GATE
2: DRAIN
3: SOURCE

ABSOLUTE MAXIMUM RATINGS (T_A = 25 ° C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C = 25 ° C	I_D	24	A
	T_C = 100 ° C		15	
Pulsed Drain Current[1]		I_{DM}	70	
Avalanche Current		I_{AS}	11.7	
Avalanche Energy	L = 0.1mH	E_{AS}	6.9	mJ
Power Dissipation	T_C = 25 ° C	P_D	37.9	W
	T_C = 100 ° C		15	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	° C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$		62.5	° C / W
Junction-to-Case	$R_{\theta JC}$		3.3	

[1]Pulse width limited by maximum junction temperature.

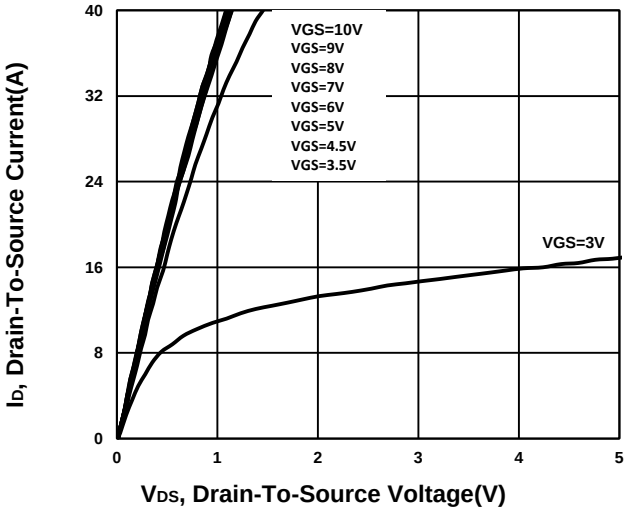
.ELECTRICAL CHARACTERISTICS (T_J = 25 ° C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS MIN	LIMITS TYP	LIMITS MAX	UNIT
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 80V, V_{GS} = 0V			1	μA
		V_{DS} = 80V, V_{GS} = 0V, T_J = 125 ° C			10	
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 4.5V, I_D = 10A		23.3	35	mΩ
		V_{GS} =10V, I_D = 10A		19.3	26.8	

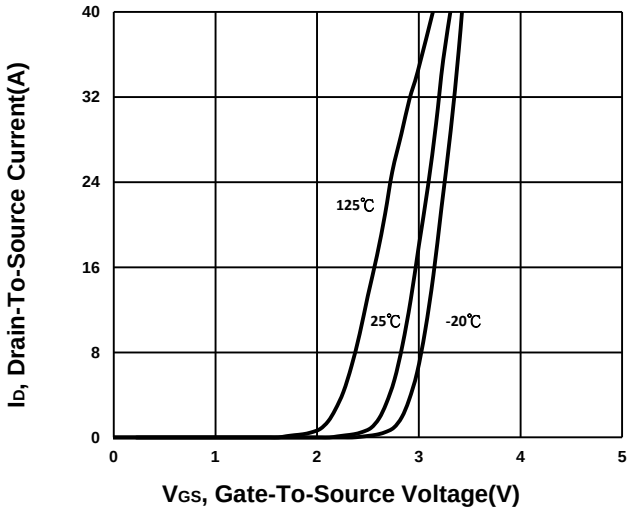
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D = 10A		50		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		1900		pF
Output Capacitance	C_{oss}			149		
Reverse Transfer Capacitance	C_{rss}			92		
Gate Resistance	R_g	V_{GS} = 0V, V_{DS} = 0V, f = 1MHz		0.8		Ω
Total Gate Charge[2]	$Q_{g(VGS=10V)}$	V_{DS} = 50V , I_D =10A		41.5		nC
	$Q_{g(VGS=4.5V)}$			22.4		
Gate-Source Charge[2]	Q_{gs}			6		
Gate-Drain Charge[2]	Q_{gd}			11.6		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} = 50V , $I_D \cong$ 10A, V_{GS} = 10V, R_{GEN} =6Ω		16		nS
Rise Time[2]	t_r			45		
Turn-Off Delay Time[2]	$t_{d(off)}$			47		
Fall Time[2]	t_f			38		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 ° C)						
Continuous Current	I_S				24	A
Forward Voltage[1]	V_{SD}	I_F = 10A, V_{GS} = 0V			1.2	V
Diode Reverse Recovery Time	t_{rr}	I_F= 10A, dI/dt=100A/μs		32		nS
Diode Reverse Recovery Charge	Q_{rr}			36		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.

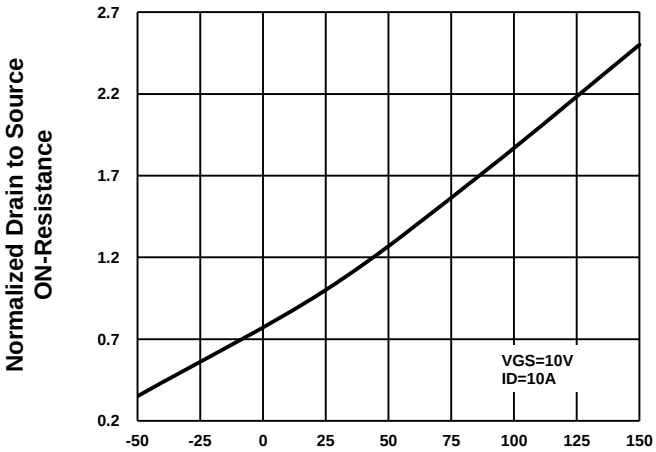
Output Characteristics



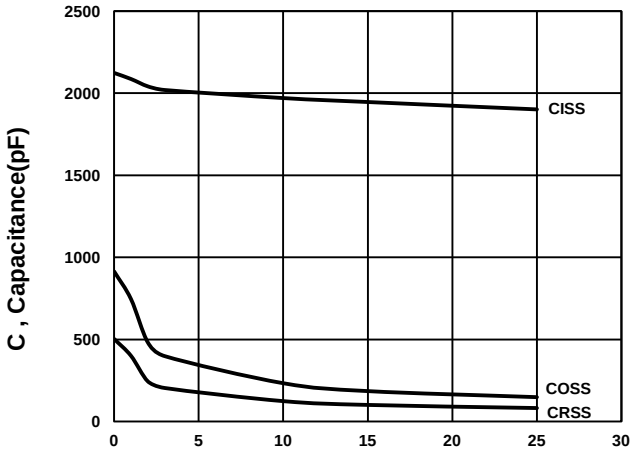
Transfer Characteristics



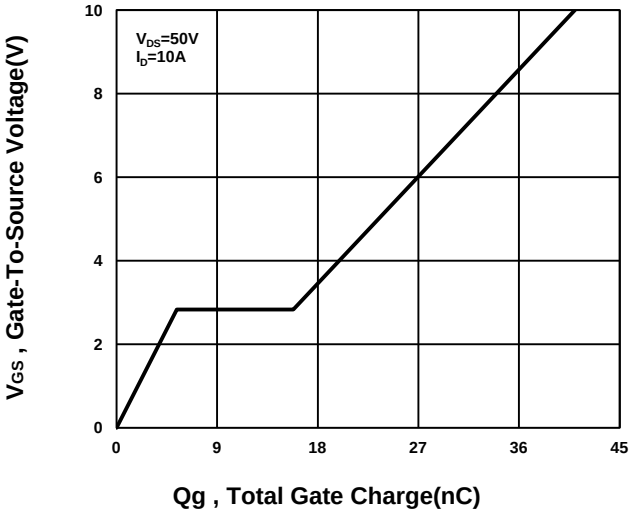
On-Resistance VS Temperature



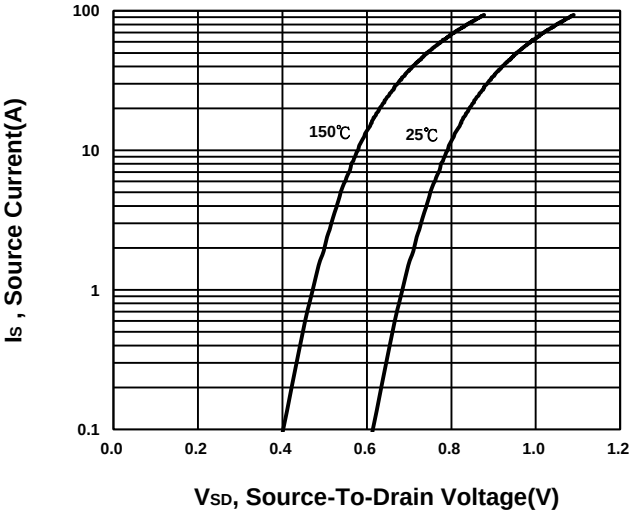
Capacitance Characteristic



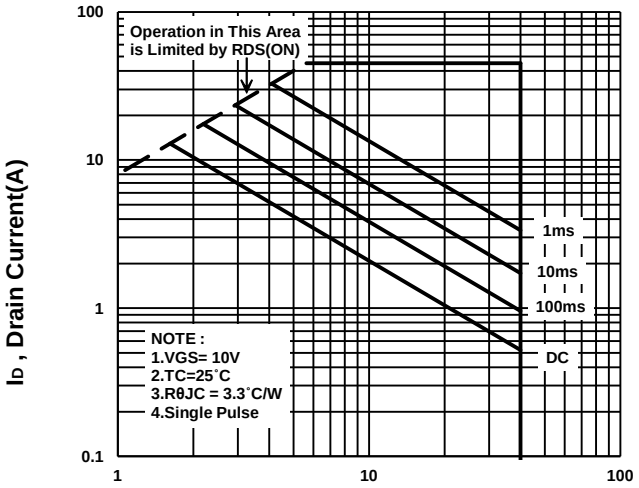
Gate charge Characteristics



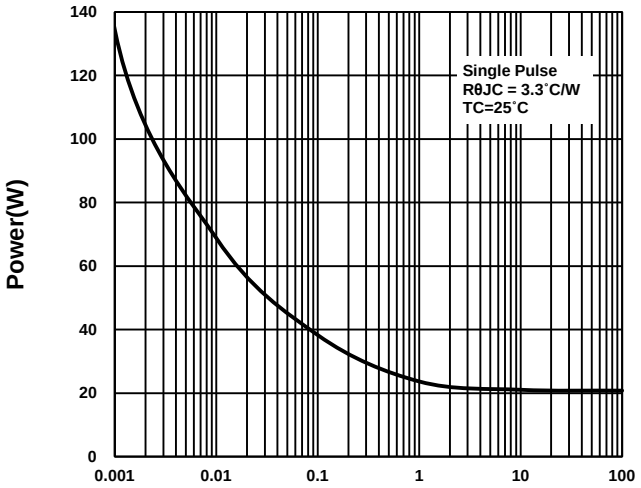
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve