NIKO-SEM

N-Channel Enhancement Mode Field Effect Transistor

P5015CD

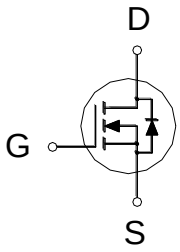
TO-252

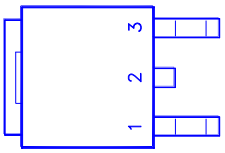
Halogen-Free & Lead-Free

RoHS Compliant

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	50mΩ	23A





1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current	T_C = 25 °C	I_D	23	A
	T_C = 100 °C		14	
Pulsed Drain Current[1]		I_{DM}	80	
Avalanche Current		I_{AS}	9.6	
Avalanche Energy	L = 1mH	E_{AS}	46	mJ
Power Dissipation	T_C = 25 °C	P_D	71	W
	T_C = 100 °C		28	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.75	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

[1]Pulse width limited by maximum junction temperature.

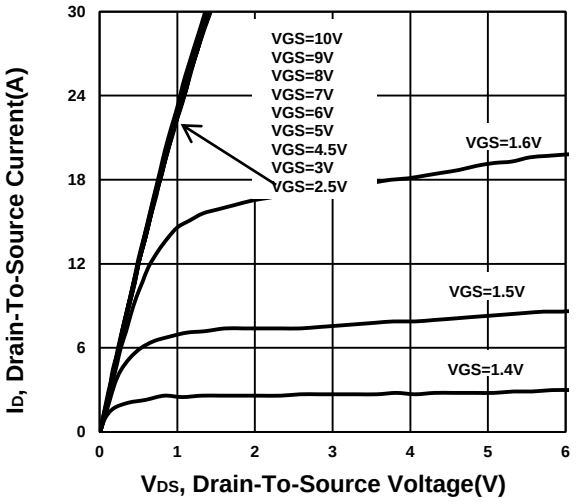
ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	150			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	0.65	0.95	1.3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±12V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 150V, V_{GS} = 0V			1	μA
		V_{DS} = 100V, V_{GS} = 0V, T_J = 125 °C			10	

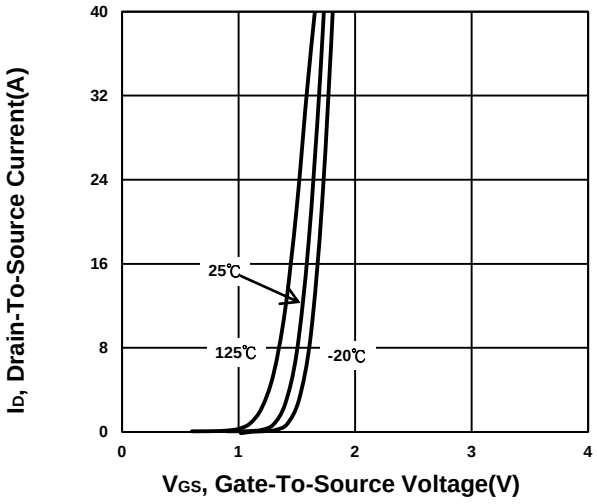
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 10V , I_D = 10A		33	50	mΩ
		V_{GS} = 4.5V, I_D = 10A		33.2	55	
		V_{GS} = 3V , I_D = 10A		33.5	60	
		V_{GS} = 2.5V , I_D = 10A		34	80	
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D = 10A		103		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 75V, f = 1MHz		3910		pF
Output Capacitance	C_{oss}			100		
Reverse Transfer Capacitance	C_{rss}			66		
Gate Resistance	R_g	V_{GS} = 0V, V_{DS} = 0V, f = 1MHz		0.8		Ω
Total Gate Charge[2]	Q_g	V_{GS} = 10 V , V_{DS} = 75V , I_D = 10A		105		nC
Gate-Source Charge[2]	Q_{gs}			8.4		
Gate-Drain Charge[2]	Q_{gd}			17		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DS} = 75V , $I_D \cong$ 10A, V_{GS} = 10V, R_{GEN} =6Ω		14		nS
Rise Time[2]	t_r			33		
Turn-Off Delay Time[2]	$t_{d(off)}$			166		
Fall Time[2]	t_f			103		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I_S				23	A
Forward Voltage[1]	V_{SD}	I_F = 10A, V_{GS} = 0V			1	V
Reverse Recovery Time	t_{rr}	I_F = 10A, dI_F/dt = 100A / μS		36		nS
Reverse Recovery Charge	Q_{rr}			39		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.

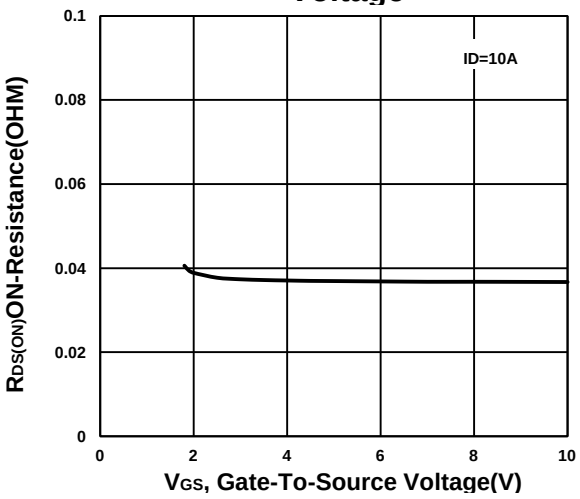
Output Characteristics



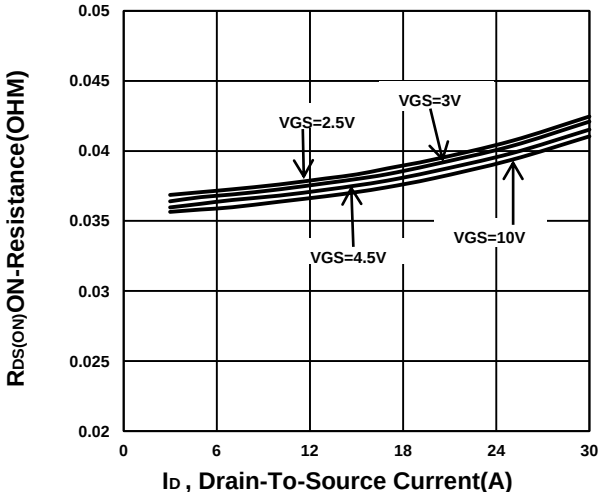
Transfer Characteristics



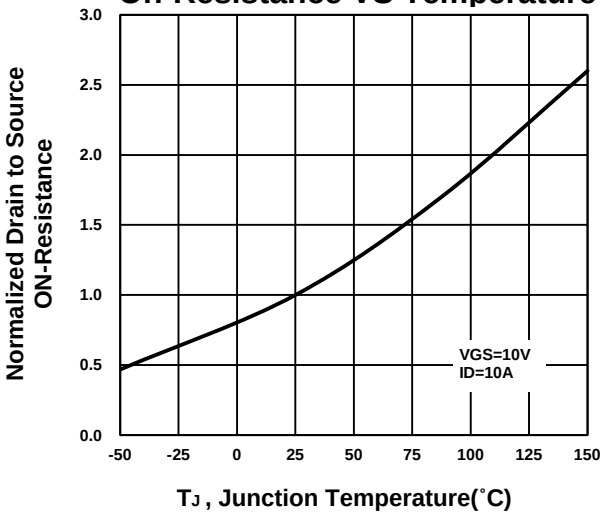
On-Resistance VS Gate-To-Source Voltage



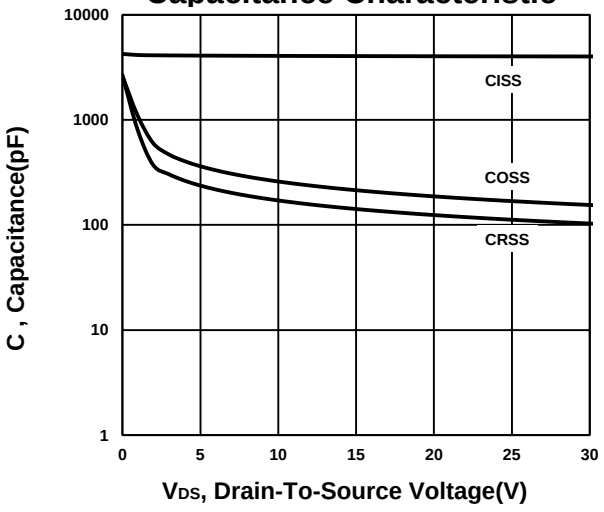
On-Resistance VS Drain Current



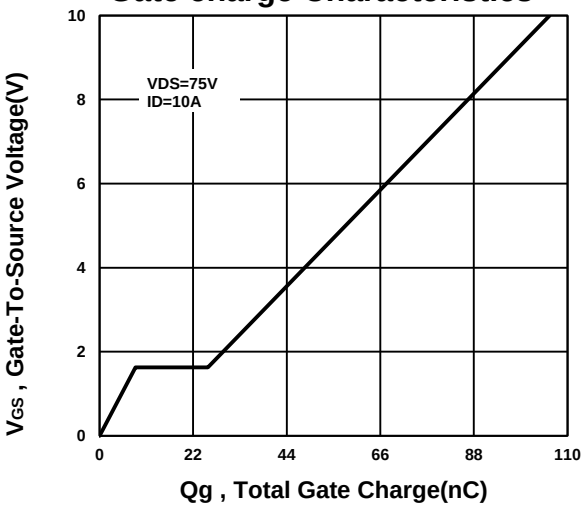
On-Resistance VS Temperature



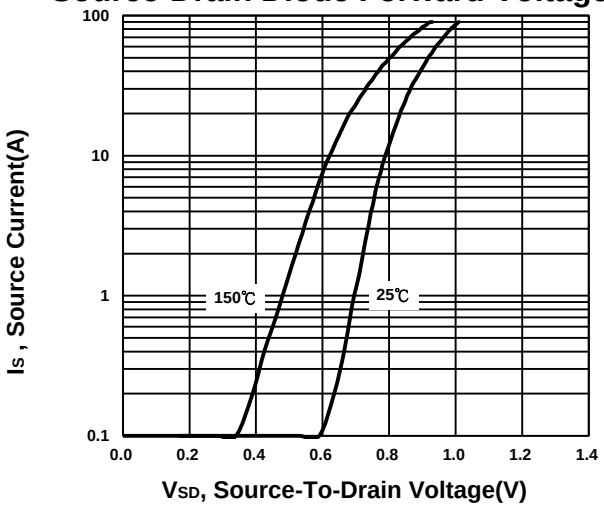
Capacitance Characteristic



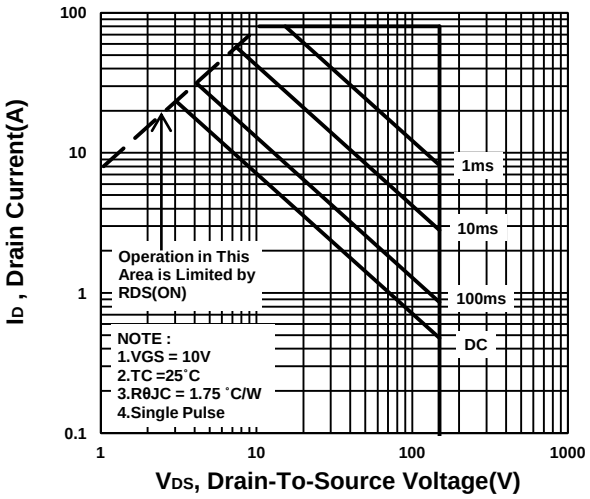
Gate charge Characteristics



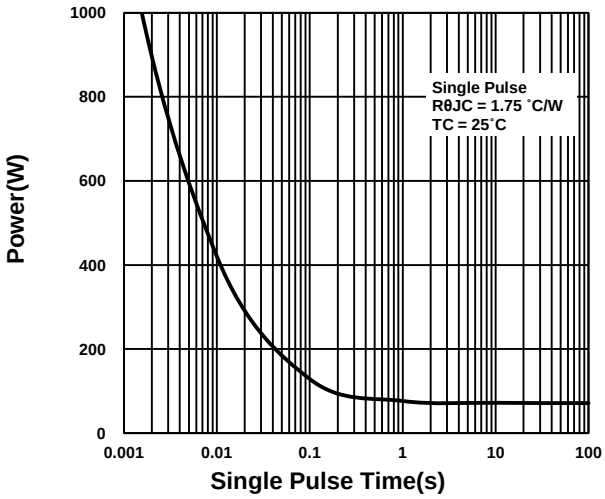
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve