

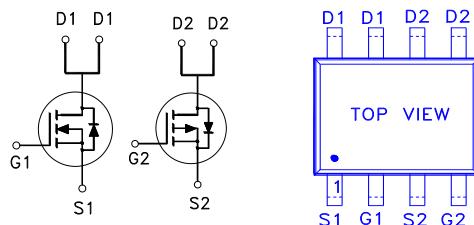
**NIKO-SEM****N- & P-Channel Enhancement Mode Field Effect Transistor****P5506NV**

SOP-8

Halogen-Free &amp; Lead-Free

**PRODUCT SUMMARY**

	$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
Q2	-60V	95mΩ	-3.3A
Q1	60V	55mΩ	4A



G. GATE  
D. DRAIN  
S. SOURCE

**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	Q2	Q1	UNITS
Drain-Source Voltage	$V_{DS}$	-60	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 25$	$\pm 20$	V
Continuous Drain Current	$I_D$	-3.3	4	A
		-2.7	3.2	
Pulsed Drain Current <sup>1</sup>	$I_{DM}$	-20	20	
Avalanche Current	$I_{AS}$	-17	14	
Avalanche Energy	$E_{AS}$	14	9.8	mJ
Power Dissipation	$P_D$	2	2.1	W
		1.3	1.3	
Operating Junction & Storage Temperature Range	$T_j, T_{stg}$	-55 to 150		°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient	$R_{\theta JA}$	Q2	61	°C / W
		Q1	60	
Junction-to-Ambient	$R_{\theta JA}$	Q2	102	
		Q1	101	

<sup>1</sup>Pulse width limited by maximum junction temperature.

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**ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	Q2	-60		V
		$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	Q1	60		
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	Q2	-1.3	-1.8	-2.3
		$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	Q1	1.3	1.8	2.3
Gate-Body Leakage	$I_{\text{GSS}}$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 25\text{V}$	Q2			$\pm 100$
		$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$	Q1			$\pm 100$
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -48\text{V}, V_{\text{GS}} = 0\text{V}$	Q2			-1
		$V_{\text{DS}} = 48\text{V}, V_{\text{GS}} = 0\text{V}$	Q1			1
		$V_{\text{DS}} = -40\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$	Q2			-10
		$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$	Q1			10
Drain-Source On-State Resistance <sup>1</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = -4.5\text{V}, I_D = -3.4\text{A}$	Q2		115	135
		$V_{\text{GS}} = 4.5\text{V}, I_D = 4\text{A}$	Q1		54	72
		$V_{\text{GS}} = -10\text{V}, I_D = -3.4\text{A}$	Q2		82	95
		$V_{\text{GS}} = 10\text{V}, I_D = 4\text{A}$	Q1		46	55
Forward Transconductance <sup>1</sup>	$g_{\text{fs}}$	$V_{\text{DS}} = -5\text{V}, I_D = -3.4\text{A}$	Q2		10	S
		$V_{\text{DS}} = 5\text{V}, I_D = 4\text{A}$	Q1		21	
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -25\text{V}, f = 1\text{MHz}$	Q2		545	pF
Output Capacitance	$C_{\text{oss}}$		Q1		389	
Reverse Transfer Capacitance	$C_{\text{rss}}$		Q2		76	
Gate Resistance	$R_g$		Q1		55	
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$	Q2		49	nC
Gate-Source Charge <sup>2</sup>	$Q_{\text{gs}}$		Q1		38	
Gate-Drain Charge <sup>2</sup>	$Q_{\text{gd}}$		Q2		8.9	
			Q1		1.7	
			Q2		12	
			Q1		11	
			Q2		6.7	
			Q1		6.1	

**NIKO-SEM****N- & P-Channel Enhancement Mode Field  
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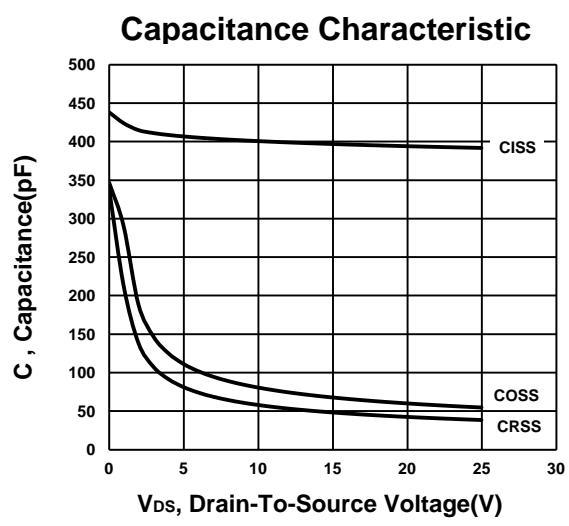
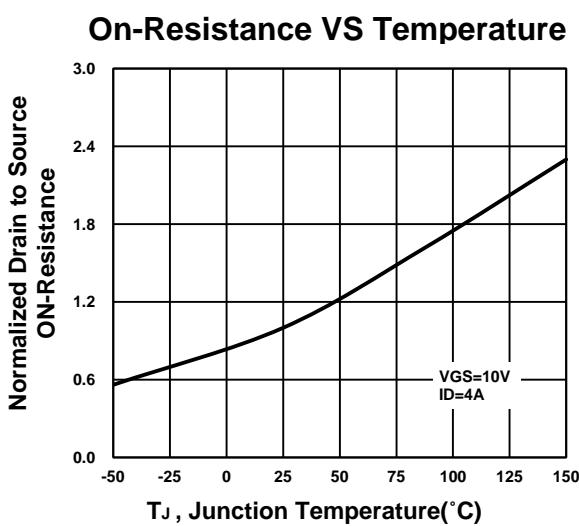
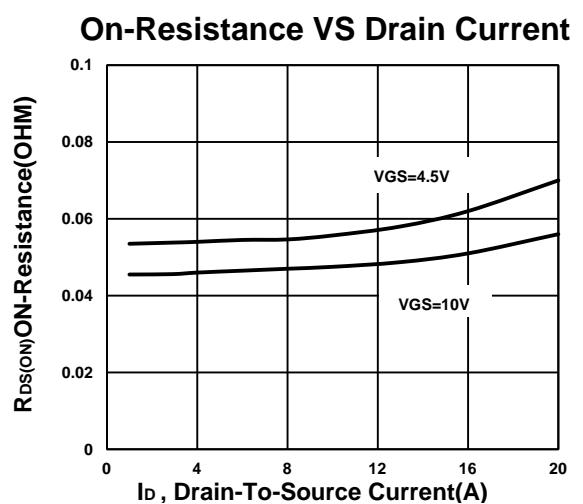
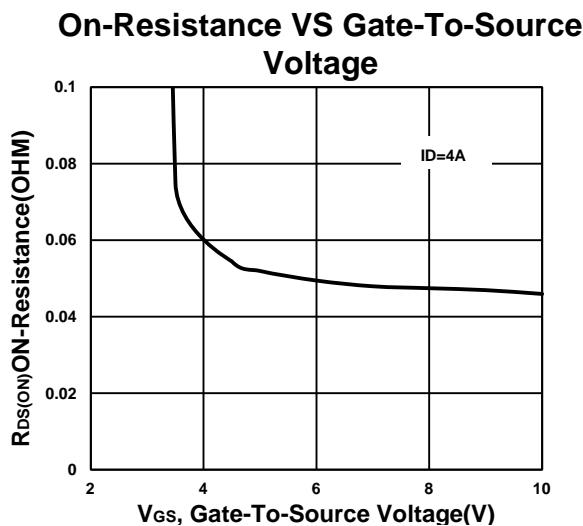
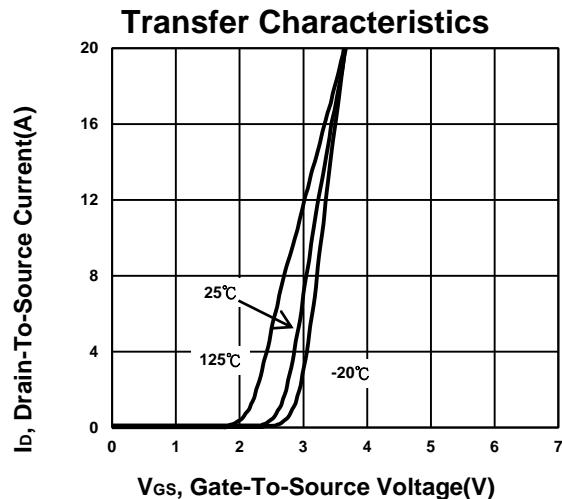
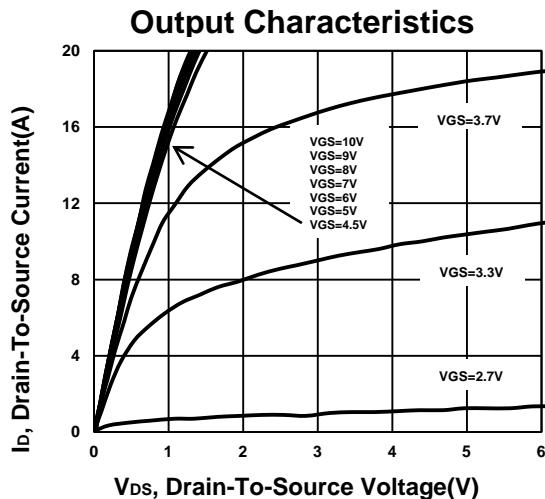
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	Q2 , $V_{DS} = -30V$ , $I_D \geq -3.4A$ , $V_{GS} = -10V$ , $R_{GEN} = 6\Omega$	Q2		12		nS
Rise Time <sup>2</sup>	$t_r$		Q1		11		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		Q2		33		
Fall Time <sup>2</sup>	$t_f$		Q1		49		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (<math>T_J = 25^\circ C</math>)</b>							
Continuous Current <sup>3</sup>	$I_S$		Q2			-2	A
			Q1			1.8	
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = -3.4A$ , $V_{GS} = 0V$	Q2			-1	V
		$I_F = 4A$ , $V_{GS} = 0V$	Q1			1.2	
Reverse Recovery Time	$t_{rr}$	Q2 $I_F = -3.4A$ , $dI_F/dt = 100A/\mu S$	Q2		17		nS
			Q1		17		
Reverse Recovery Charge	$Q_{rr}$	Q1 $I_F = 4A$ , $dI_F/dt = 100A/\mu S$	Q2		15		nC
			Q1		11		

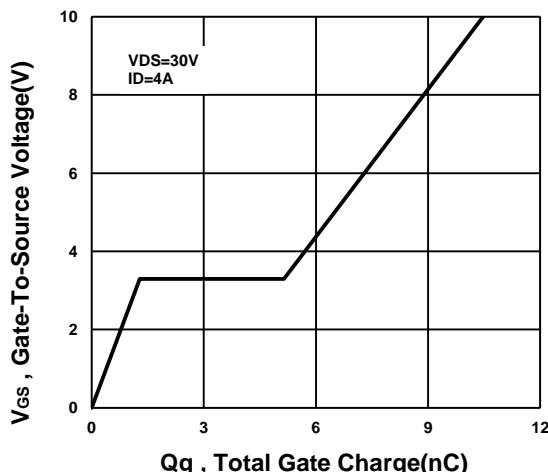
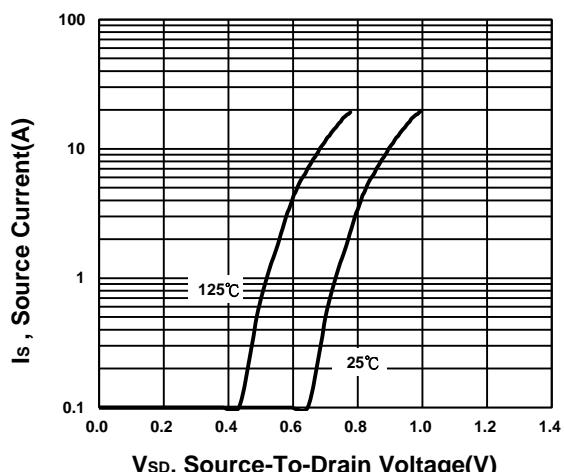
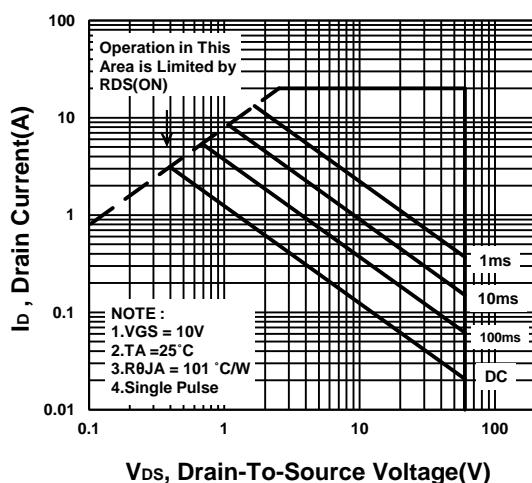
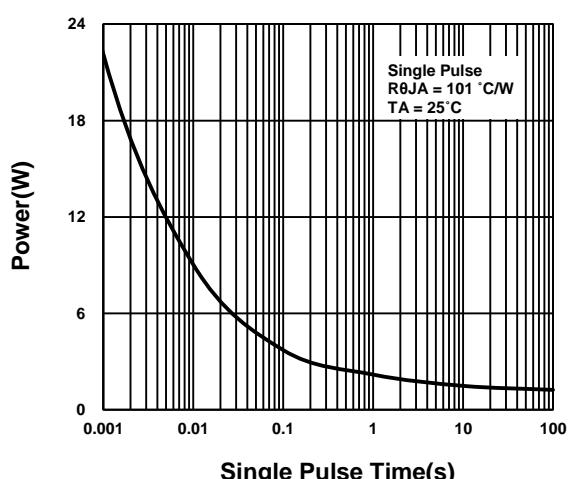
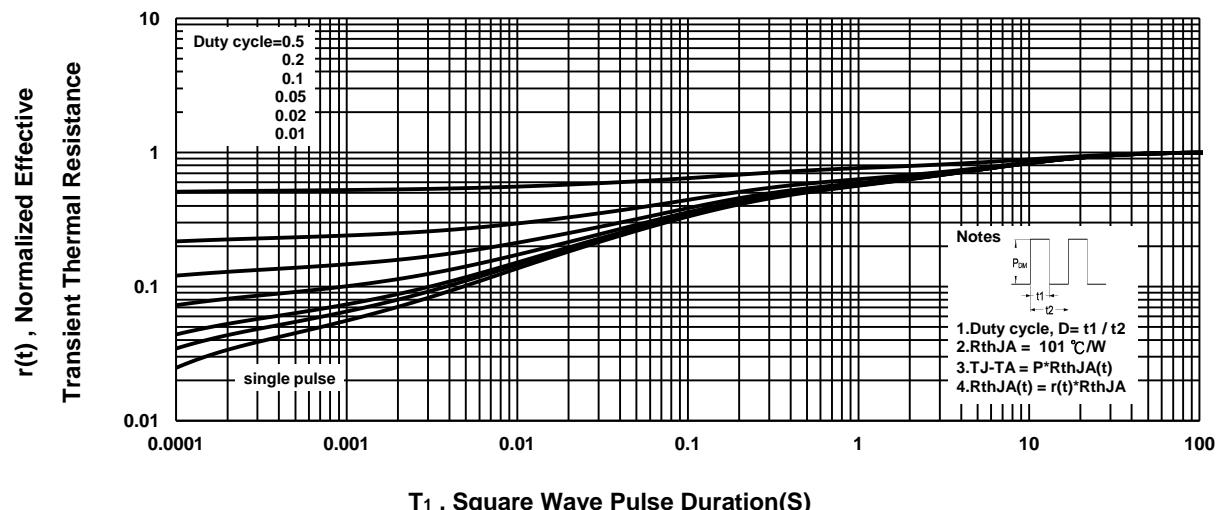
<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .<sup>2</sup>Independent of operating temperature.

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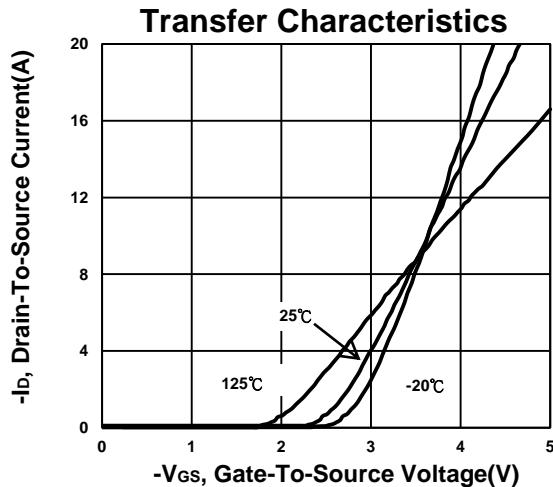
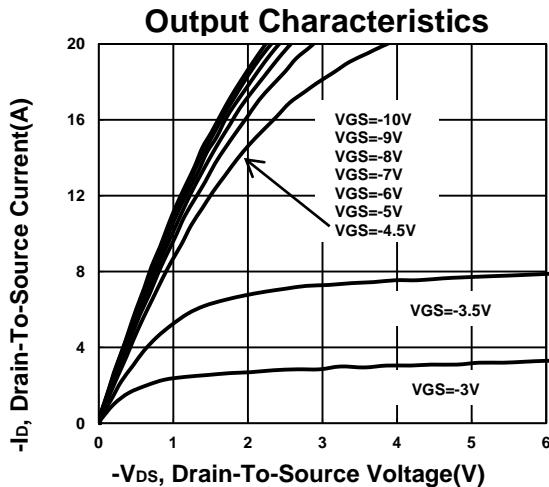
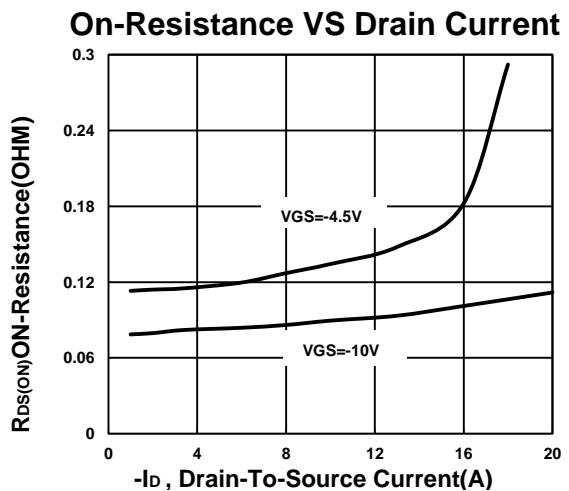
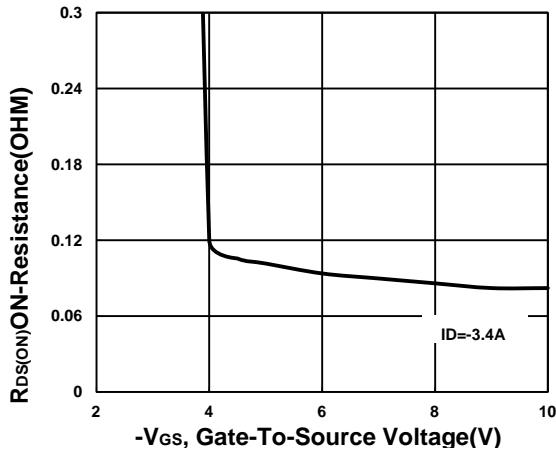
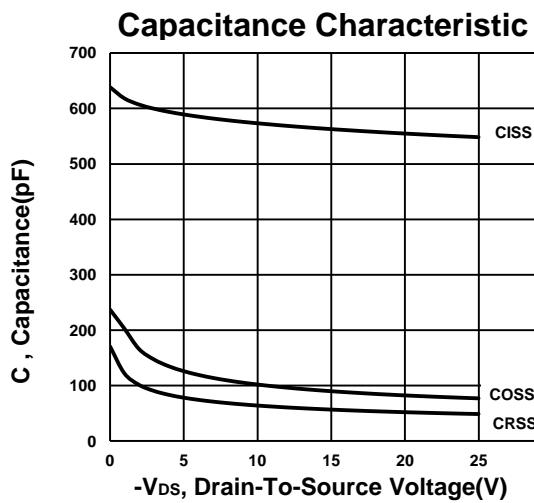
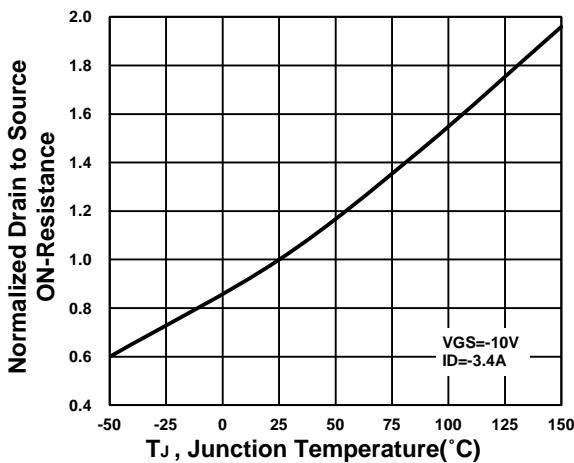
**Typical performance characteristics****N-channel**

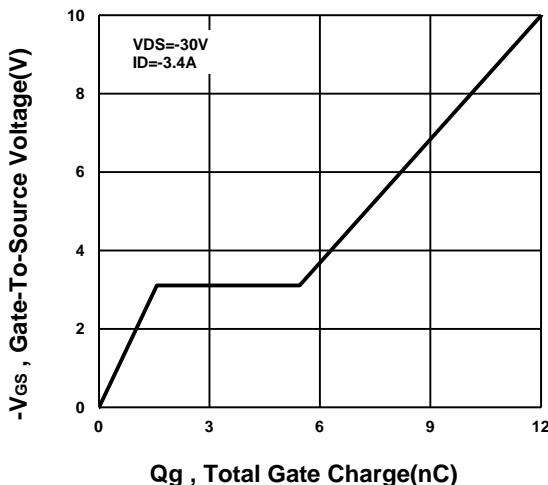
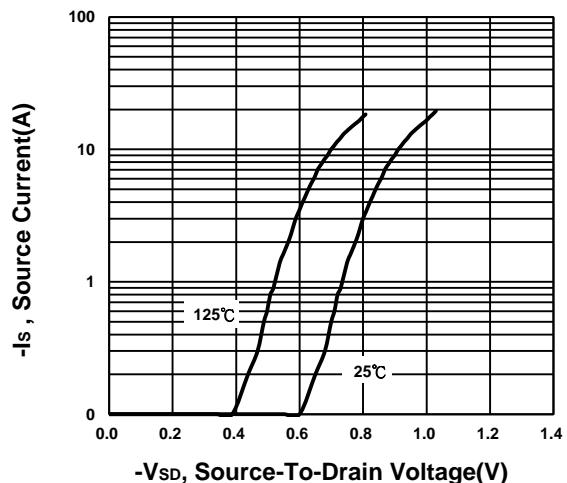
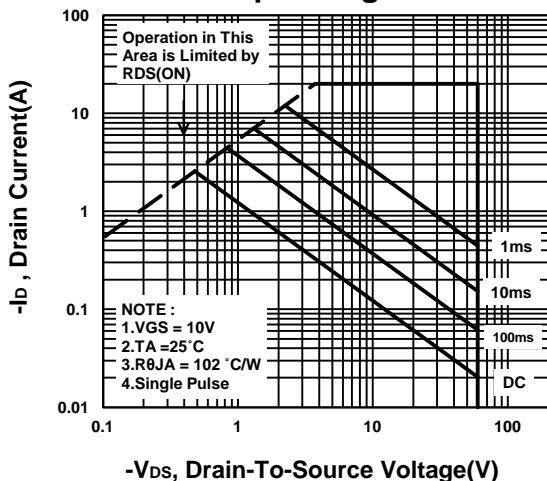
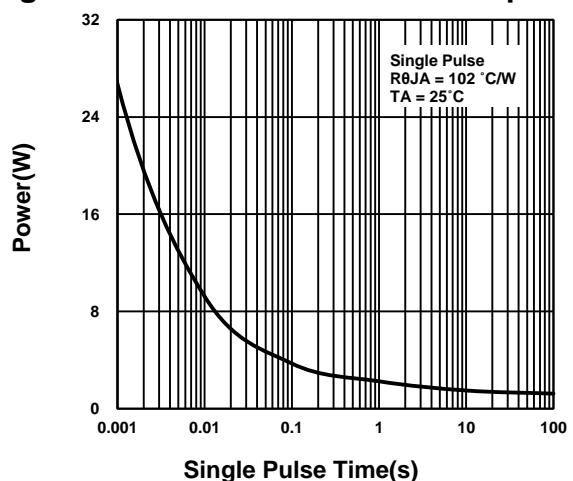
**NIKO-SEM****N- & P-Channel Enhancement Mode Field Effect Transistor****P5506NV**  
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Halogen-Free & Lead-Free**Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**

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**Typical performance characteristics****P-channel****On-Resistance VS Gate-To-Source Voltage****On-Resistance VS Temperature**

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Halogen-Free & Lead-Free**Gate charge Characteristics****Source-Drain Diode Forward Voltage****Safe Operating Area****Single Pulse Maximum Power Dissipation****Transient Thermal Response Curve**