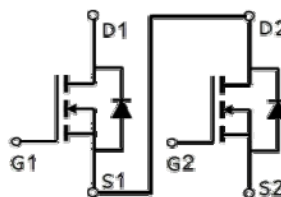




**PRODUCT SUMMARY**

	$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
Q2	30V	1.9mΩ	99A
Q1	30V	9.5mΩ	34A

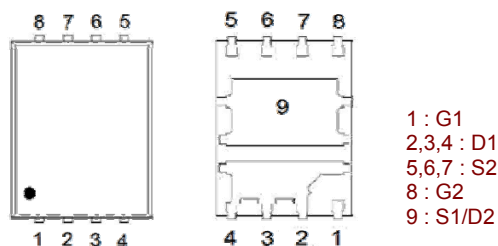


**Features**

- Pb-Free, Halogen Free and RoHS compliant.
- Low  $R_{DS(on)}$  to Minimize Conduction Losses.
- Ohmic Region Good  $R_{DS(on)}$  Ratio.
- Optimized Gate Charge to Minimize Switching Losses.

**Applications**

- Protection Circuits Applications.
- Logic/Load Switch Circuits Applications
- Computer for DC to DC Converters Applications.



**ABSOLUTE MAXIMUM RATINGS ( $T_A = 25\text{ °C}$  Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS		SYMBOL	Q2	Q1	UNITS
Drain-Source Voltage		$V_{DS}$	30	30	V
Gate-Source Voltage		$V_{GS}$	±20	±20	V
Continuous Drain Current <sup>3</sup>	$T_C = 25\text{ °C}$	$I_D$	99	34	A
	$T_C = 100\text{ °C}$		63	21	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	150	70	
Continuous Drain Current	$T_A = 25\text{ °C}$	$I_D$	25	9.2	
	$T_A = 70\text{ °C}$		20	7.3	
Avalanche Current		$I_{AS}$	52	22	
Avalanche Energy	$L = 0.1\text{mH}$	$E_{AS}$	135	24	mJ
Power Dissipation	$T_C = 25\text{ °C}$	$P_D$	36	24	W
	$T_C = 100\text{ °C}$		14.7	9.6	
Power Dissipation	$T_A = 25\text{ °C}$	$P_D$	2.4	1.7	W
	$T_A = 70\text{ °C}$		1.5	1.1	
Operating Junction & Storage Temperature Range		$T_j, T_{stg}$	-55 to 150		°C

**THERMAL RESISTANCE RATINGS**

THERMAL RESISTANCE	SYMBOL		TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>2</sup>	R <sub>θJA</sub>	Q2		52	°C / W
	R <sub>θJA</sub>	Q1		72	
Junction-to-Case	R <sub>θJC</sub>	Q2		3.4	
	R <sub>θJC</sub>	Q1		5.2	

<sup>1</sup>Pulse width limited by maximum junction temperature T<sub>J(MAX)</sub>=150°C.

<sup>2</sup>The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C. The value in any given application depends on the user's specific board design.

<sup>3</sup>Package limitation current :Q1=35A,Q2=35A.

**ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25 °C, Unless Otherwise Noted)**

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT	
			MIN	TYP	MAX		
<b>STATIC</b>							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	Q2	30		V	
			Q1	30			
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	Q2	1.3	1.75	2.3	V
			Q1	1.3	1.75	2.3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	Q2			±100	nA
			Q1			±100	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V	Q2			1	μA
			Q1			1	
		V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 55 °C	Q2			10	
			Q1			10	
Drain-Source On-State Resistance <sup>1</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 16A	Q2		2.1	2.5	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 13A	Q1		10.5	14	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	Q2		1.5	1.9	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 13A	Q1		6.7	9.5	
Forward Transconductance <sup>1</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A	Q2		72		S
		V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	Q1		37		

DYNAMIC							
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$	Q2	2868	pF		
			Q1	525			
Output Capacitance	$C_{oss}$		Q2	515			
			Q1	146			
Reverse Transfer Capacitance	$C_{rss}$		Q2	315			
			Q1	70			
Gate Resistance	$R_g$	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$	Q2	1.1	$\Omega$		
		Q1	1.1				
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 15V, I_D = 20A$	$V_{GS} = 10V$		nC		
			Q2	56			
			Q1	10.5			
			$V_{GS} = 4.5V$				
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		Q2	29			
			Q1	5.8			
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		Q2	8.3			
			Q1	1.5			
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 15V, I_D \cong 20A, V_{GS} = 10V, R_{GEN} = 6\Omega$	Q2		nS		
			Q2	25			
Rise Time <sup>2</sup>	$t_r$		Q1	11			
			Q2	18			
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$		Q1				
			Q2	54			
Fall Time <sup>2</sup>	$t_f$		$V_{DS} = 15V, I_D \cong 13A, V_{GS} = 10V, R_{GEN} = 6\Omega$				
			Q1	7			
		Q2	20				
		Q1	5				
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ( $T_J = 25^\circ C$ )							
Continuous Current <sup>3</sup>	$I_S$		Q2	36	A		
			Q1	20			
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 20A, V_{GS} = 0V$	Q2	1	V		
		$I_F = 13A, V_{GS} = 0V$	Q1	1.2			
Reverse Recovery Time	$t_{rr}$	$I_F = 20A, dl_F/dt = 100A / \mu S$	Q2		nS		
			Q2	28			
Q1	10						
Reverse Recovery Charge			Q1				
	$Q_{rr}$	$I_F = 13A, dl_F/dt = 100A / \mu S$	Q2	12	nC		
		Q1	2				

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .

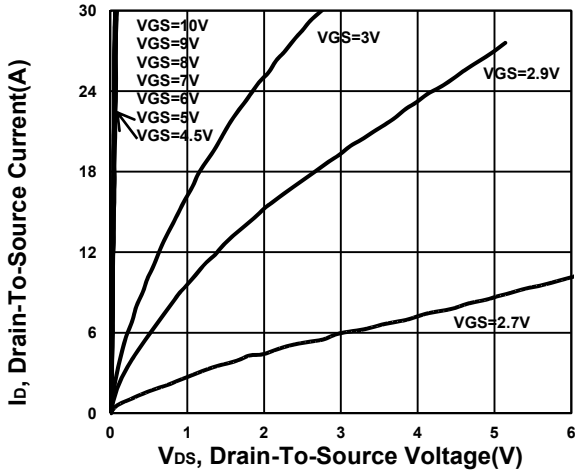
<sup>2</sup>Independent of operating temperature.

<sup>3</sup>Package limitation current : Q1=35A, Q2=35A.

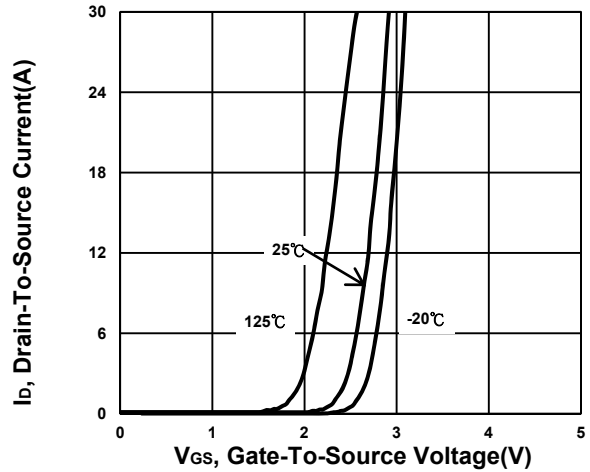
**TYPICAL PERFORMANCE CHARACTERISTICS**

**Q2**

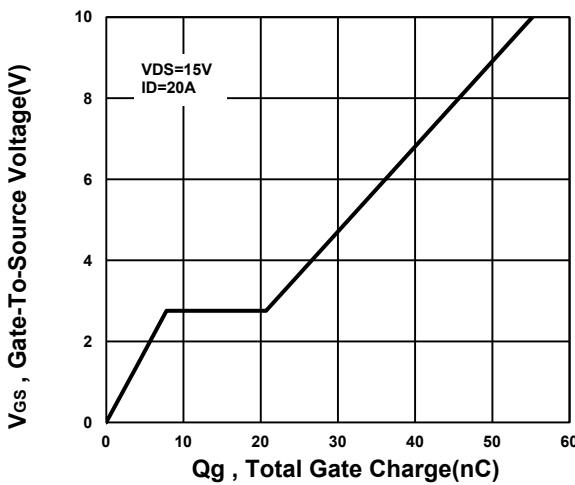
**Output Characteristics**



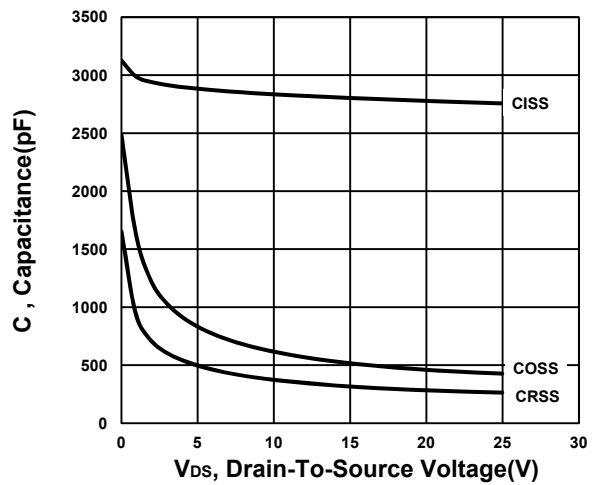
**Transfer Characteristics**



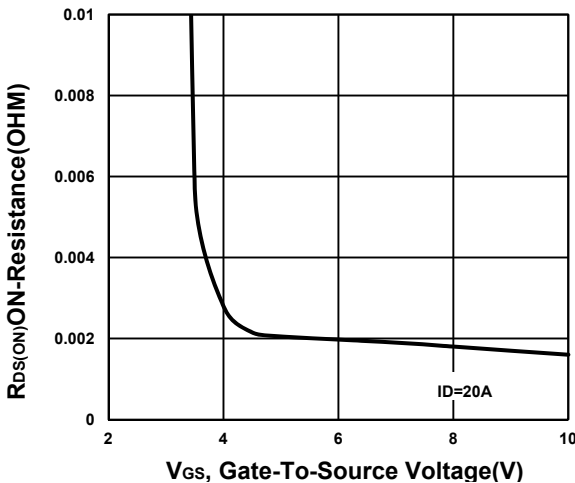
**Gate charge Characteristics**



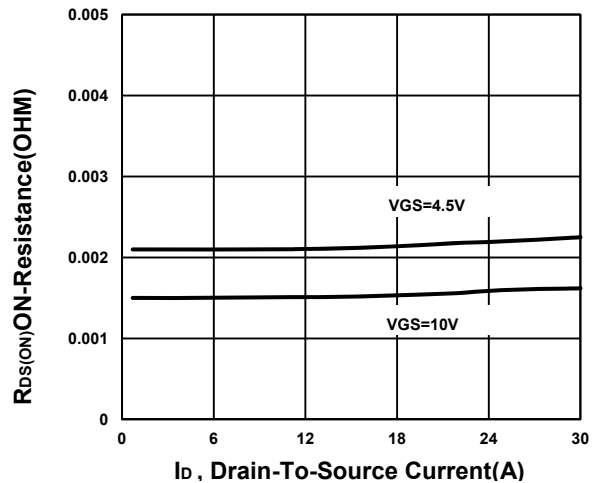
**Capacitance Characteristic**



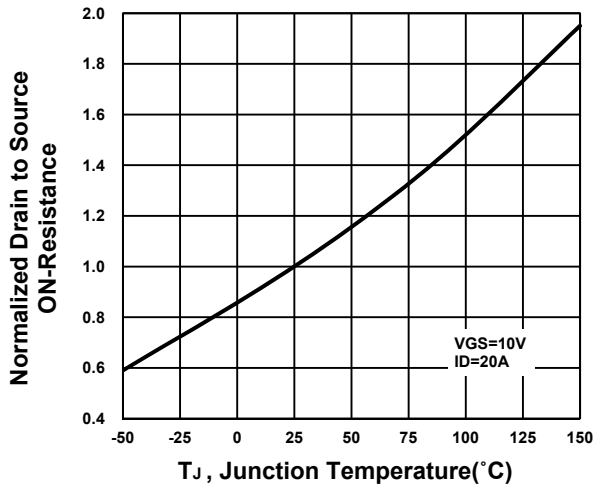
**On-Resistance VS Gate-To-Source**



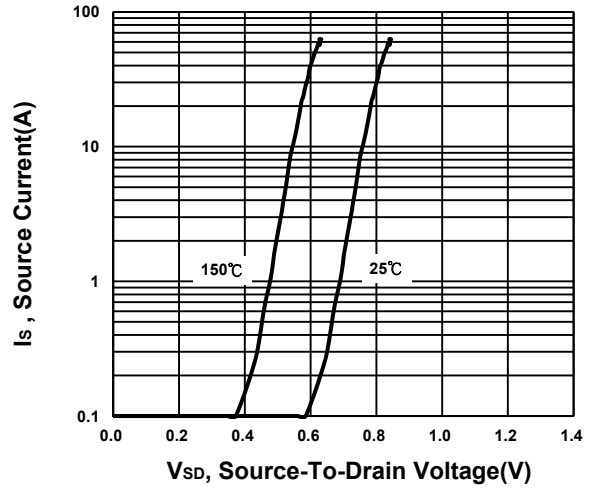
**On-Resistance VS Drain Current**



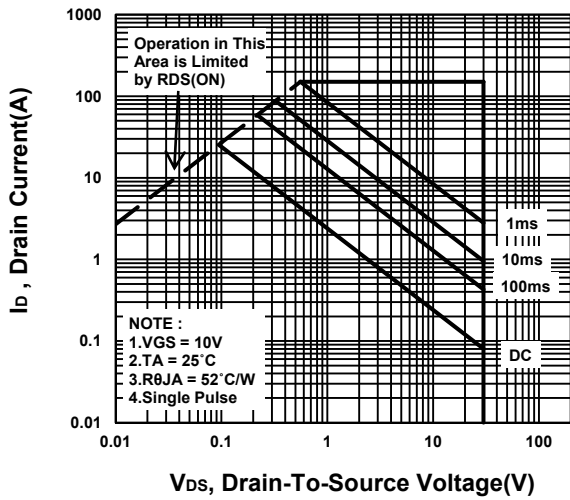
**On-Resistance VS Temperature**



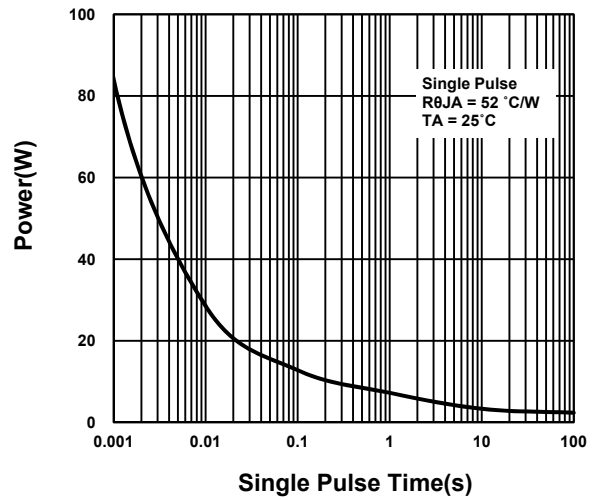
**Source-Drain Diode Forward Voltage**



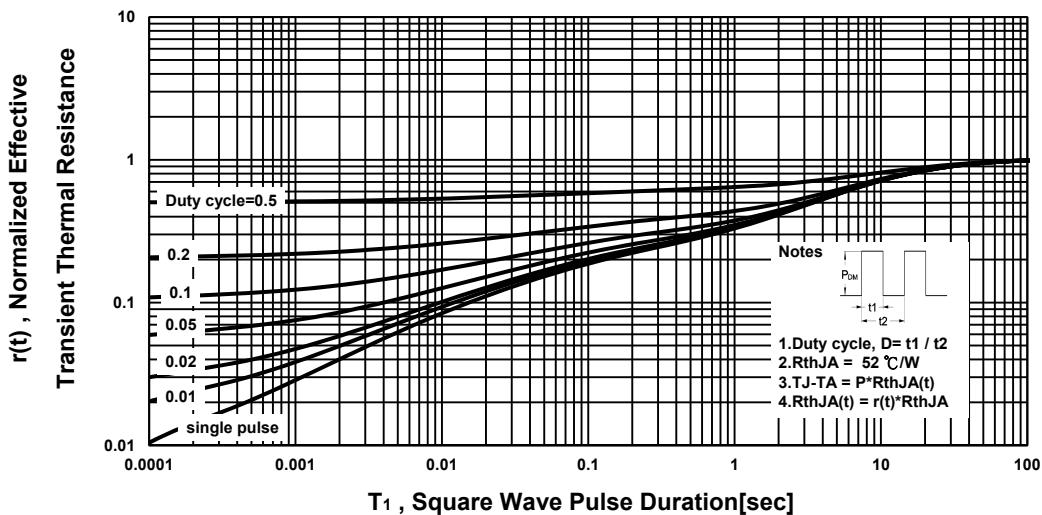
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**

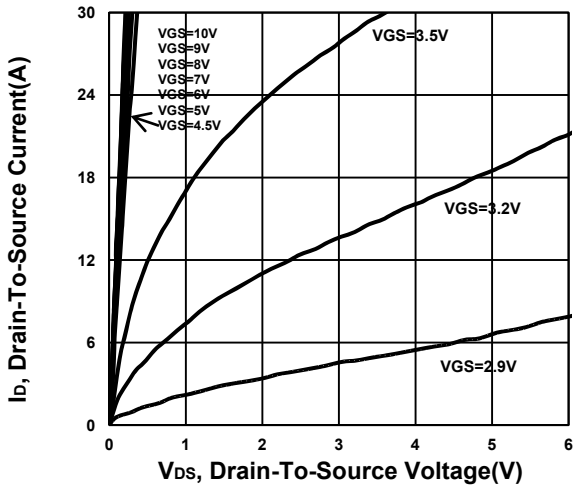


**Transient Thermal Response Curve**

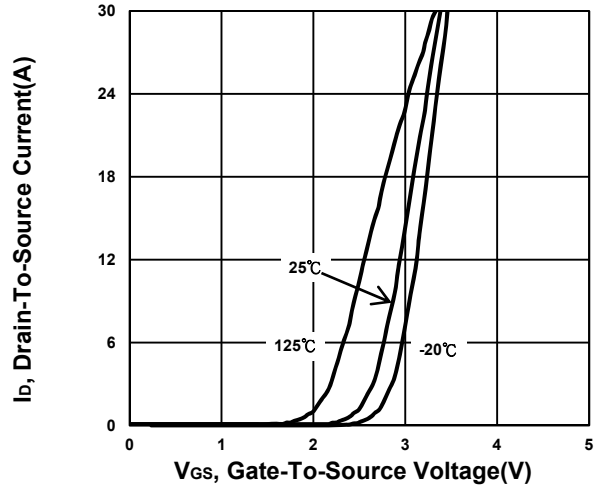


**Q1**

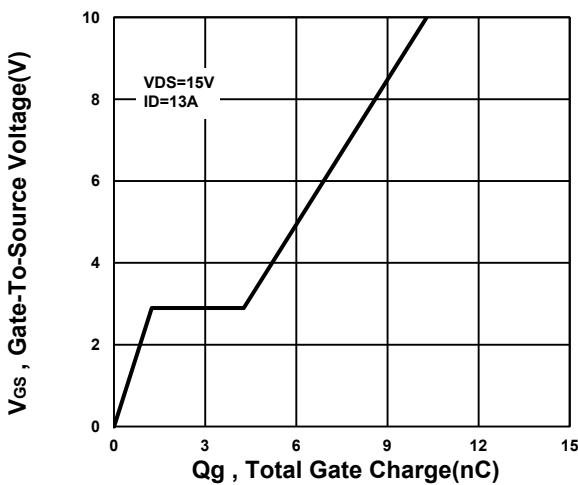
**Output Characteristics**



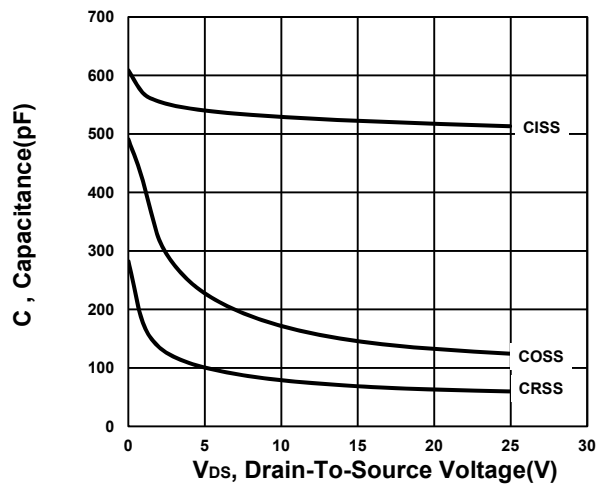
**Transfer Characteristics**



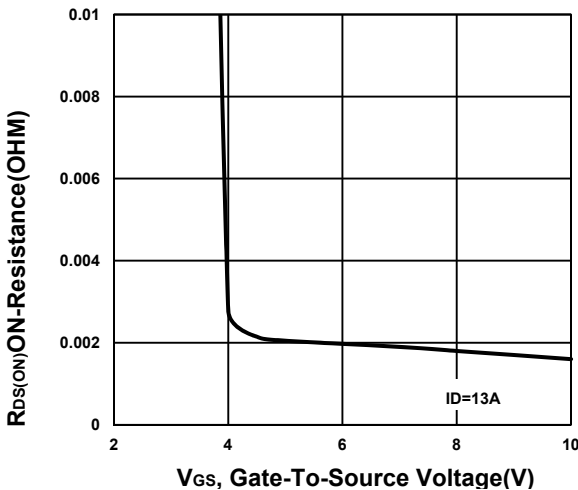
**Gate charge Characteristics**



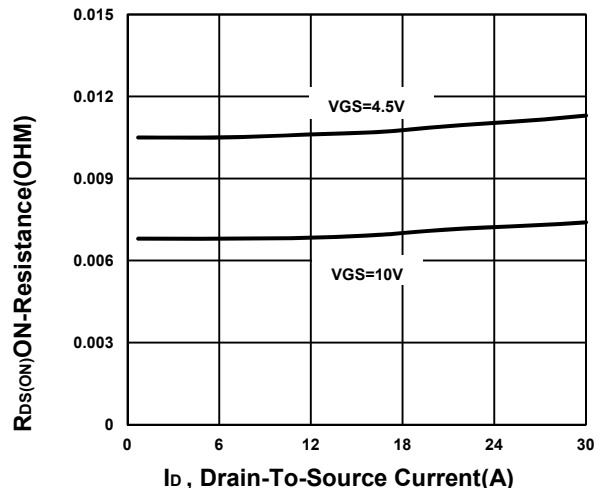
**Capacitance Characteristic**



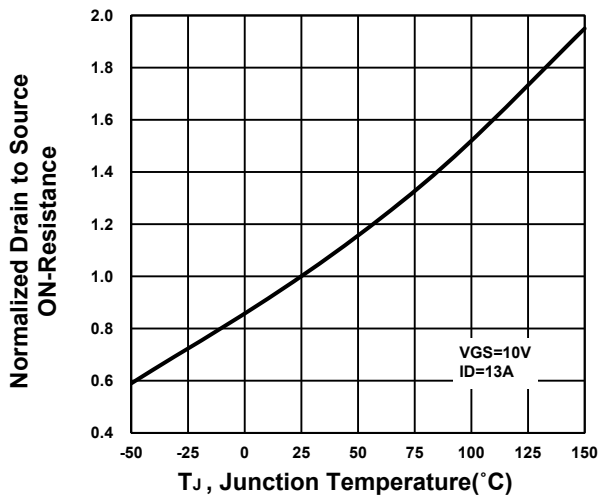
**On-Resistance VS Gate-To-Source**



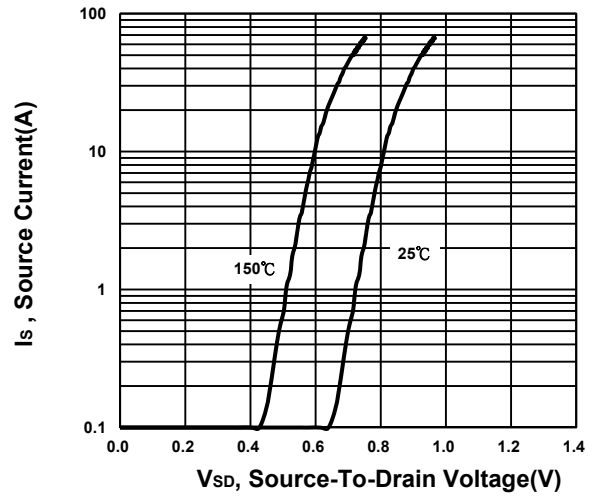
**On-Resistance VS Drain Current**



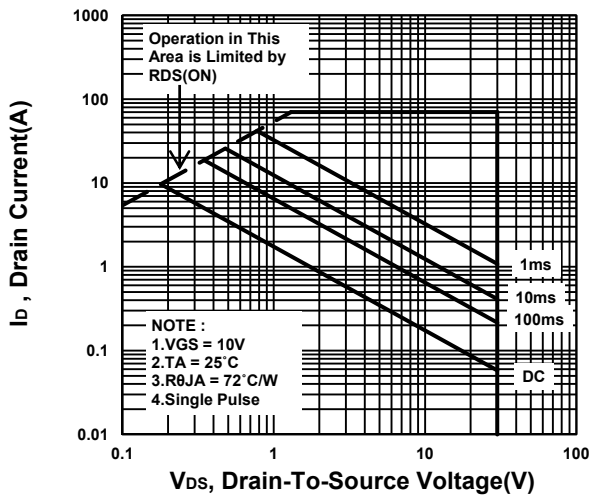
**On-Resistance VS Temperature**



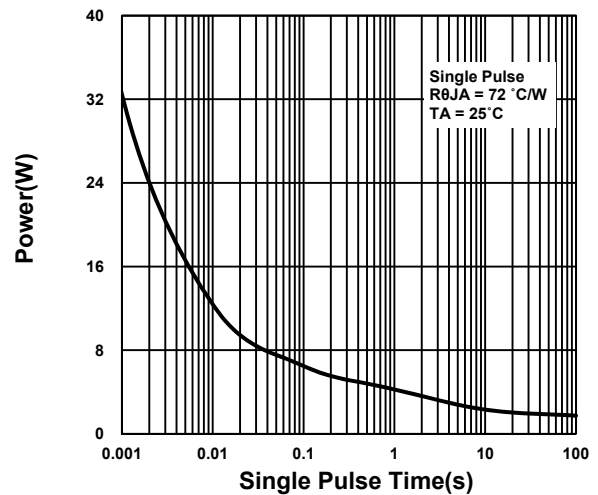
**Source-Drain Diode Forward Voltage**



**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**

