NIKO-SEM

N-Channel Enhancement Mode Field Effect Transistor

PG2910BD

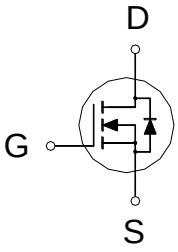
TO-252

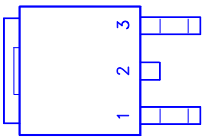
Halogen-Free & Lead-Free

RoHS Compliant

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
100V	29.8mΩ	27A





1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS (T_A = 25 ° C Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C = 25 ° C	I_D	27	A
	T_C = 100 ° C		17	
Pulsed Drain Current[1]		I_{DM}	74	
Avalanche Current		I_{AS}	4	
Avalanche Energy	L = 1mH	E_{AS}	8	mJ
Power Dissipation	T_C = 25 ° C	P_D	50	W
	T_C = 100 ° C		20	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	° C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.5	° C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

[1]Pulse width limited by maximum junction temperature.

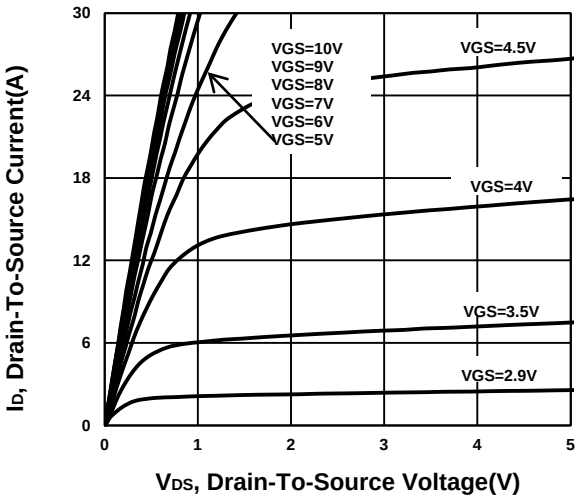
ELECTRICAL CHARACTERISTICS (T_J = 25 ° C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	1.4	2	3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0V, V_{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100V, V_{GS} = 0V			1	μA
		V_{DS} = 100V, V_{GS} = 0V, T_J = 125 ° C			100	

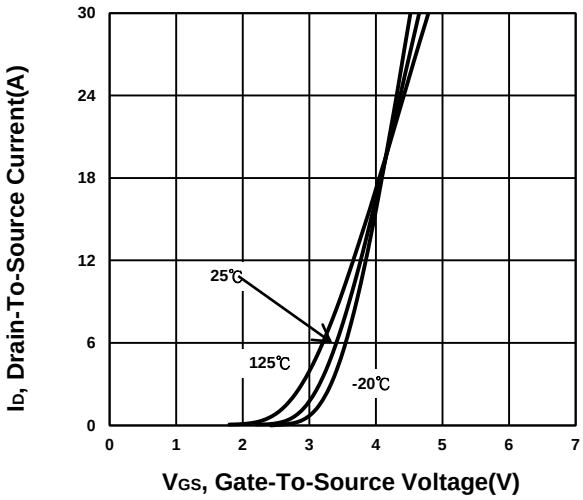
Drain-Source On-State Resistance[1]	$R_{DS(ON)}$	V_{GS} = 4.5V, I_D = 10A		40.3	49.8	mΩ
		V_{GS} = 10V, I_D = 14A		24.3	29.8	
Forward Transconductance[1]	g_{fs}	V_{DS} = 5V, I_D = 14A		19		S
DYNAMIC						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 50V, f = 1MHz		780		pF
Output Capacitance	C_{oss}			77		
Reverse Transfer Capacitance	C_{rss}			9.6		
Gate Resistance	Rg	V_{GS} = 0V, V_{DS} = 0V, f = 1MHz		1.1		Ω
Total Gate Charge[2]	Q_g (V_{GS} = 10V)	V_{DS} =50V,I_D = 14A		14.6		nC
	Q_g (V_{GS} = 4.5V)			9		
Gate-Source Charge[2]	Q_{gs}			3.5		
Gate-Drain Charge[2]	Q_{gd}			4.9		
Turn-On Delay Time[2]	$t_{d(on)}$	V_{DD} = 50V, $I_D \cong$ 14A, V_{GS} = 10V, R_{GEN} =6Ω		10		nS
Rise Time[2]	t_r			40		
Turn-Off Delay Time[2]	$t_{d(off)}$			21		
Fall Time[2]	t_f			57		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I_S				27	A
Forward Voltage[1]	V_{SD}	I_F = 14A , V_{GS} = 0V			1.2	V
Reverse Recovery Time	t_{rr}	I_F = 14A , dI_F/dt= 100A/μs		25		nS
Reverse Recovery Charge	Q_{rr}			14		nC

[1]Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.
[2]Independent of operating temperature.

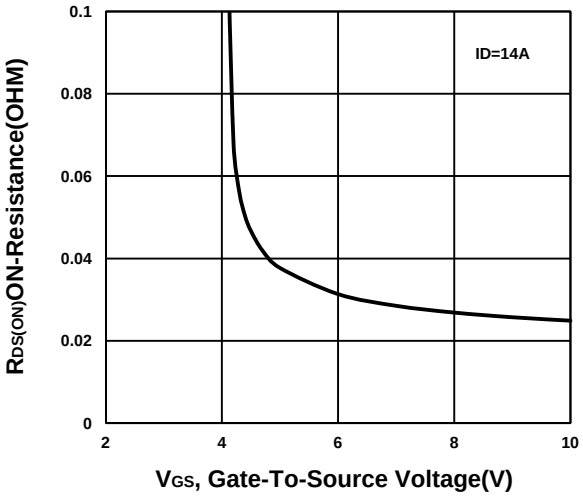
Output Characteristics



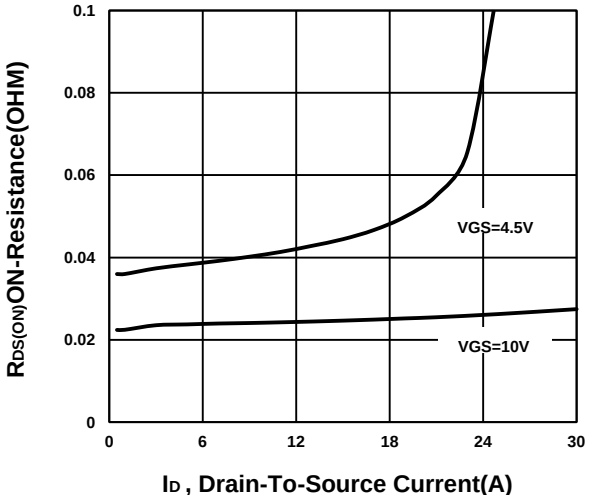
Transfer Characteristics



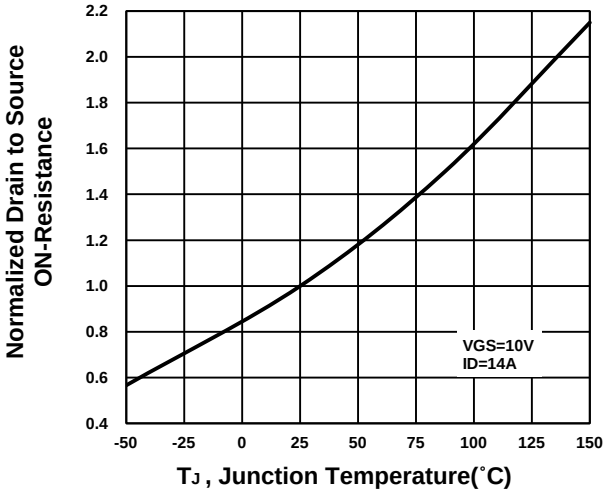
On-Resistance VS Gate-To-Source Voltage



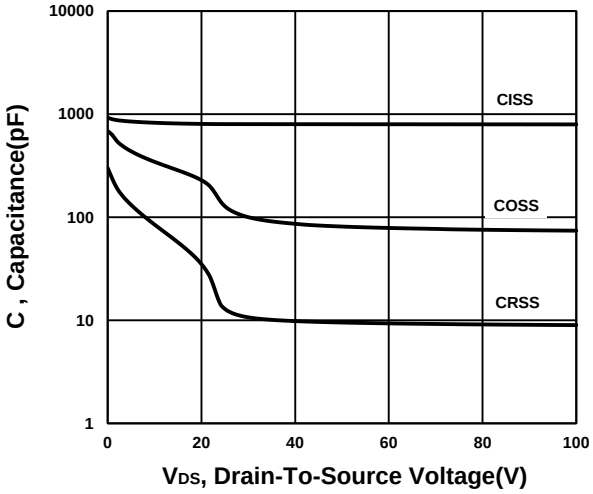
On-Resistance VS Drain Current



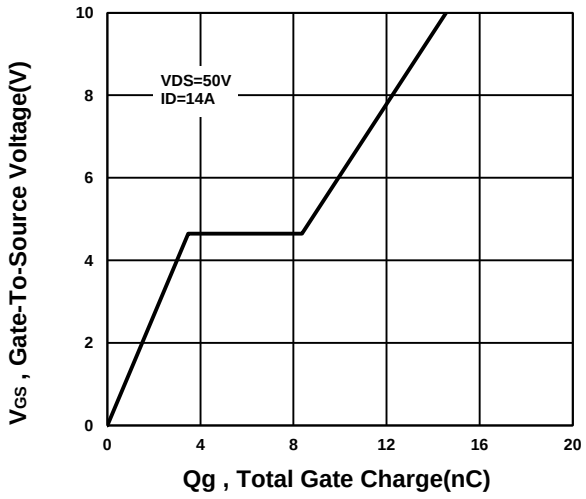
On-Resistance VS Temperature



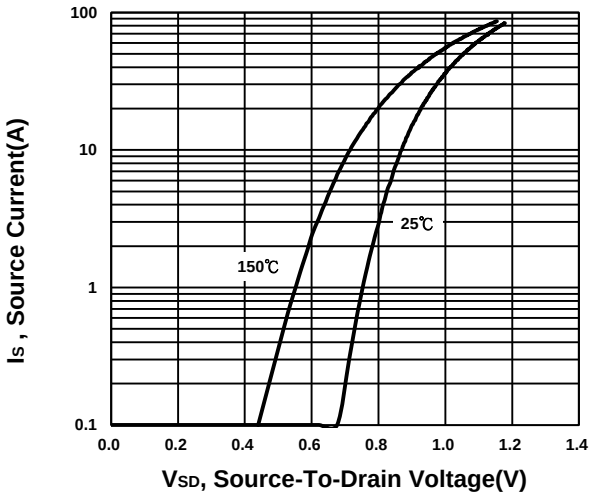
Capacitance Characteristic



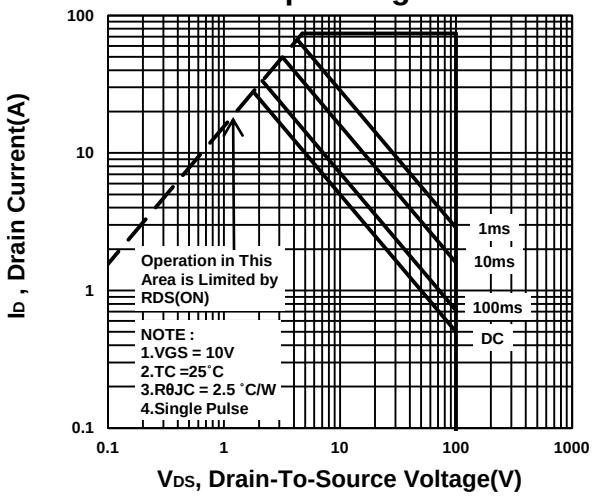
Gate charge Characteristics



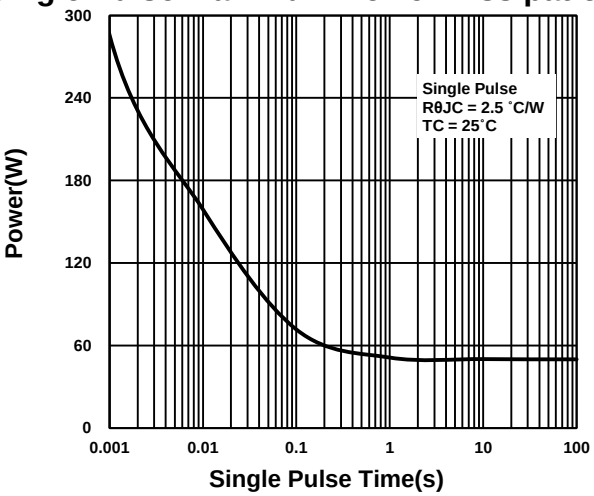
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve