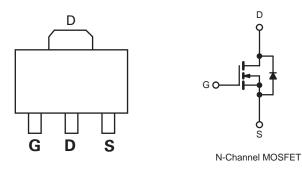
DTC9058 www.din-tek.jp

N-Channel 100 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY							
V _{DS} (V)	R _{DS(on)} (Ω) Max.	I _D (A) ^a	Q _g (Typ.)				
	0.122 at V _{GS} = 10 V	3.1					
100	0.134 at V _{GS} = 6 V	2.9	2.9 nC				
	0.154 at V _{GS} = 4.5 V	2.6					



FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Material categorization:

APPLICATIONS

- DC/DC Converters / Boost Converters
- Load Switch
- LED Backlighting in LCD TVs
- Power Management for Mobile Computing

Parameter		Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage	V _{GS}	± 20	v	
	T _C = 25 °C		3.1	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		2.5	
	T _A = 25 °C	I _D	2.2 ^{b,c}	
	T _A = 70 °C		1.8 ^{b,c}	А
Pulsed Drain Current (t = 300 µs)	I _{DM}	9.3	~ ^	
Continuous Source-Drain Diode Current	T _C = 25 °C	la la	2.1	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	3	
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	0.45	mJ
	T _C = 25 °C		2.5	
Maximum Power Dissipation	T _C = 70 °C	P _D	1.6	w
	T _A = 25 °C	'D	1.25 ^{b, c}	~ ~ ~
	T _A = 70 °C		0.8 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Typical	Maximum	Unit				
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	75	100	°C/W			
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	40	50	0/11			

Notes:

a. Based on T_C = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under steady state conditions is 166 $^{\circ}\text{C/W}.$



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static			1	1		
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			59		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 4.8		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.2		3	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 100 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5$ V, V_{GS} = 10 V	5			А
		V _{GS} = 10 V, I _D = 2 A		0.122	0.135	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 6 \text{ V}, \text{ I}_{D} = 1 \text{ A}$		0.134	0.148	
	(,	$V_{GS} = 4.5 \text{ V}, I_{D} = 1 \text{ A}$		0.154	0.170	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 20 \text{ V}, I_D = 2 \text{ A}$		5		S
Dynamic ^b						
Input Capacitance	C _{iss}		İ.	196		l –
Output Capacitance	C _{oss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		67		pF
Reverse Transfer Capacitance	C _{rss}			14		
		V _{DS} = 50 V, V _{GS} = 10 V, I _D = 2.2 A		5.2	10.4	+
Total Gate Charge	Q _g			2.9	5.8	nC
Gate-Source Charge	Q _{gs}	$V_{DS} = 50$ V, $V_{GS} = 4.5$ V, $I_{D} = 2.2$ A		1		
Gate-Drain Charge	Q _{gd}			1.4		
Gate Resistance	R _g f = 1 MHz		0.9	4.3	8.6	Ω
Turn-On Delay Time	t _{d(on)}			40	60	-
Rise Time	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{1} = 27.7 \Omega$		68	102	
Turn-Off Delay Time	t _{d(off)}	$I_{\rm D}$ = 1.8 A, $V_{\rm GEN}$ = 4.5 V, $R_{\rm q}$ = 1 Ω		14	21	
Fall Time	t _f	C C		20	30	
Turn-On Delay Time	t _{d(on)}			8	16	- ns - -
Rise Time	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{1} = 27.7 \Omega$		10	20	
Turn-Off Delay Time	t _{d(off)}	$I_{D} = 1.8 \text{ A}, V_{GEN} = 10 \text{ V}, R_{g} = 1 \Omega$		10	20	
Fall Time	t _f	Č.		7	14	
Drain-Source Body Diode Characteristi						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 2.1	
Pulse Diode Forward Current ^a	I _{SM}				- 9.3	A
Body Diode Voltage	V _{SD}	I _S = 1.8 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	-		23	35	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 1.8 A, dl/dt = 100 A/μs,		21	32	nC
Reverse Recovery Fall Time	t _a	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$		17	-	
Reverse Recovery Rise Time	t _b	-		6		ns

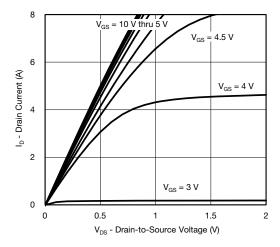
Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

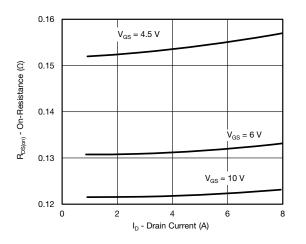
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

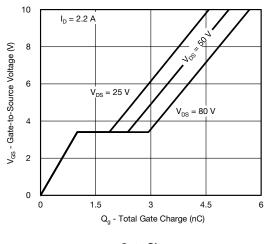




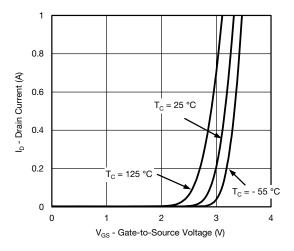
Output Characteristics



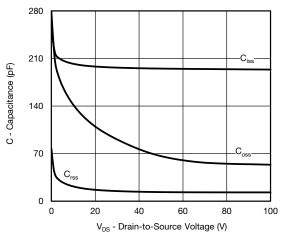
On-Resistance vs. Drain Current and Gate Voltage



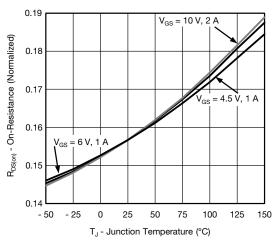
Gate Charge



Transfer Characteristics



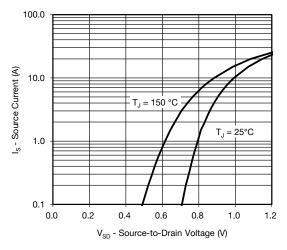




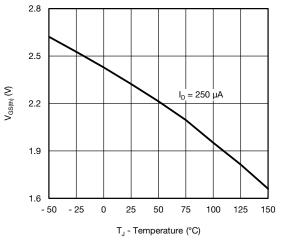
On-Resistance vs. Junction Temperature

3

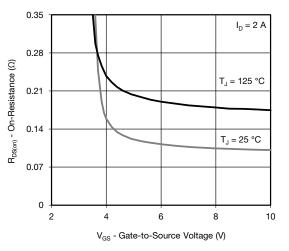




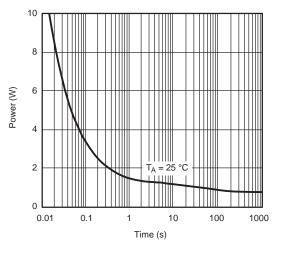
Source-Drain Diode Forward Voltage



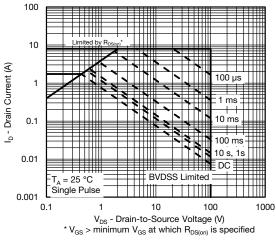
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

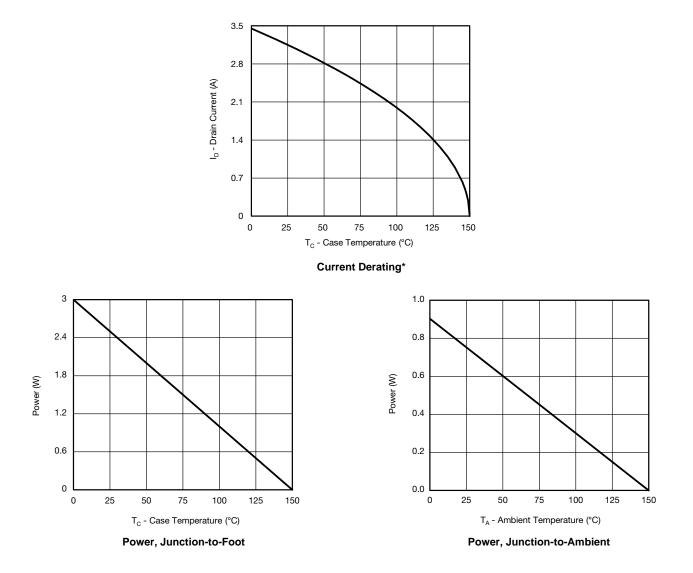






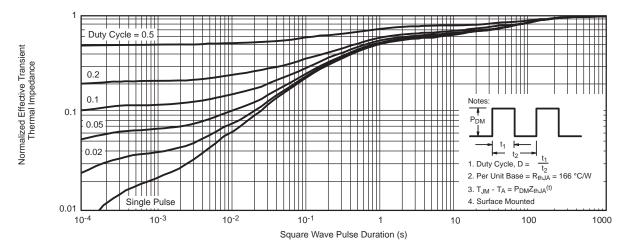
Safe Operating Area



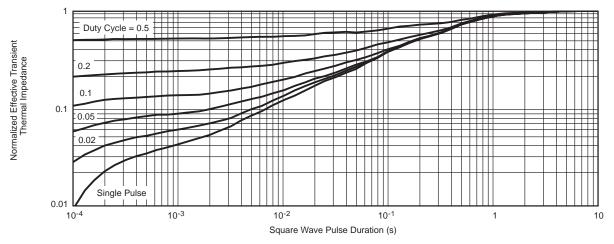


* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





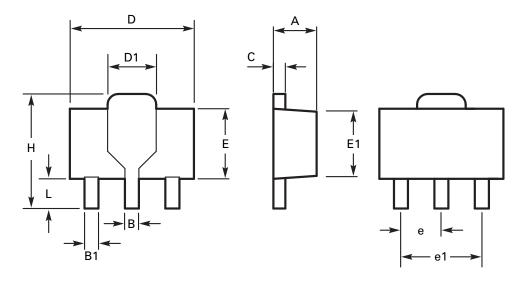
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



Package outline - SOT89



DIM	Millim	neters	Inc	Inches DIM Millimeters Inche		Millimeters		hes	
	Min	Max	Min	Мах		Min	Мах	Min	Мах
Α	1.40	1.60	0.550	0.630	E	2.29	2.60	0.090	0.102
В	0.44	0.56	0.017	0.022	E1	2.13	2.29	0.084	0.090
B1	0.36	0.48	0.014	0.019	е	1.50 BSC		0.059 BSC	
С	0.35	0.44	0.014	0.017	e1	3.00 BSC		0.118 BSC	
D	4.40	4.60	0.173	0.181	Н	3.94	4.25	0.155	0.167
D1	1.62	1.83	0.064	0.072	L	0.89	1.20	0.035	0.047

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches



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