

# N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ)		
30	0.0021 at V <sub>GS</sub> = 10 V	120	92 nC		
30	0.0029 at V <sub>GS</sub> = 4.5 V	100	92 110		

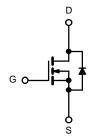
### **FEATURES**

- **DT-Trench Power MOSFET**
- 100 % R<sub>g</sub> and UIS Tested
   Compliant to RoHS Directive 2011/65/EU



### **APPLICATIONS**

- OR-ing
- Server
- DC/DC



N-Channel MOSFET

# TO-220AB G D S

Top View

<b>ABSOLUTE MAXIMUM RATING</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ss otherwise not	ted)	
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	V	
Gate-Source Voltage	V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		120 <sup>a, e</sup>	
Continuous Drain Current (T. – 175 °C)	T <sub>C</sub> = 70 °C		98 <sup>e</sup>	
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	35.8 <sup>b, c</sup>	A
	T <sub>A</sub> = 70 °C		27 <sup>b, c</sup>	
Pulsed Drain Current	I <sub>DM</sub>	360		
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	39	
Single Pulse Avalanche Energy	L=0.1 IIII	E <sub>AS</sub>	375	mJ
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I.	90 <sup>a, e</sup>	A
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3.13 <sup>b, c</sup>	
	T <sub>C</sub> = 25 °C		250 <sup>a</sup>	
Manianum Danian Dinain ation	T <sub>C</sub> = 70 °C	ь	175	10/
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.75 <sup>b, c</sup>	W
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>	
Operating Junction and Storage Temperature R	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Тур.	Max.	Unit		
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 sec	R <sub>thJA</sub>	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.5	0.6	*C/vv	

### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.

- b. Surface motived of 1. X.1.1144 board.
  c. t = 10 sec.
  d. Maximum under steady state conditions is 90 °C/W.
  e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	ΔVps/Tμ		35		m\//°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_{D} = 250 \mu\text{A}$		- 7.5		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0		3.0	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta na Dunin Commant	1	$V_{DS} = 24 \text{ V } V_{GS} = 0 \text{ V}$	1		1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24 V V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α	
	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 38.8 A		0.0021	0.0023		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 37 \text{ A}$	0.0029 0.003		0.0032	Ω	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 38.8 \text{ A}$		160		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			6201		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1725			
Reverse Transfer Capacitance	C <sub>rss</sub>			970			
Total Cata Chausa	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 38.8 \text{ A}$		171	257	nC	
Total Gate Charge				81.5	123		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 28.8 \text{ A}$		34			
Gate-Drain Charge	$Q_{gd}$			29			
Gate Resistance	$R_g$	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			18	27		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		11	17		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 24$ A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		70	105		
Fall Time	t <sub>f</sub>			10	15	20	
Turn-On Delay Time	t <sub>d(on)</sub>			55	83	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.67 $\Omega$		180	270	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 22.5$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		55	83		
Fall Time	t <sub>f</sub>			12	18		
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			120	۸	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				360	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns	
Body Diode Reverse Recovery Charge	ry Charge Q <sub>rr</sub>			70.2	105	nC	
$t_a$ $t_b$		$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		27		ns	
Reverse Recovery Rise Time	t <sub>b</sub>	_		25			

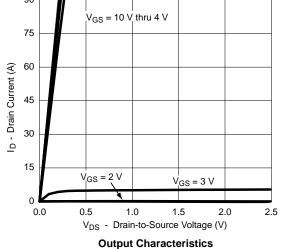
### Notes:

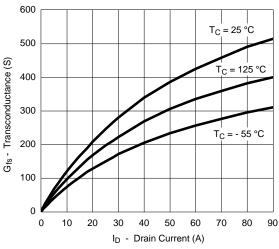
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

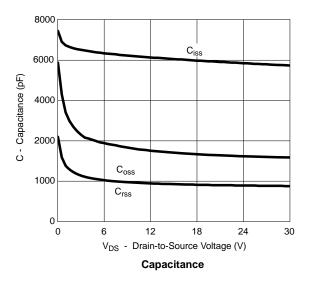


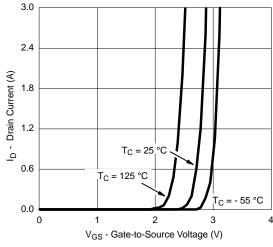
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



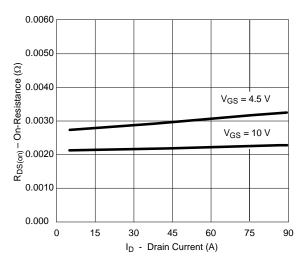


**Transconductance** 

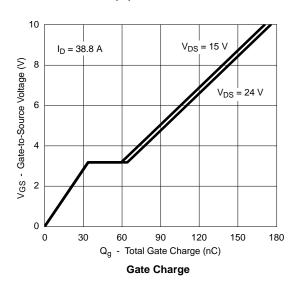




**Transfer Characteristics** 



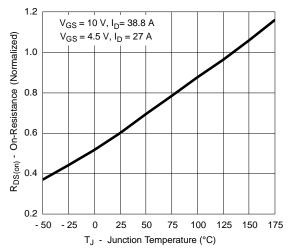
R<sub>DS(on)</sub> vs. Drain Current



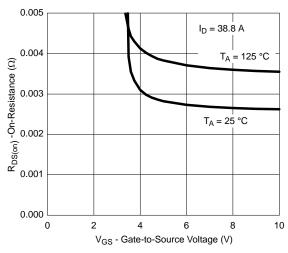
3



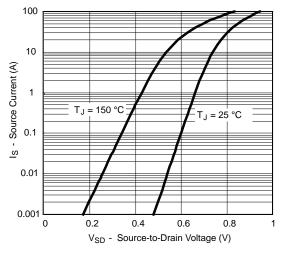
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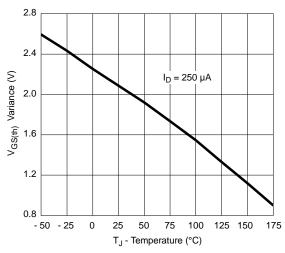
On-Resistance vs. Junction Temperature



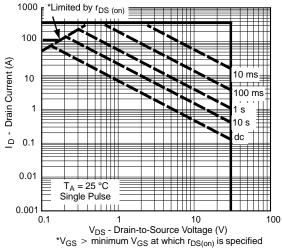
 $R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



Forward Diode Voltage vs. Temperature



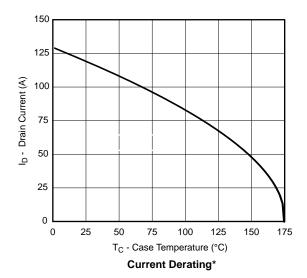
Threshold Voltage

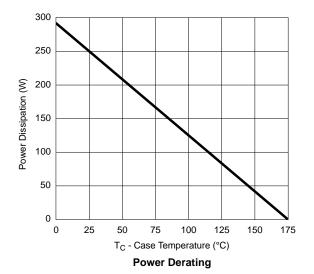


Safe Operating Area, Junction-to-Ambient

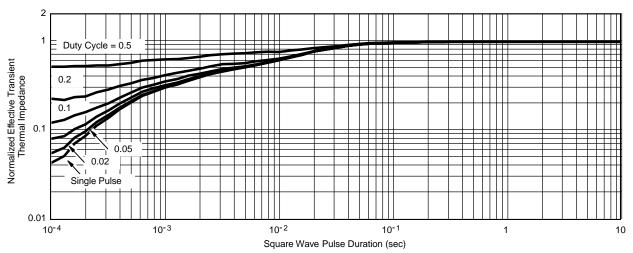


# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





\*The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

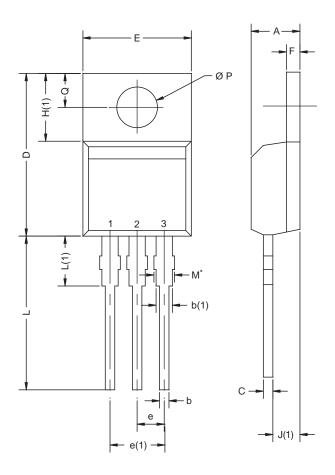


Normalized Thermal Transient Impedance, Junction-to-Case





# **TO-220AB**



	MILLIM	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12					

DWG: 5471

### Notes

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM





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