N-Channel 80 V (D-S) Super Junction Power MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (Ω) MAX.	I_D (A)	Q_g (TYP.)
80	0.0021 at V_{GS} = 10 V	200	168 nC
	0.0027 at V_{GS} = 4.5 V	180	

FEATURES

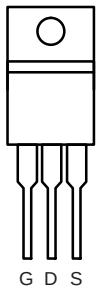
- Maximum 175 °C junction temperature
- 100 % R_g and UIS tested

RoHS COMPLIANT

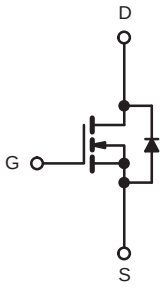
APPLICATIONS

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Battery management

TO-220AB



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	200	A
	T_C = 125 °C		140	
Pulsed Drain Current (t = 100 µs)		I_{DM}	800	
Avalanche Current	L = 0.1 mH	I_{AS}	190	
Single Avalanche Energy [a]		E_{AS}	1925	mJ
Maximum Power Dissipation [a]	T_C = 25 °C	P_D	300 [b]	W
	T_C = 125 °C		125 [b]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) [c]	R_{thJA}	2.1	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.5	

Notes

a. Duty cycle ≤ 1 %.
b. See SOA curve for voltage derating.
c. When mounted on 1" square PCB (FR4 material).

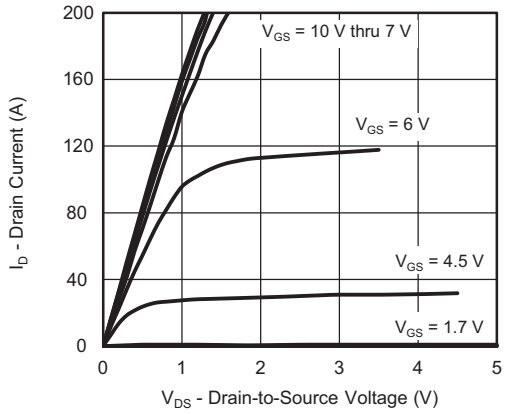
SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	80	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1	-	3	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 80 V, V_{GS} = 0 V	-	-	1	µA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 125 °C	-	-	100	
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 175 °C	-	-	2	mA
On-State Drain Current [a]	$I_{D(on)}$	V_{DS} ≥ 10 V, V_{GS} = 10 V	200	-	-	A
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 30 A	-	0.0021	0.0027	Ω
		V_{GS} = 4.5 V, I_D = 30 A	-	0.0028	0.0036	
Forward Transconductance [a]	g_{fs}	V_{DS} = 15 V, I_D = 30 A	-	90	-	S
Dynamic [b]						
Input Capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 50 V, f = 1 MHz	-	10500	-	pF
Output Capacitance	C_{oss}		-	1650	-	
Reverse Transfer Capacitance	C_{rss}		-	75	-	
Total Gate Charge [c]	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 30 A	-	168		nC
Gate-Source Charge [c]	Q_{gs}		-	55	-	
Gate-Drain Charge [c]	Q_{gd}		-	23	-	
Gate Resistance	R_g	f = 1 MHz		2.3		Ω
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = 50 V, R_L = 1.67 Ω I_D ≅ 30 A, V_{GEN} = 10 V, R_g = 1 Ω	-	28	-	ns
Rise Time [c]	t_r		-	75	-	
Turn-Off Delay Time [c]	$t_{d(off)}$		-	88	-	
Fall Time [c]	t_f		-	32	-	
Drain-Source Body Diode Ratings and Characteristics [b] (T_C = 25 °C)						
Pulsed Current (t = 100 µs)	I_{SM}		-	-	800	A
Forward Voltage [a]	V_{SD}	I_F = 30 A, V_{GS} = 0 V	-	0.7	1.2	V
Reverse Recovery Time	t_{rr}	I_F = 30 A, di/dt = 100 A/µs	-	118	-	ns
Peak Reverse Recovery Charge	$I_{RM(REC)}$		-	5	10	A
Reverse Recovery Charge	Q_{rr}		-	305	-	µC

Notes

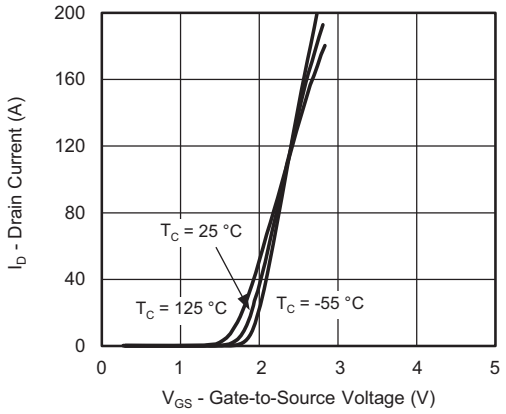
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

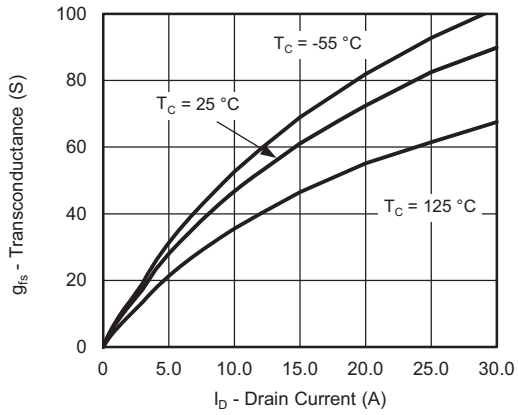
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



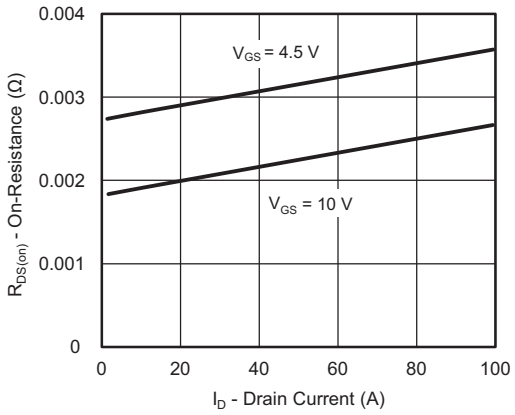
Output Characteristics



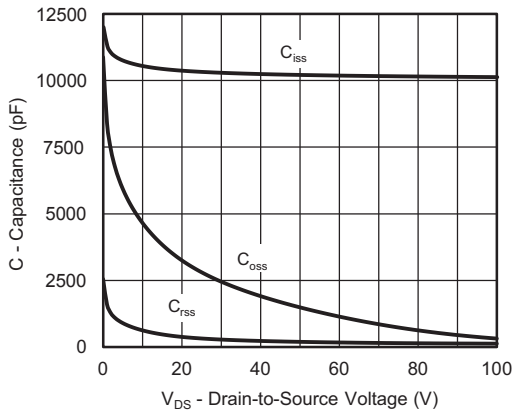
Transfer Characteristics



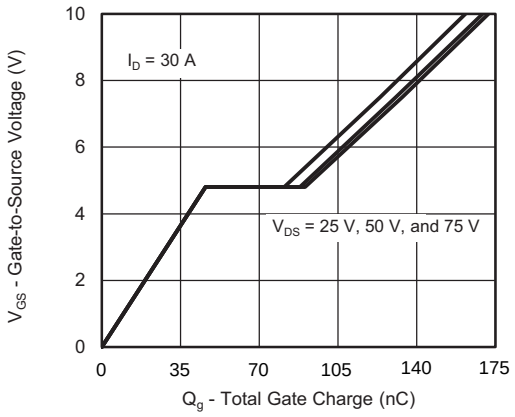
Transconductance



On-Resistance vs. Drain Current

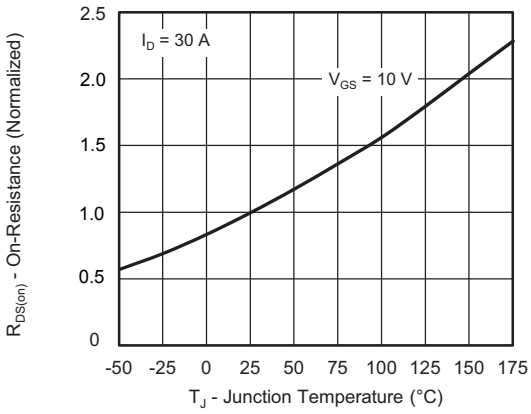


Capacitance

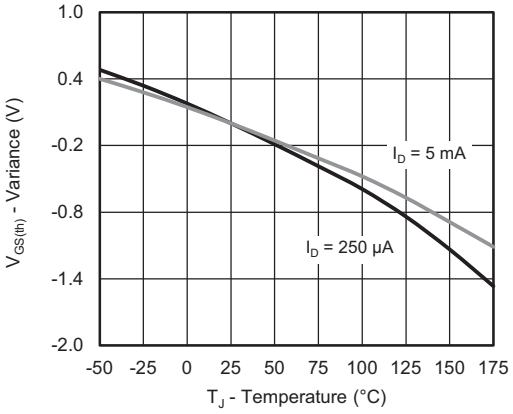


Gate Charge

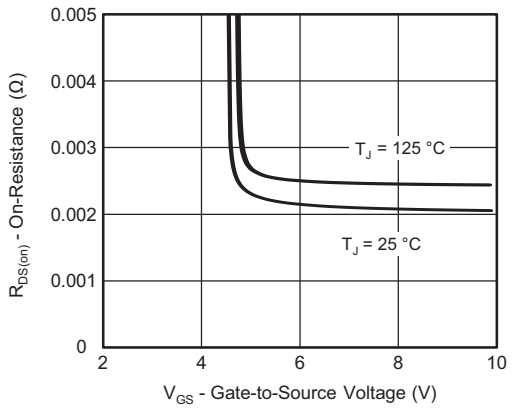
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



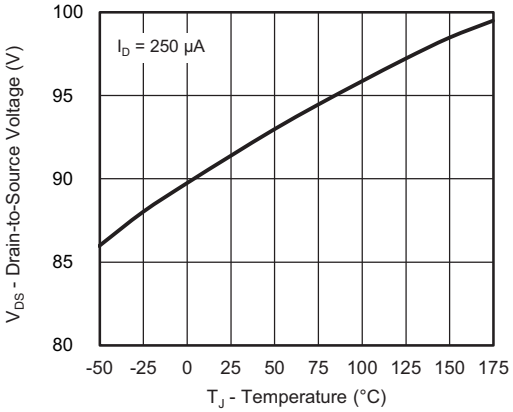
On-Resistance vs. Junction Temperature



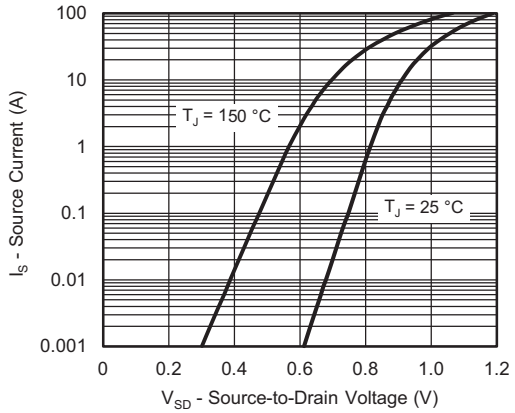
Threshold Voltage



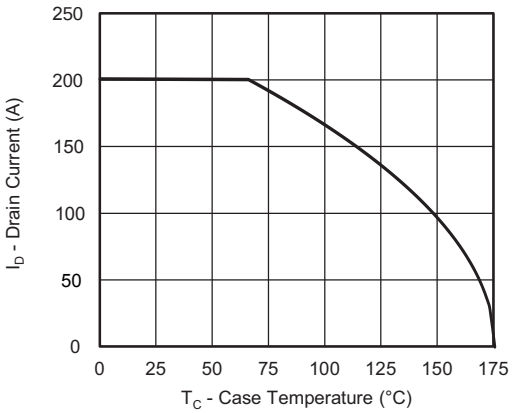
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

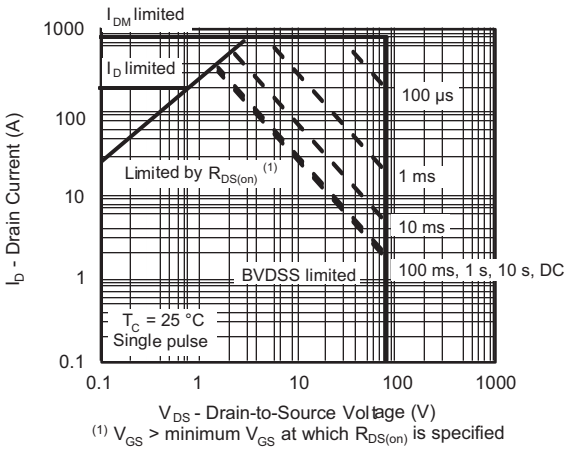


Source Drain Diode Forward Voltage

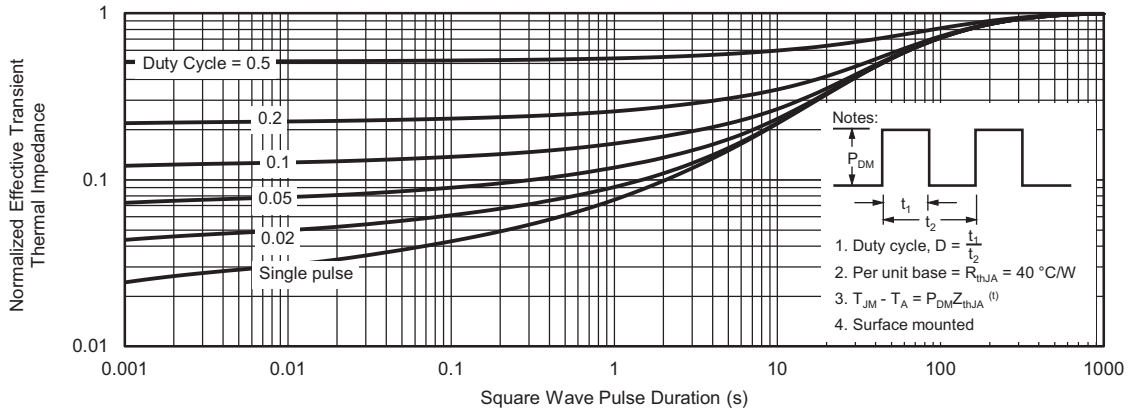


Current De-Rating

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

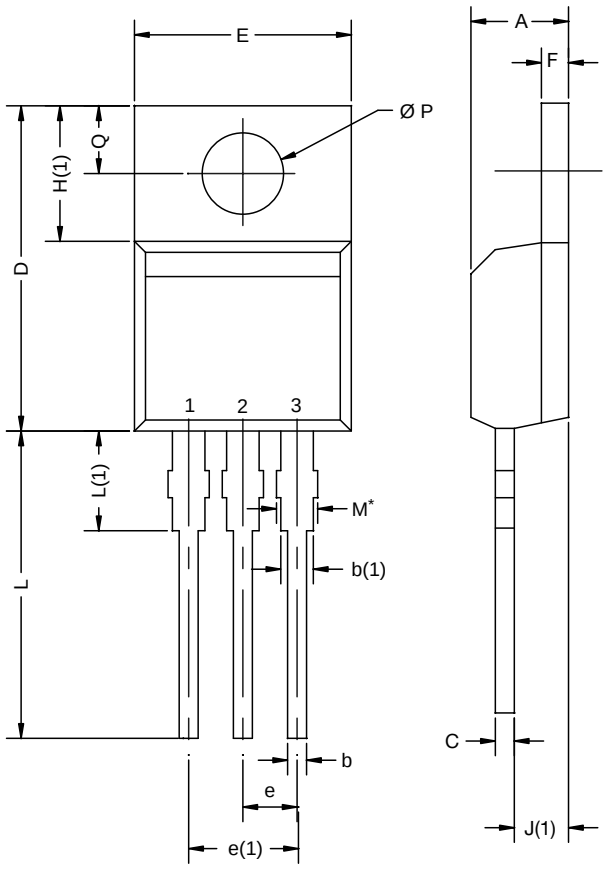


Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

TO-220AB



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.25	4.65	0.167	0.183
b	0.69	1.01	0.027	0.040
b(1)	1.20	1.73	0.047	0.068
c	0.36	0.61	0.014	0.024
D	14.85	15.49	0.585	0.610
E	10.04	10.51	0.395	0.414
e	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.09	6.48	0.240	0.255
J(1)	2.41	2.92	0.095	0.115
L	13.35	14.02	0.526	0.552
L(1)	3.32	3.82	0.131	0.150
Ø P	3.54	3.94	0.139	0.155
Q	2.60	3.00	0.102	0.118
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471				

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM

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