

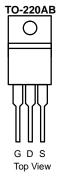
# N-Channel 60 V (D-S) Super Junction Power MOSFET

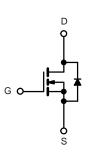
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a</sup>			
60	0.0026 at V <sub>GS</sub> = 10 V	150			
	0.0042 at V <sub>GS</sub> = 4.5 V	110			

#### **FEATURES**

- 175 °C Junction Temperature
- DT-Trench Power MOSFET
- Material categorization:







NI	Chr	nne	I N A	$\cap$	C			г
IV	-0116	mne	ΙVΙ	ı	0	_	_	

ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)							
Parameter	Symbol	Limit	Unit				
Gate-Source Voltage	$V_{GS}$	± 20	V				
Continuous Drain Current (T <sub>1</sub> = 175 °C) <sup>b</sup>	T <sub>C</sub> = 25 °C	I-	150				
Continuous Drain Current (1 <sub>J</sub> = 175°C) <sup>2</sup>	T <sub>C</sub> = 100 °C	l <sub>D</sub>	110 <sup>a</sup>				
Pulsed Drain Current	I <sub>DM</sub>	600	А				
Continuous Source Current (Diode Conduction)	I <sub>S</sub>	150 <sup>a</sup>					
Avalanche Current	I <sub>AS</sub>	110					
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E <sub>AS</sub>	750	mJ			
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	210	W			
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	3.5 <sup>b</sup> , 9.2 <sup>b, c</sup>	VV			
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C				

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Mariana lugation to Ambient	t ≤ 10 sec	R <sub>thJA</sub>	13	18	°C/W		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	'`thJA	35	45			
Maximum Junction-to-Case		R <sub>thJC</sub>	0.8	1.0			

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c.  $t \le 10 \text{ s}$ .



<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25	°C, unless o	otherwise noted)					
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit	
Static	•			•			
Drain-Source Breakdown Voltage	lown Voltage $V_{DS}$ $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$ 60		60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		2.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ	
		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	150			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0026	0.0032	Ω	
5 1 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	В	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0032	0.0041		
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		0.0036	0.0048		
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A		0.0042	0.0050		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 48 V, I <sub>D</sub> = 20 A		50		S	
Dynamic							
Input Capacitance	C <sub>iss</sub>			3950		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 48 \text{ V}, f = 1 \text{ MHz}$		670			
Reverse Transfer Capacitance	C <sub>rss</sub>			23			
Total Gate Charge <sup>c</sup>	$Q_g$			67	78		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 48 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$		12		nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			8.5			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 48 \text{ V}, R_{L} = 0.6 \Omega$		5	15	no	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		55	70	- ns -	
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20		
Source-Drain Diode Ratings and Ch	aracteristics (	T <sub>C</sub> = 25 °C)					
Pulsed Current	I <sub>SM</sub>				150	Α	
Diode Forward Voltage	$V_{SD}$	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$		1	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs		45	100	ns	

#### Notes:

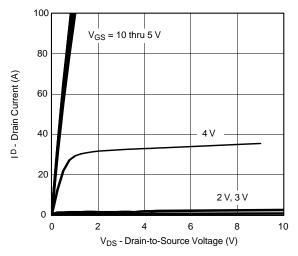
- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

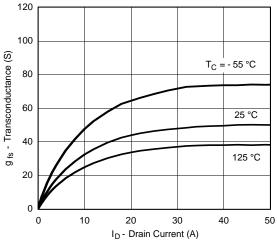




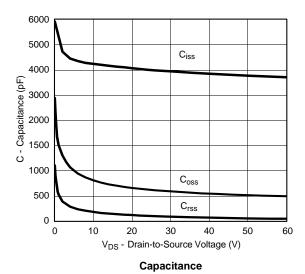
#### TYPICAL CHARACTERISTICS (25 °C unless noted)



#### **Output Characteristics**

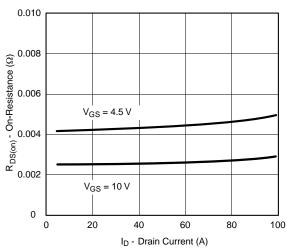


#### Transconductance

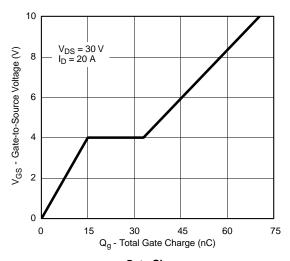


 $T_{C} = 125 \, ^{\circ}\text{C}$   $T_{C} = 125 \, ^{\circ}\text{C}$ 

#### **Transfer Characteristics**

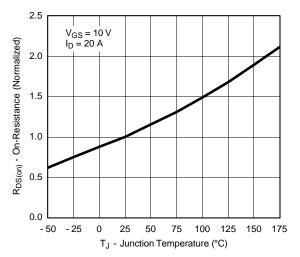


On-Resistance vs. Drain Current

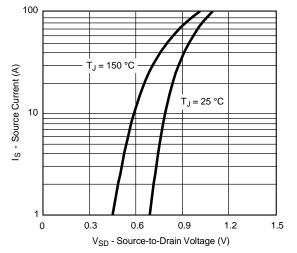


**Gate Charge** 

### TYPICAL CHARACTERISTICS (25 °C unless noted)

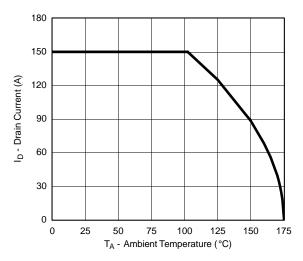


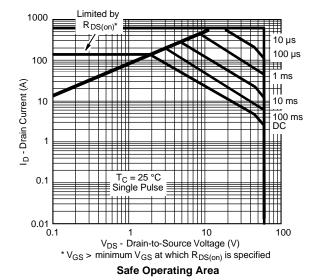
On-Resistance vs. Junction Temperature



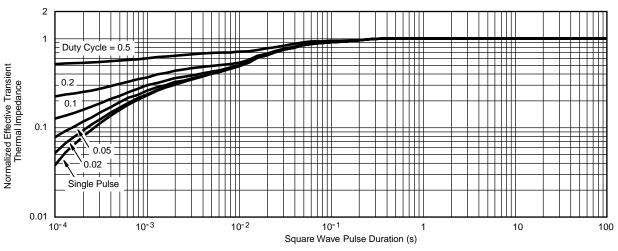
Source-Drain Diode Forward Voltage

#### **THERMAL RATINGS**





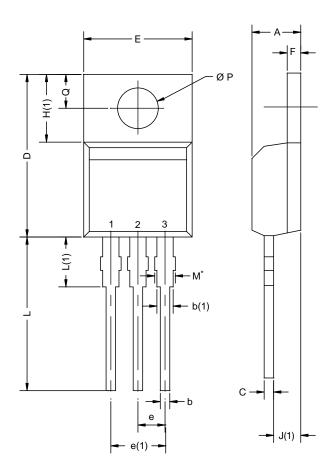
Maximum Drain Current vs. Ambient Temperature



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220AB**



	MILLIM	IETERS	INCHES			
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	4.25	4.65	0.167	0.183		
b	0.69	1.01	0.027	0.040		
b(1)	1.20	1.73	0.047	0.068		
С	0.36	0.61	0.014	0.024		
D	14.85	15.49	0.585	0.610		
E	10.04	10.51	0.395	0.414		
е	2.41	2.67	0.095	0.105		
e(1)	4.88	5.28	0.192	0.208		
F	1.14	1.40	0.045	0.055		
H(1)	6.09	6.48	0.240	0.255		
J(1)	2.41	2.92	0.095	0.115		
L	13.35	14.02	0.526	0.552		
L(1)	3.32	3.82	0.131	0.150		
ØΡ	3.54	3.94	0.139	0.155		
Q	2.60	3.00	0.102	0.118		
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471						

#### Notes

 $<sup>^{*}</sup>$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM





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