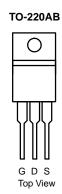
# N-Channel 100 V (D-S) Power MOSFET

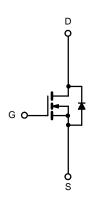
PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
100	$0.0085 \text{ at V}_{GS} = 10 \text{ V}$	100 <sup>a</sup>		

#### **FEATURES**

- DT-Trench Power MOSFET
- New Package with Low Thermal Resistance
- 100 % R<sub>g</sub> Tested







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>C</sub> = 25 °C, unless otherwise noted					
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	100	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20	v		
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	,	100 <sup>a</sup>		
Continuous Dialii Curient (1 j = 175 C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	81 <sup>a</sup>	A	
Pulsed Drain Current	I <sub>DM</sub>	300	7		
Avalanche Current		I <sub>AR</sub>	75		
Repetitive Avalanche Energy <sup>b</sup>	L = 0.1 mH	E <sub>AR</sub>	280	mJ	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	Б	375 <sup>c</sup>	W	
Maximum Fower Dissipation —	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.75	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Operating Junction and Storage Temperature	Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Limit	Unit			
Junction-to-Ambient	PCB Mount (TO-263) <sup>d</sup>	R <sub>thJA</sub>	40	°C/W			
Junction-to-Case (Drain)		R <sub>thJC</sub>	0.4	C/VV			

#### Notes:

- a. Package limited.
- b. Duty cycle ≤ 1 %.
- c. See SOA curve for voltage derating.
  d. When mounted on 1" square PCB (FR-4 material).

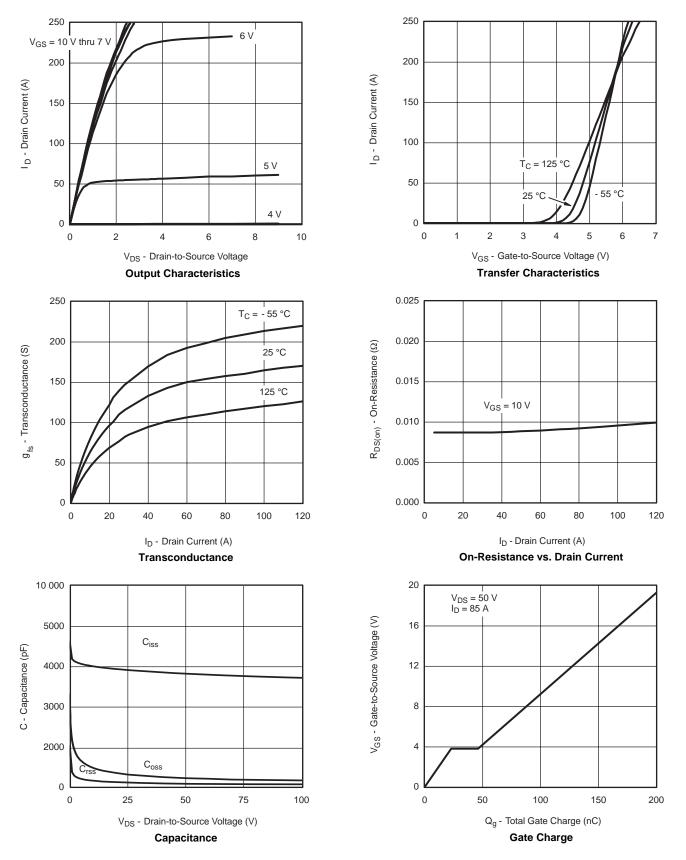
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250  \mu\text{A}$	100			4 V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	μΑ	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.0085	0.010		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.014	0.020	Ω	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.021	0.035		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S	
Dynamic <sup>b</sup>	•						
Input Capacitance	C <sub>iss</sub>			3900		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		650			
Reverse Transfer Capacitance	C <sub>rss</sub>			230			
Total Gate Charge <sup>c</sup>	Qg			110	160	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 85 \text{ A}$		24			
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			24			
Gate Resistance	R <sub>g</sub>		1.0		6.2	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			20	30		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_1 = 0.6 \Omega$		125	200	ns ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 85 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		55	85		
Fall Time <sup>c</sup>	t <sub>f</sub>			130	195		
Source-Drain Diode Ratings and Cha	aracteristics 7	「 <sub>C</sub> = 25 °C <sup>b</sup>		_			
Continuous Current	I <sub>S</sub>				110	۸	
Pulsed Current	I <sub>SM</sub>				240	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			70	140	ns	
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	I <sub>F</sub> = 50 A, dl/dt = 100 A/μs		5.5	10	Α	
Reverse Recovery Charge	Q <sub>rr</sub>	<b>⊣</b> ' '		0.19	0.35	μC	

#### Notes:

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

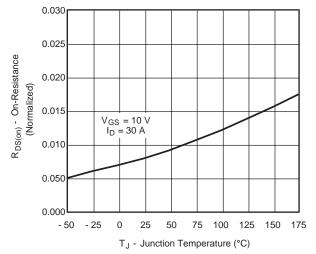
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

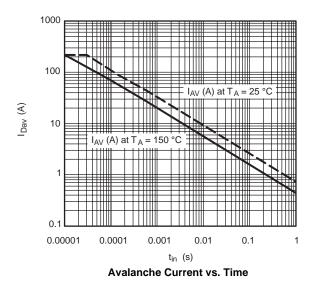




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

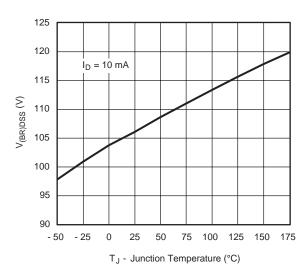


On-Resistance vs. Junction Temperature



100 Is - Source Current (A) T<sub>J</sub> = 150 °C  $T_J = 25$  °C 10 0 0.3 0.6 0.9 1.2 V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

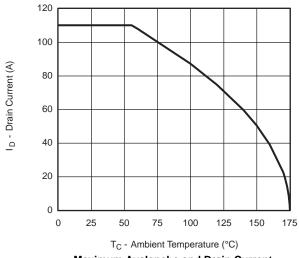


**Drain Source Breakdown** vs. Junction Temperature

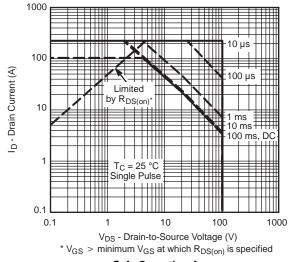


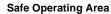


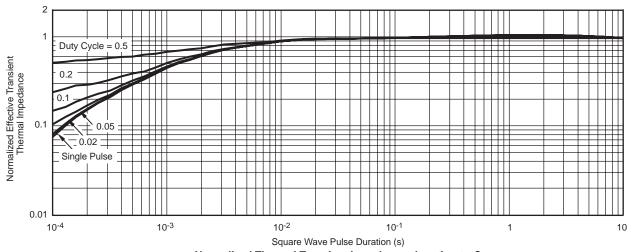
#### **THERMAL RATINGS**



**Maximum Avalanche and Drain Current** vs. Case Temperature



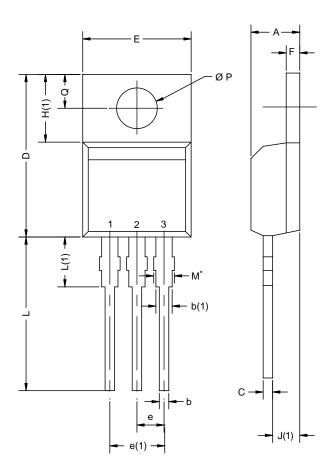




Normalized Thermal Transient Impedance, Junction-to-Case



# **TO-220AB**



	MILLIM	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12 DWG: 5471					

#### Notes

 $<sup>^{*}</sup>$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM





www.din-tek.jp

### **Disclaimer**

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Din-Tek Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Din-Tek"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Din-Tek makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Din-Tek disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Din-Tek's knowledge of typical requirements that are often placed on Din-Tek products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Din-Tek's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Din-Tek products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Din-Tek product could result in personal injury or death. Customers using or selling Din-Tek products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Din-Tek personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Din-Tek. Product names and markings noted herein may be trademarks of their respective owners.

## **Material Category Policy**

Din-Tek Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Din-Tek documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Din-Tek Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Din-Tek documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.