

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
100	0.030 at V _{GS} = 10 V	8.0		
100	0.045 at V _{GS} = 4.5 V	6.5		

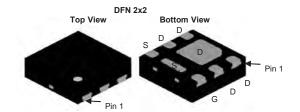
FEATURES

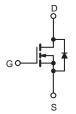
- **DT-Trench Power MOSFET**
- 100 % R_g Tested 100 % UIS Tested

COMPLIANT

APPLICATIONS

- · Primary Side Switch
- Synchronous Rectification





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V _{DS}	100	V			
Gate-Source Voltage	V _{GS}	± 20	v			
Continuous Drain Current (T _{.1} = 150 °C)	T _A = 25 °C	I _D	8.0			
Continuous Diam Curient (1) = 150°C)	T _A = 70 °C		5.2			
Pulsed Drain Current		I _{DM}	32	Α		
Continuous Source Current (Diode Conduction) ^a	I _S	8.0				
Maximum Power Dissipation ^a	T _A = 25 °C	D	4.5	W		
Maximum Tower Dissipation	T _A = 70 °C	- P _D	2.9			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature)b, c			260			

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^a	t ≤ 10 s	D	26	33	0C/M	
Maximum Junction-to-Amblem	Steady State	R _{thJA}	40	60	°C/W	

- a. Surface mounted on 1" x 1" FR4 board.
- b. The DFN2X2 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- c. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1		3	V	
Gate Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zoro Coto Voltago Proin Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 55 °C			5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	8			Α	
Drain-Source On-State Resistance ^a	В	V _{GS} = 10 V, I _D = 8 A		0.030	0.036	Ω	
Dialii-Source Oil-State Resistance	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 6 \text{ A}$		0.045	0.060	22	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 8 A		25		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 1 A, V _{GS} = 0 V		0.75	1.2	V	
Dynamic ^b						•	
Total Gate Charge	Qg			15	45		
Gate-Source Charge	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 8 \text{ A}$		2.9		nC	
Gate-Drain Charge	Q_{gd}			1.8		7	
Gate Resistance	R _g			1.5		Ω	
Turn-On Delay Time	t _{d(on)}			14			
Rise Time	t _r	$V_{DD} = 50 \text{ V}, R_{L} = 30 \Omega,$		12			
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		50		ns	
Fall Time	t _f			12		1	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 3.2 A, dl/dt = 100 A/μs		60		1	

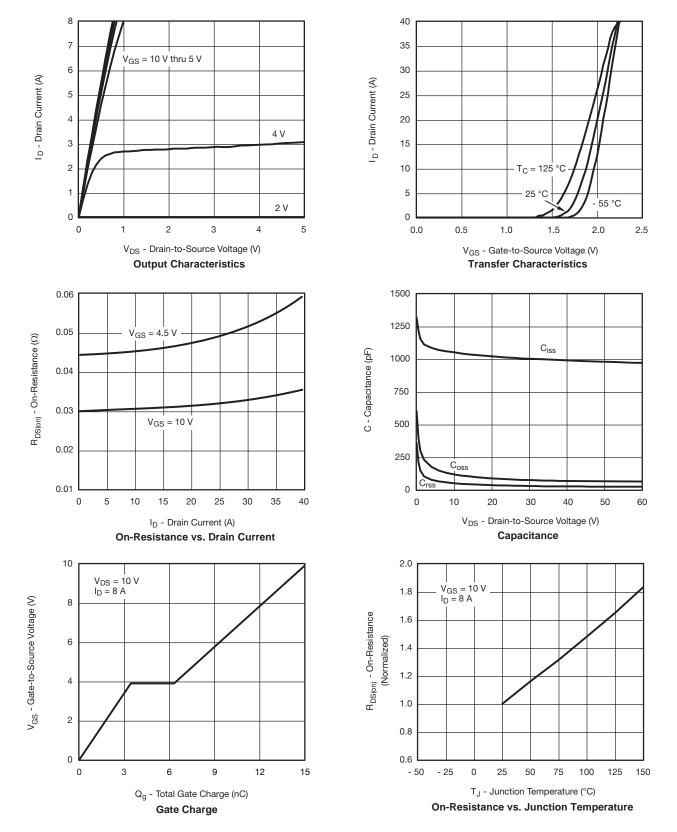
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

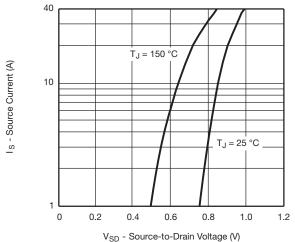
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

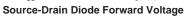


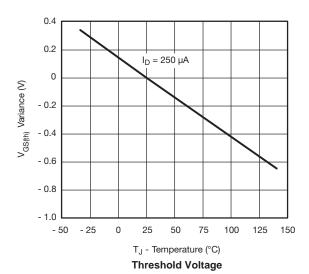
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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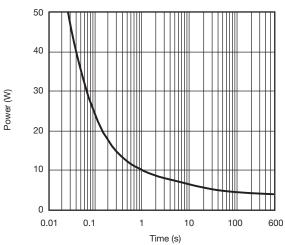




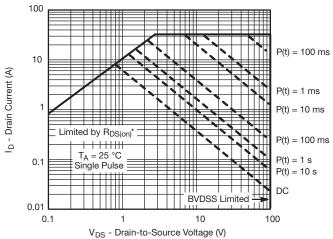


0.11 0.09 0.07 0.05 0.05 0.01 0 2 4 6 8 10

 $\label{eq:VGS} V_{GS} \text{ - Gate-to-Source Voltage (V)} \\$ On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

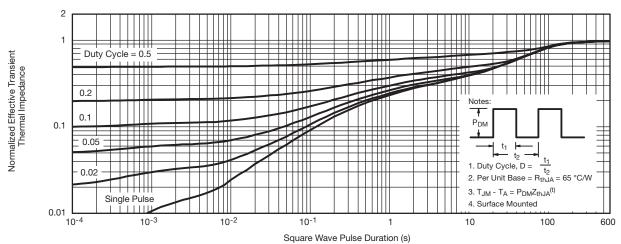


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

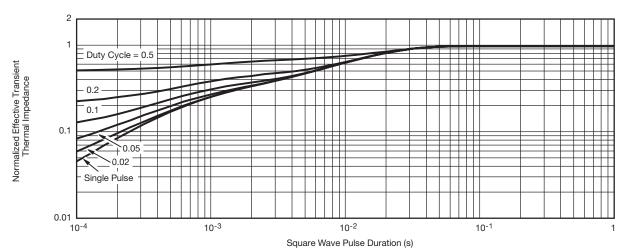
Safe Operating Area



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

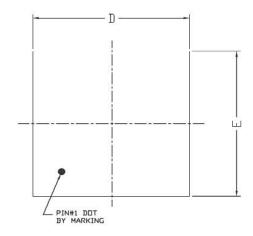


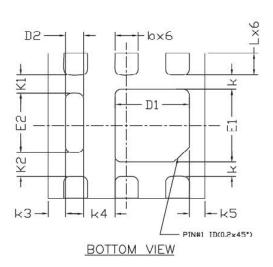
Normalized Thermal Transient Impedance, Junction-to-Ambient

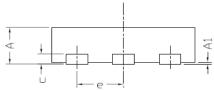


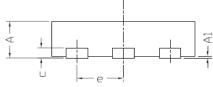
Normalized Thermal Transient Impedance, Junction-to-Case

DFN2x2 _6L_EP1_S PACKAGE OUTLINE









0.650	0.300 00
2.050	-0.820 -0.330 -1.725

RECOMMENDED LAND PATTERN

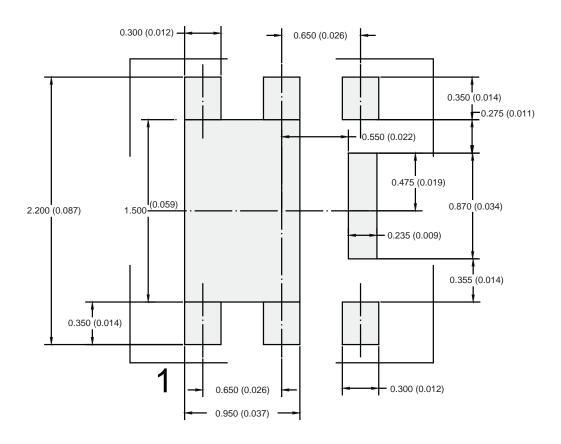
SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0. 55	0.60	0.020	0.022	0.024
A1	0.00		0.05	0.000		0.002
ь	0.25	0.30	0.35	0.010	0.012	0.014
С		0.152 REF			0.006 REF	
D	1.90	2.00	2.10	0.075	0.079	0.083
D1	0.85	0.95	1.05	0.033	0.037	0.041
D2	0.13	0.23	0.33	0.005	0.009	0.013
E	1.90	2.00	2.10	0.075	0.079	0.083
E1	0.90	1.00	1.10	0.035	0.039	0.043
E2	0.72	0.82	0.92	0.028	0.032	0.036
e	0.65 BSC			0. 026 BSC		
K	0. 20 BSC			0.008 BSC		
K1	0. 25 BSC			0.010 BSC		
K2	0.33 BSC			0.013 BSC		
K3	0. 22 BSC			0.009 BSC		
K4	0.40 BSC			0. 016 BSC		
K5	0. 20 BSC			0.008 BSC		
L	0.25	0.30	0.35	0.010	0.012	0.014

NOTE

CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

UNIT: mm

RECOMMENDED PAD LAYOUT FOR DFN2X2



Dimensions in mm/(Inches)





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