P-Channel 30 V (D-S) MOSFET

.

FEATURES

Package

Devices

APPLICATIONS

DT-Trench Power MOSFET

- Small Footprint Area

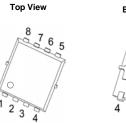
- Low On-Resistance

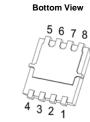
Thermally Enhanced PDFN3.3X3.3

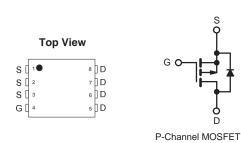
· Load Switch, PA Switch, and Battery Switch for Portable

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω) (Max.)	I _D (A)	Q _g (Typ.)		
- 30	0.021 at V _{GS} = - 4.5 V	- 35 ^a	33 nC		
- 30	0.030 at V _{GS} = - 2.5 V	- 25 ^a	00110		

PDFN 3.3x3.3







ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	- 30	V		
Gate-Source Voltage		V _{GS}	± 20	v		
	T _C = 25 °C		- 35 ^a			
Continuous Drain Current (T _J = 150 °C)	$T_{\rm C} = 70 ^{\circ}{\rm C}$	I _D	- 25 ^a	_		
	T _A = 25 °C T _A = 70 °C	-	- 16 ^{b, c} - 9 ^{b, c}	А		
Pulsed Drain Current (t = 300 µs)		I _{DM}	- 140			
Continuous Source-Drain Diode Current	$T_{\rm C} = 25 ^{\circ}{\rm C}$	I _S	- 35 ^a			
	$T_A = 25 \text{ °C}$	-	- 12 ^{b, c}			
	T _C = 25 °C		38			
Maximum Power Dissipation	T _C = 70 °C	Pn –	20	w		
	T _A = 25 °C	· D	3.5 ^{b, c}	vv		
	T _A = 70 °C		2.1 ^{b, c}	1		
Operating Junction and Storage Temperature Ra	T _J , T _{stg}	- 55 to 150	°C			
Soldering Recommendations (Peak Temperature		260				

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, f}	t ≤ 5 s	R _{thJA}	27	35	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	3.3	4.5	0/10	

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 2 s.

d. See solder profile The PDFN3.3X3.3 is a leadless package. The end of the lead terminal is exposed copper

(not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

f. Maximum under steady state conditions is 80 °C/W.



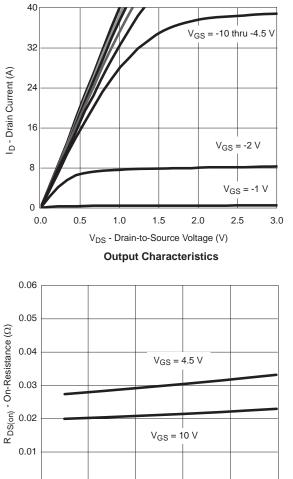
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•	•	•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	l _D = - 250 μA		- 11		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_{\rm D} = -230 \mu{\rm A}$		2.7			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \ \mu A$	- 1		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zana Cata Maltana Duain Coursent		$V_{DS} = -24 V, V_{GS} = 0 V$			- 1	μA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 4.5 V	- 140			Α	
	Read	V _{GS} = - 10 V, I _D = - 10 A		0.021	0.025	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 8 A		0.030	0.035		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 10 A		40		S	
Dynamic ^b		50 5		1	1	<u> </u>	
Input Capacitance	C _{iss}			2100	1	pF	
Output Capacitance	C _{oss}	V _{DS} = - 24 V, V _{GS} = 0 V, f = 1 MHz		980			
Reverse Transfer Capacitance	C _{rss}			490			
Total Gate Charge	Qg	V _{DS} = - 24 V, V _{GS} = - 8 V, I _D = - 10 A		33	52		
				20	31	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -24 V$, $V_{GS} = -4.5 V$, $I_{D} = -10 A$		4			
Gate-Drain Charge	Q _{gd}			6			
Gate Resistance	Rg	f = 1 MHz		6		Ω	
Turn-On Delay Time	t _{d(on)}			16	27	-	
Rise Time	t _r	V_{DD} = - 24 V, R_L = 0.75 Ω		17	24		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 8 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		21	30		
Fall Time	t _f			44	65		
Turn-On Delay Time	t _{d(on)}			11	17	- ns -	
Rise Time	t _r	V_{DD} = - 24 V, R_L = 0.75 Ω		15	20		
Turn-Off Delay Time	t _{d(off)}	${\rm I}_{\rm D}\cong$ - 8 A, ${\rm V}_{\rm GEN}$ = - 8 V, ${\rm R}_{\rm g}$ = 1 Ω		30	50		
Fall Time	t _f	-		43	66	1	
Drain-Source Body Diode Characterist	ics					1	
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 35	Δ	
Pulse Diode Forward Current	I _{SM}				140	A	
Body Diode Voltage	V _{SD}	I _S = - 8 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			40	65	ns	
Body Diode Reverse Recovery Charge	harge Q_{rr} I _F = - 8 A, di/dt = 100 A/µs, T _J = 25 °C			20	35	nC	
Reverse Recovery Fall Time	t _a	$r_{\rm F} = -6$ A, $u/u_{\rm I} = 100$ A/µs, $r_{\rm J} = 25$ °C		15			
Reverse Recovery Rise Time	t _b			26		ns	

Notes:

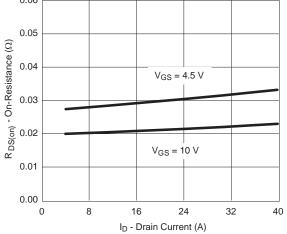
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

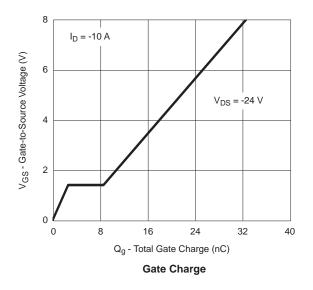
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

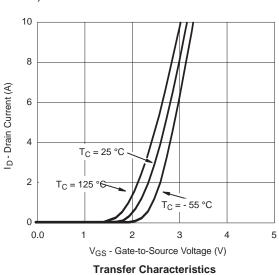


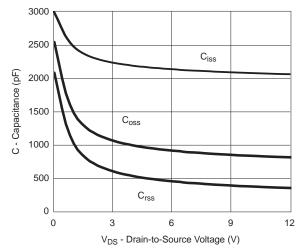
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



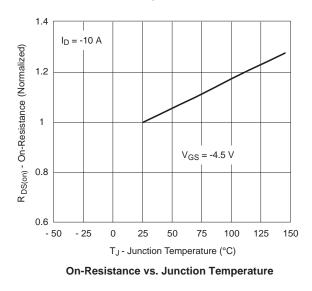
On-Resistance vs. Drain Current and Gate Voltage



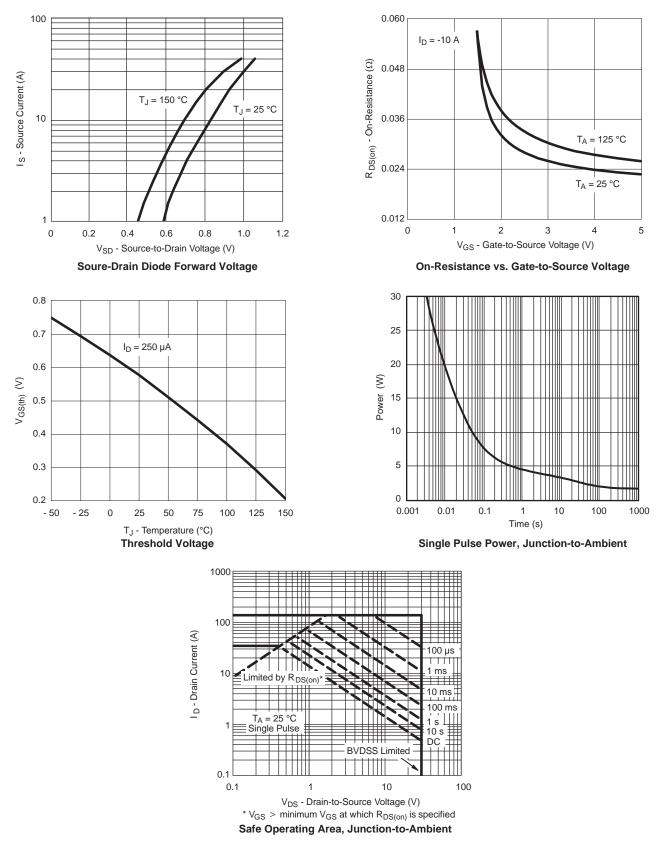




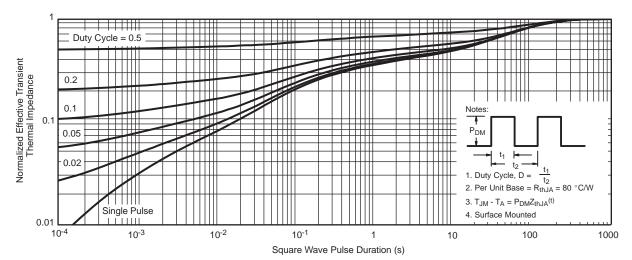
Capacitance



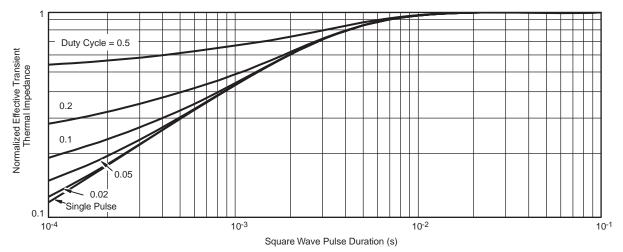
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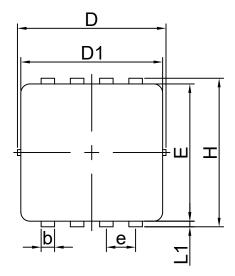


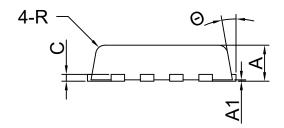
Normalized Thermal Transient Impedance, Junction-to-Ambient

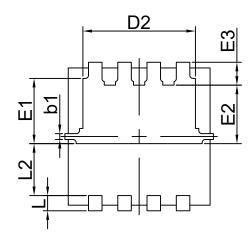


Normalized Thermal Transient Impedance, Junction-to-Case

PDFN3.3*3.3-8L Case Outline







MIN	NOM	MAX	
0.70	0.80	0.90	
0.00	0.03	0.05	
0.24	0.30	0.35	
0.08	0.13	0.18	
0.152REF			
3.25	3.32	3.40	
3.05	3.15	3.25	
2.40	2.50	2.60	
3.00	3.10	3.20	
1.35	1.45	1.55	
1.20	1.30	1.40	
0.40	0.50	0.60	
0.65 BSC			
3.20	3.30	3.40	
0.30	0.40	0.50	
0.10	0.15	0.20	
1.13 REF			
0.20 REF			
6°	10°	14°	
	0.70 0.00 0.24 0.08 0 3.25 3.05 2.40 3.00 1.35 1.20 0.40 0.30 0.30 0.10 1.10	0.70 0.80 0.00 0.03 0.24 0.30 0.08 0.13 0.152REI 3.32 3.05 3.15 2.40 2.50 3.00 3.10 1.35 1.45 1.20 1.30 0.40 0.50 3.20 3.30 0.40 0.50 0.65 BSC 3.20 3.20 3.30 0.30 0.40 0.10 0.15 1.13 REF 0.20 REF	



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