

**DTS1005** www.din-tek.jp

# N-Channel 100 V (D-S) MOSFET

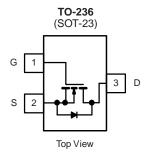
PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	
100	0.275 at V <sub>GS</sub> = 10 V	0.9	

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- 100 % R<sub>g</sub> and UIS Tested
  TrenchFET<sup>®</sup> Power MOSFET
- Compliant to RoHS Directive 2002/95/EC



HALOGEN FREE Available



ABSOLUTE MAXIMUM RATINGS (	T <sub>A</sub> = 25 °C, unle	ess otherwise	noted)			
Parameter		Symbol		Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current (T <sub>.1</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C			0.9		
Continuous Drain Current $(T_j = 150 \text{ C})$	T <sub>A</sub> = 70 °C	I <sub>D</sub>		0.8	А	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	6		A	
Avalanche Current <sup>b</sup>	L = 0.1 mH	I <sub>AS</sub>	6			
Single Avalanche Energy		E <sub>AS</sub>	1	.8	mJ	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	0.6		А	
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	Р	1.25	0.73	W	
Power Dissipation	T <sub>A</sub> = 70 °C	P <sub>D</sub>	0.80	0 0.47	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	R <sub>thJA</sub>	80	100	
Maximum Junction-to-Ambient	Steady State		130	170	°C/W
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	45	55	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

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			Limits			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_{D} = 1 mA$	100			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$ 2 4		4	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
Zara Cata Valtaga Drain Current		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	μA
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 \text{ °C}$			75	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge$ 15 V, $V_{GS}$ = 10 V	6			А
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.5 \text{ A}$		0.275		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 0.5 \text{ A}$		4		S
Diode Forward Voltage	V <sub>SD</sub>	$I_{S} = 0.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V
Dynamic <sup>b</sup>						
Total Gate Charge	Qg			3.3		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 50 V, $V_{GS}$ = 10 V, $I_D$ = 0.5 A		0.47		nC
Gate-Drain Charge	Q <sub>gd</sub>			1.45		
Gate Resistance	Rg			1.3		Ω
Switching			•			
Turn-On Delay Time	t <sub>d(on)</sub>			7		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 50 V, $R_{L}$ = 33 $\Omega$		11		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 0.2$ Å, $V_{GEN} = 10$ V, $R_g = 6 \Omega$		9		ns
Fall Time	t <sub>f</sub>			10		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, dl/dt = 100 A/μs	1	50		

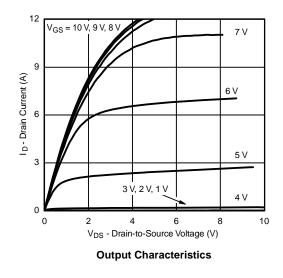
Notes:

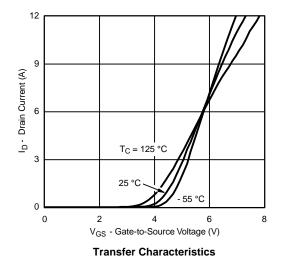
a. Pulse test: PW  $\leq 300~\mu s,$  duty cycle  $\leq 2$  %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

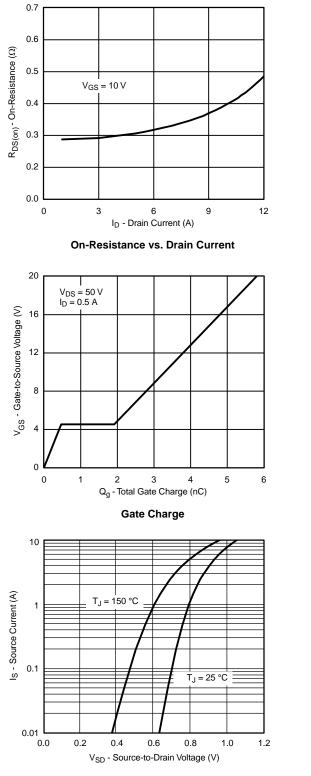
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



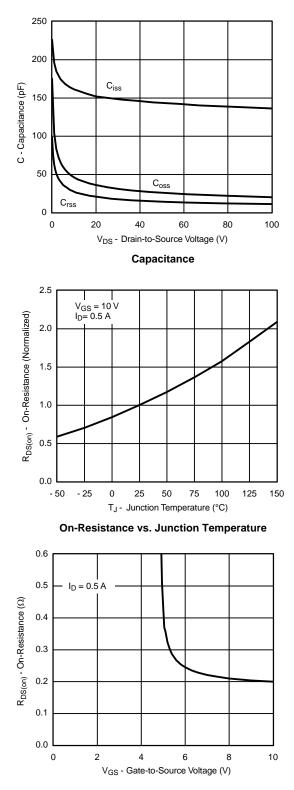




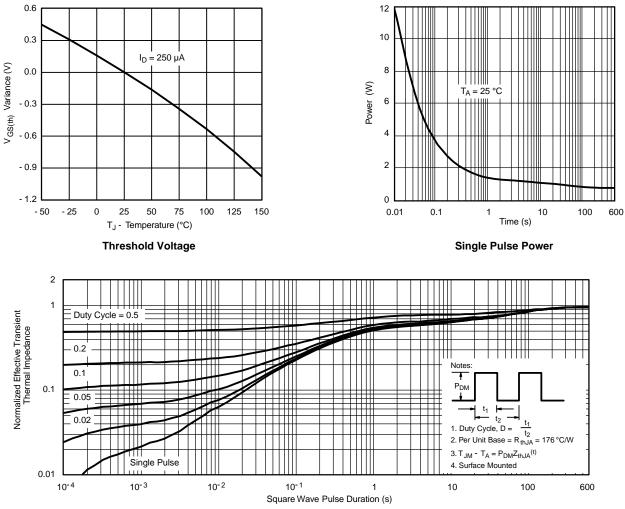
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

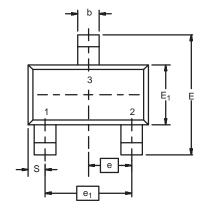


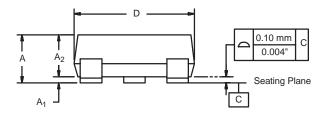
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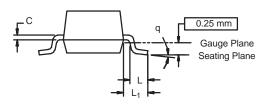
Normalized Thermal Transient Impedance, Junction-to-Ambient



### SOT-23 (TO-236): 3-LEAD



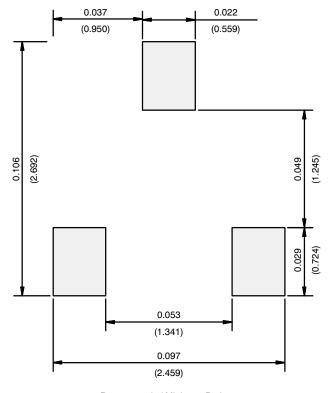




Dim	MILLIM	ETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025	Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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