

# N-Channel 20-V (D-S) MOSFET

PRODUC	CT SUMMARY		
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A) <sup>a, g</sup>	Q <sub>g</sub> (Typ.)
20	0.0095 at V <sub>GS</sub> = 10 V	12	7.3 nC
20	0.0105 at V <sub>GS</sub> = 4.5 V	10	7.5110

### **FEATURES**

- DT-Trench Power MOSFET
- 100 % R<sub>q</sub> Tested
- 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

#### **APPLICATIONS**

- DC/DC Conversion
- POL

(SOT-23)	D <b>9</b>
G 1 3 D	G S S
Top View	N-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20	V	
Gate-Source Voltage		V <sub>GS</sub>	± 12		
-	T <sub>C</sub> = 25 °C		12 <sup>a, g</sup>		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	10 <sup>g</sup>		
Continuous Diam Current (1) = 150 °C)	T <sub>A</sub> = 25 °C	'D	11.3 <sup>b, c</sup>	Α.	
	T <sub>A</sub> = 70 °C		10.3 <sup>b, c</sup>		
Pulsed Drain Current		I <sub>DM</sub>	36 <sup>g</sup>		
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	15		
Avalanche Energy	L = 0.111111	E <sub>AS</sub>	11.25	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1-	12 <sup>a, g</sup>	A	
Continuous Source-Diam Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	2.9 <sup>b, c</sup>	A	
	T <sub>C</sub> = 25 °C		27.7		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	ь —	17.7	w	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.5 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		2.2 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature) <sup>d, e</sup>			260		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 10 s	$R_{thJA}$	29	36	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	$R_{thJC}$	3.6	4.5	0/11		

- a. Based on  $T_C = 25$  °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. The end of the lead terminal is exposed copper

(not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

- e. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under Steady State conditions is 81 °C/W.
- g. Package limited.





Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		22		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 5.0		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	0.5		1.0	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	12			Α
Danis Courses On Otata Banistana a		$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		0.0095	0.0105	Ω
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_{D} = 7 \text{ A}$		0.0105	0.0120	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A		26		S
Dynamic <sup>b</sup>			I.	I		
Input Capacitance	C <sub>iss</sub>			880		
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		310		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			125		
Total Cata Charma	Qg	$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		15	23	nC
Total Gate Charge				7.3	11	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		2.2		
Gate-Drain Charge	$Q_{gd}$			2.1		
Gate Resistance	$R_{g}$	f = 1 MHz	0.2	0.9	1.8	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			15	30	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 2 $\Omega$		11	22	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 5$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		16	30	
Fall Time	t <sub>f</sub>			8	16	
Turn-On Delay Time	t <sub>d(on)</sub>			10	20	ns
Rise Time	t <sub>r</sub>	$V_{DD}$ = 10 V, $R_L$ = 2 $\Omega$		8	16	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 5$ A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		16	30	
Fall Time	t <sub>f</sub>			7	14	
<b>Drain-Source Body Diode Characteristi</b>	cs		l	l .		
Continuous Source-Drain Diode Current	I <sub>S</sub>	$T_C = 25  ^{\circ}C$			12	٨
Pulse Diode Forward Current	I <sub>SM</sub>				36	A
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 3 A, V <sub>GS</sub> = 0 V		0.77	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			14	28	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 10 A, dI/dt = 100 A/μs, T <sub>.1</sub> = 25 °C		4.5	9	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 10 \text{ A}$ , $I_J = 25 \text{ C}$		5.5		
Reverse Recovery Rise Time				8.5		ns

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

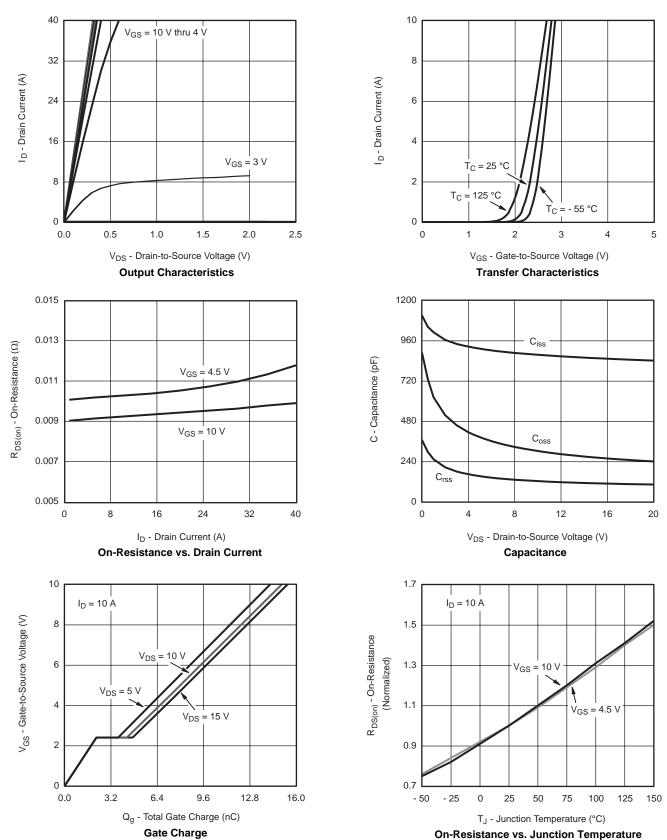
a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$ 

b. Guaranteed by design, not subject to production testing.



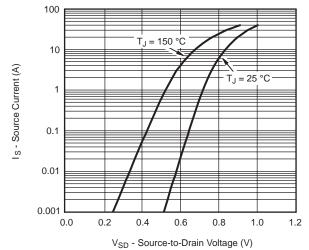


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

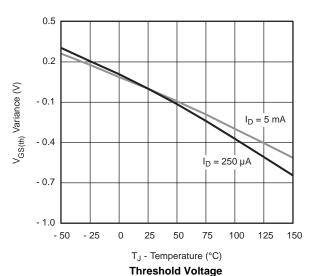




#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



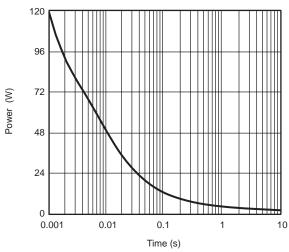
Source-Drain Diode Forward Voltage



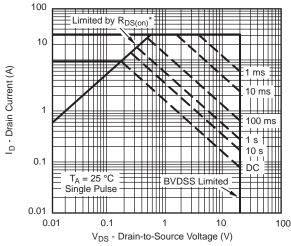
0.05 0.04 0.02 0.02 0.00 0.01 0.00 0 1 2 3 4 5 6 7 8 9 10

V<sub>GS</sub> - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power (Junction-to-Ambient)

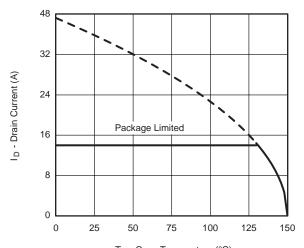


\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Ambient

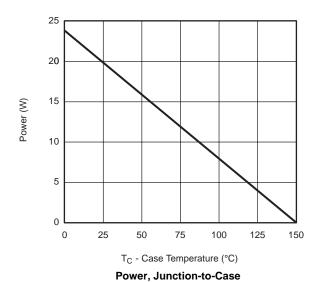
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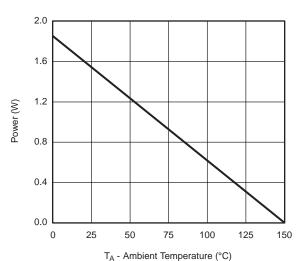
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



T<sub>C</sub> - Case Temperature (°C)

#### **Current Derating\***



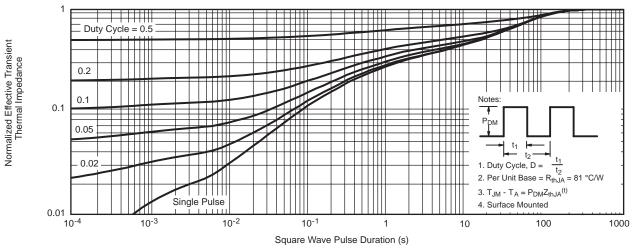


Power, Junction-to-Ambient

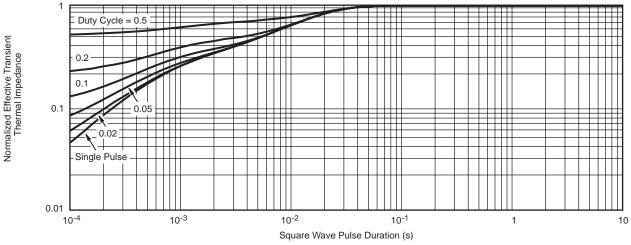
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



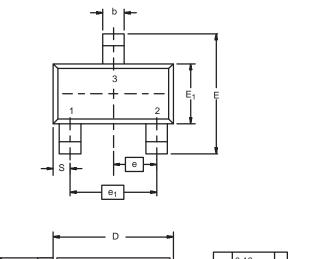
Normalized Thermal Transient Impedance, Junction-to-Ambient

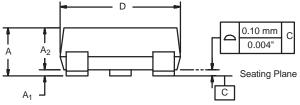


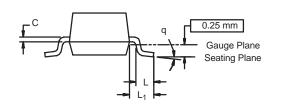
Normalized Thermal Transient Impedance, Junction-to-Case



### **SOT-23: 3-LEAD**



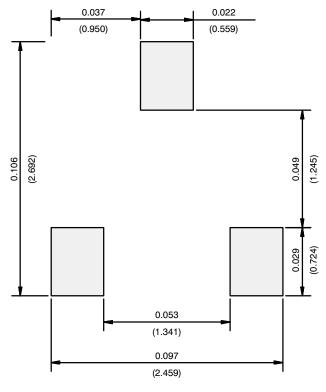




Dim	MILLIM	ETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	

DWG: 5479

#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)





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