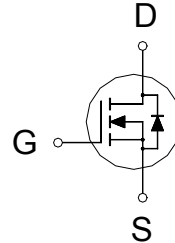




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	6.2mΩ	63A

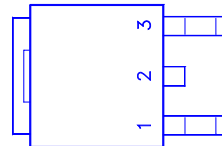


Features

- Pb-Free, Halogen Free and RoHS compliant.
- Low $R_{DS(on)}$ to Minimize Conduction Losses.
- Ohmic Region Good $R_{DS(on)}$ Ratio.
- Optimized Gate Charge to Minimize Switching Losses.

Applications

- Protection Circuits Applications.
- Logic/Load Switch Circuits Applications.



1. GATE
2. DRAIN
3. SOURCE

100% UIS Tested
100% Rg Tested

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current ²	$T_C = 25\text{ °C}$	I_D	63	A
	$T_C = 100\text{ °C}$		40	
Pulsed Drain Current ¹		I_{DM}	120	
Avalanche Current		I_{AS}	25	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	31	mJ
Power Dissipation	$T_C = 25\text{ °C}$	P_D	48	W
	$T_C = 100\text{ °C}$		19	
Junction & Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.6	°C / W
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Package limitation current is 39A

ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

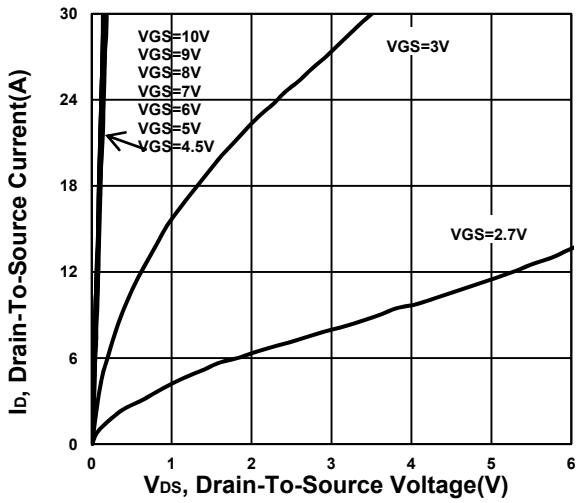
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.3	1.6	2.3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	μA
		V _{DS} = 20V, V _{GS} = 0V, T _J = 125 °C			10	
Drain-Source On-State Resistance ¹	R _{DSON}	V _{GS} = 4.5V, I _D = 15A		5.7	9	mΩ
		V _{GS} = 10V, I _D = 20A		4.7	6.2	
Forward Transconductance ¹	g _{fs}	V _{DS} = 5V, I _D = 20A		75		S
DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1MHz		992		pF
Output Capacitance	C _{oss}			189		
Reverse Transfer Capacitance	C _{rss}			122		
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz		2		Ω
Total Gate Charge ²	Q _{g(VGS=10V)}	V _{DS} = 15V, I _D = 20A		22		nC
	Q _{g(VGS=4.5V)}			12		
Gate-Source Charge ²	Q _{gs}			2.3		
Gate-Drain Charge ²	Q _{gd}			7.2		
Turn-On Delay Time ²	t _{d(on)}		V _{DS} = 15V I _D ≅ 20A, V _{GS} = 10V, R _{GEN} = 6Ω		13	
Rise Time ²	t _r			46		
Turn-Off Delay Time ²	t _{d(off)}			37		
Fall Time ²	t _f			44		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current ³	I _S				40	A
Forward Voltage ¹	V _{SD}	I _F = 20A, V _{GS} = 0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F = 20A, dI _F /dt = 100A / μS		11.5		nS
Reverse Recovery Charge	Q _{rr}				2	

¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

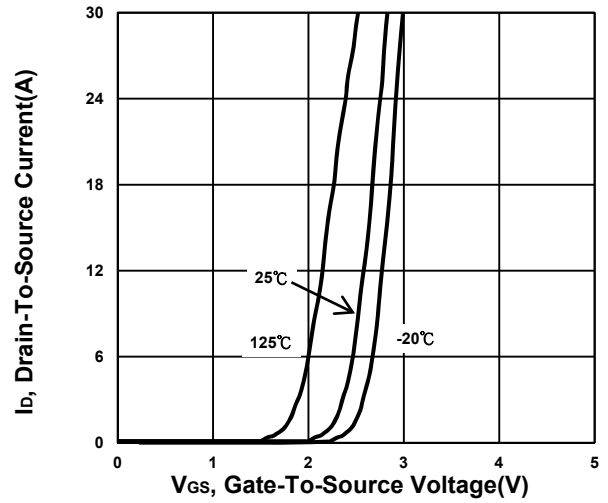
²Independent of operating temperature.

³Package limitation current is 39A

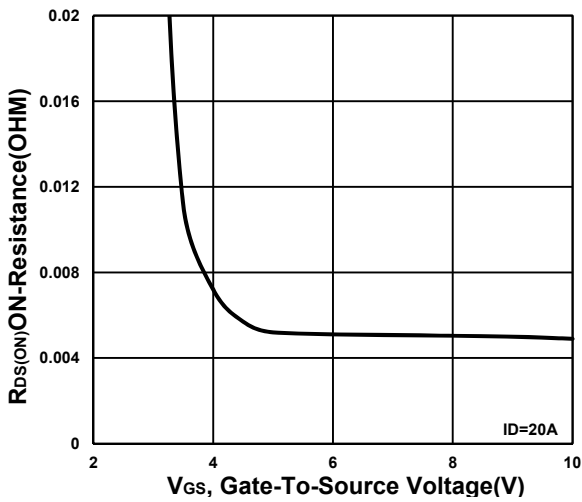
Output Characteristics



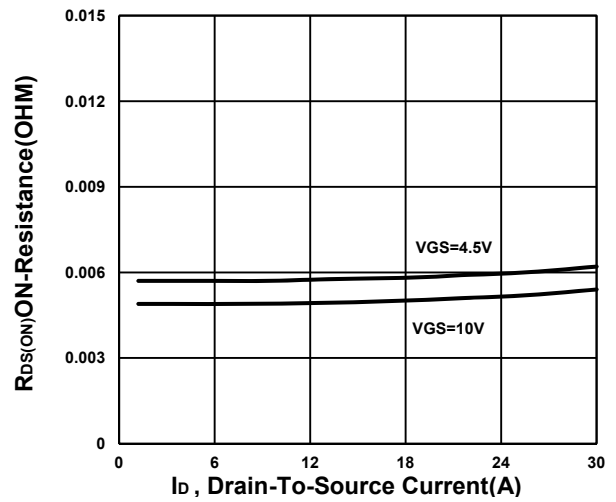
Transfer Characteristics



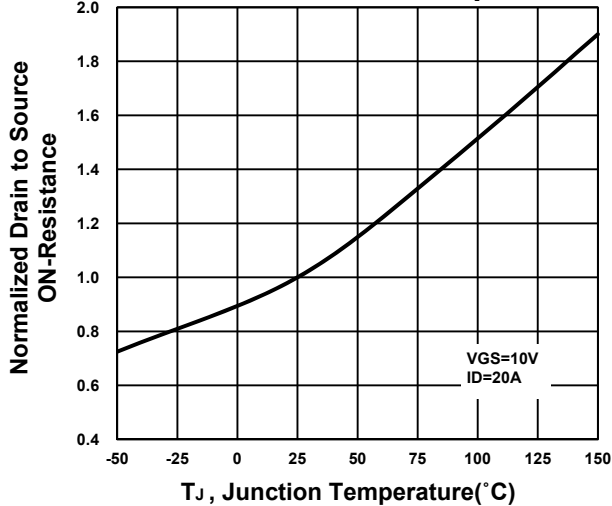
On-Resistance VS Gate-To-Source



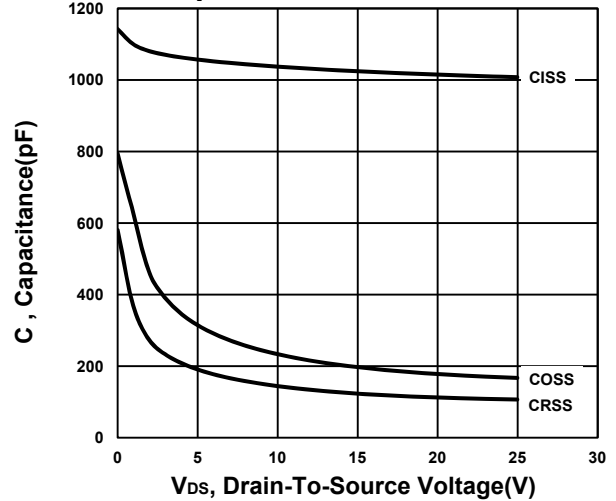
On-Resistance VS Drain Current



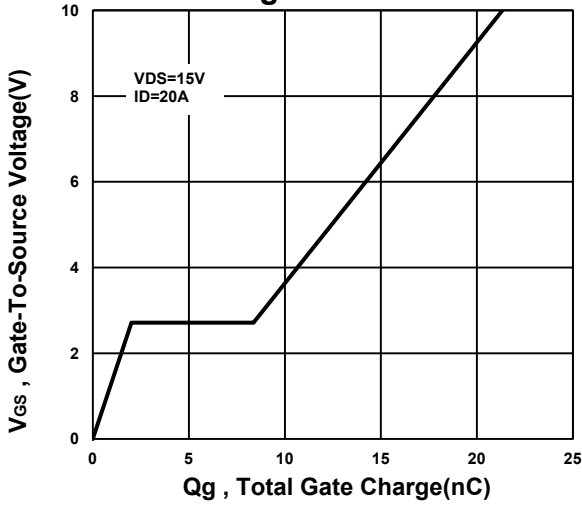
On-Resistance VS Temperature



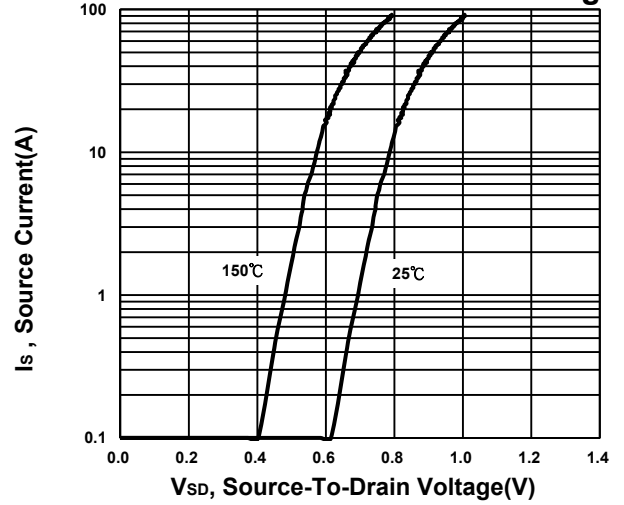
Capacitance Characteristic



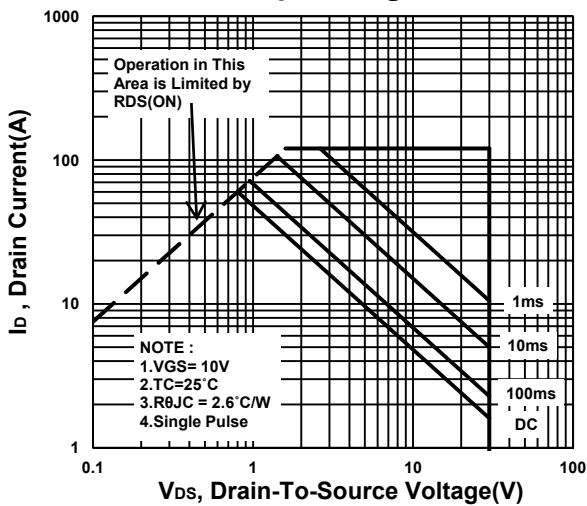
Gate charge Characteristics



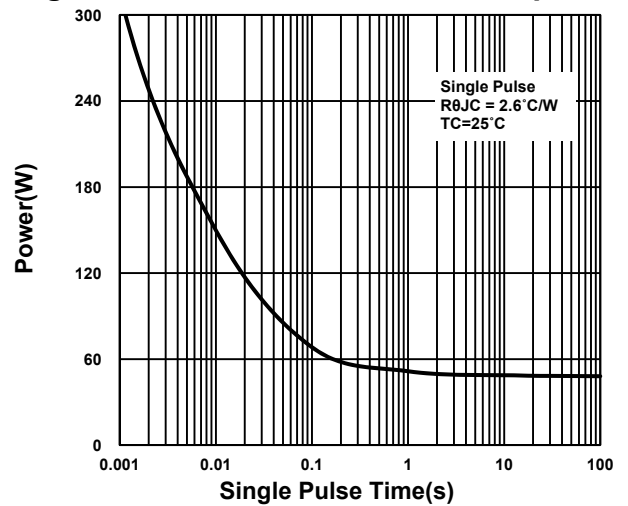
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

