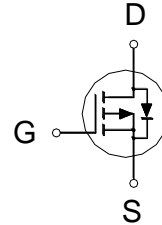




PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-40V	70mΩ	-2.5A

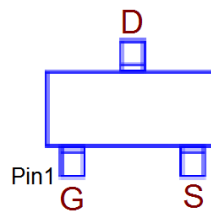


Features

- Pb-Free, Halogen Free and RoHS compliant.
- Low $R_{DS(on)}$ to Minimize Conduction Losses.
- Ohmic Region Good $R_{DS(on)}$ Ratio.
- Optimized Gate Charge to Minimize Switching Losses.

Applications

- Protection Circuits Applications.
- Logic/Load Switch Circuits Applications.



G: GATE
D: DRAIN
S: SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ °C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	$T_A = 25\text{ °C}$	I_D	-2.5	A
	$T_A = 70\text{ °C}$		-2	
Pulsed Drain Current ¹		I_{DM}	-8	
Power Dissipation ³	$T_A = 25\text{ °C}$	P_D	0.7	W
	$T_A = 70\text{ °C}$		0.4	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		173	°C/W
Junction-to-Case	$R_{\theta JC}$		65	

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper.

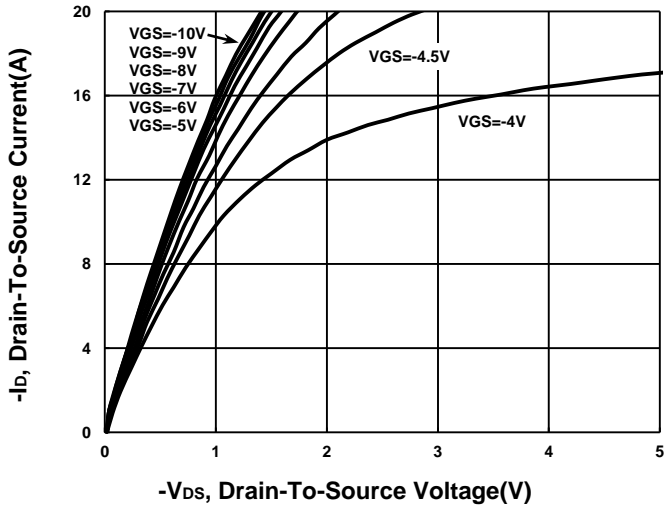
ELECTRICAL CHARACTERISTICS (T_J = 25 °C, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT		
			MIN	TYP	MAX			
STATIC								
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-40			V		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.3	-1.95	-2.3			
Gate-Body Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -32V, V _{GS} = 0V			-1	μA		
		V _{DS} = -30V, V _{GS} = 0V, T _J = 55 °C			-10			
Drain-Source On-State Resistance ¹	R _{DS(ON)}	V _{GS} = -4.5V, I _D = -2.5A		75	105	mΩ		
		V _{GS} = -10V, I _D = -2.5A		56	70			
Forward Transconductance ¹	g _{fs}	V _{DS} = -5V, I _D = -2.5A		10		S		
DYNAMIC								
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = -20V, f = 1MHz		613		pF		
Output Capacitance	C _{oss}			69				
Reverse Transfer Capacitance	C _{rss}			53				
Total Gate Charge ²	Q _{g(VGS=-10V)}	V _{DS} = -20V, V _{GS} = -10V, I _D = -2.5A		12.4		nC		
	Q _{g(VGS=-4.5V)}			6.7				
Gate-Source Charge ²	Q _{gs}			1.7				
Gate-Drain Charge ²	Q _{gd}			3.1				
Turn-On Delay Time ²	t _{d(on)}		V _{DD} = -20V, V _{GS} = -10V I _D ≅ -2.5A, R _G = 6Ω		13			nS
Rise Time ²	t _r				21			
Turn-Off Delay Time ²	t _{d(off)}			70				
Fall Time ²	t _f			40				
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)								
Continuous Current	I _S				-0.7	A		
Forward Voltage ¹	V _{SD}	I _F = -2.5A, V _{GS} = 0V			-1	V		
Reverse Recovery Time	t _{rr}	I _F = -2.5A, dI _F /dt = 100A / μS		13		nS		
Reverse Recovery Charge	Q _{rr}				7		nC	

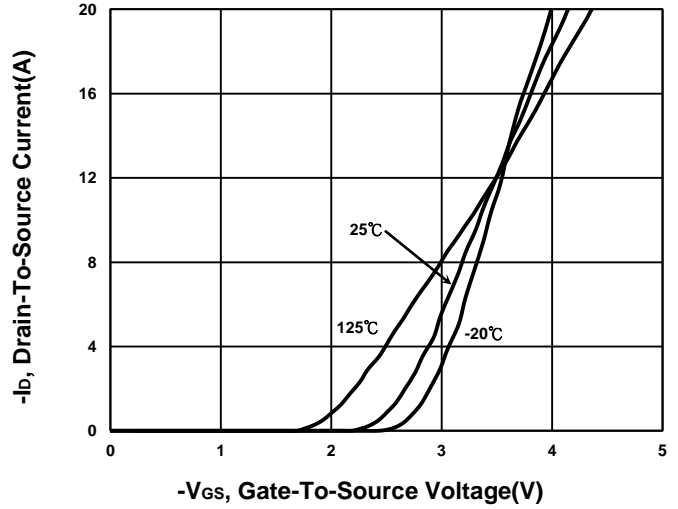
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

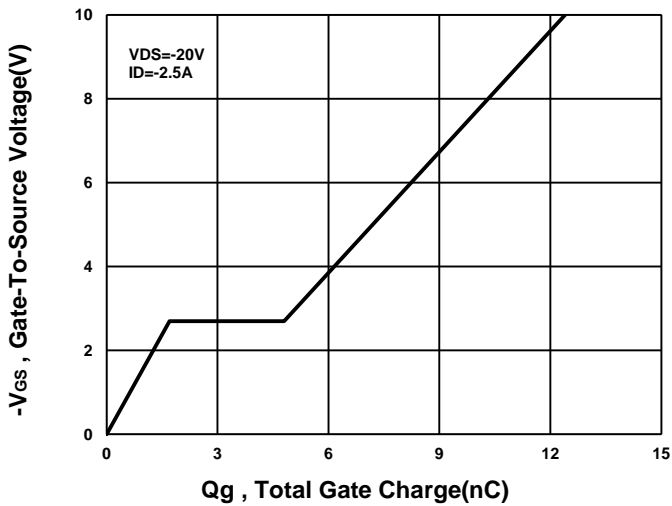
Output Characteristics



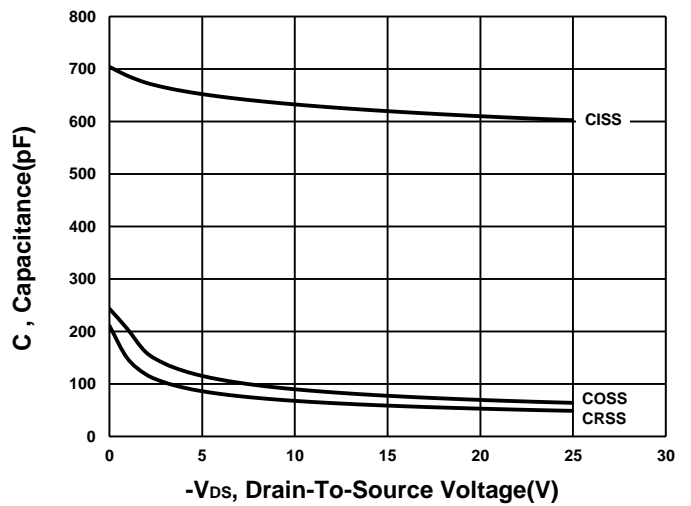
Transfer Characteristics



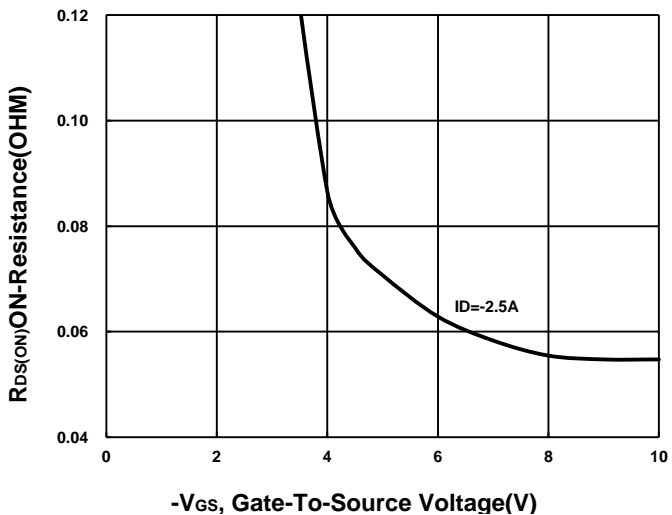
Gate charge Characteristics



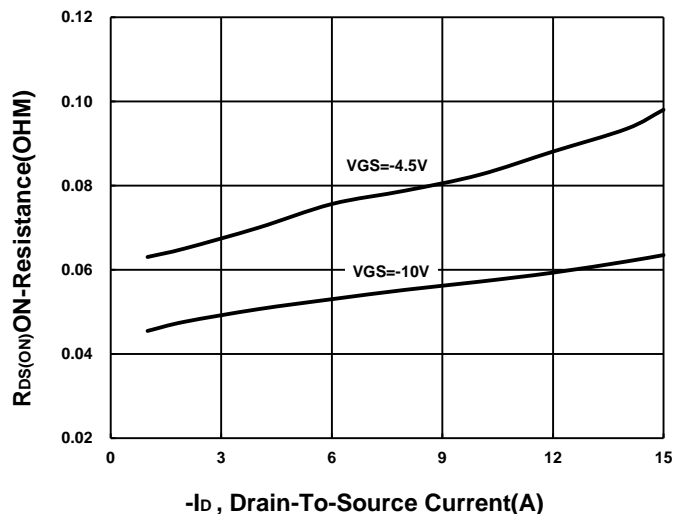
Capacitance Characteristic



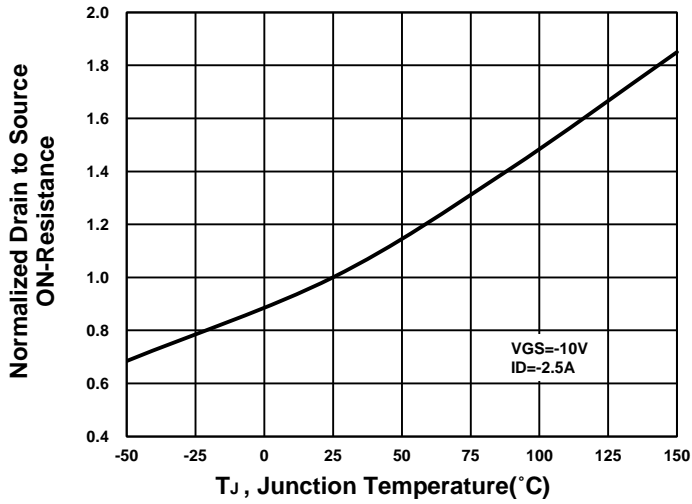
On-Resistance VS Gate-To-Source



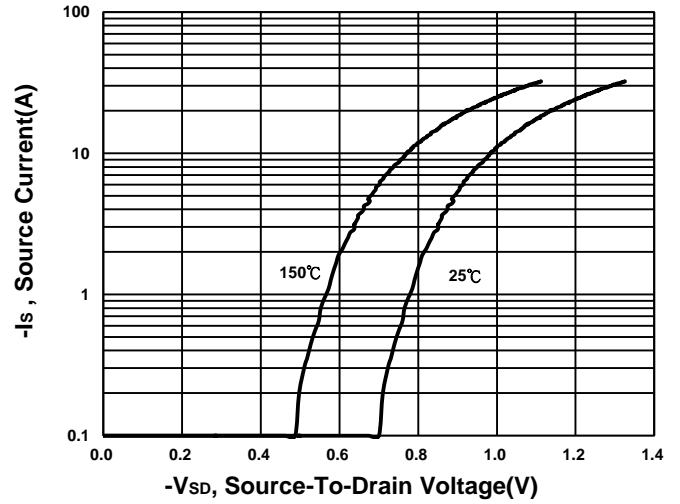
On-Resistance VS Drain Current



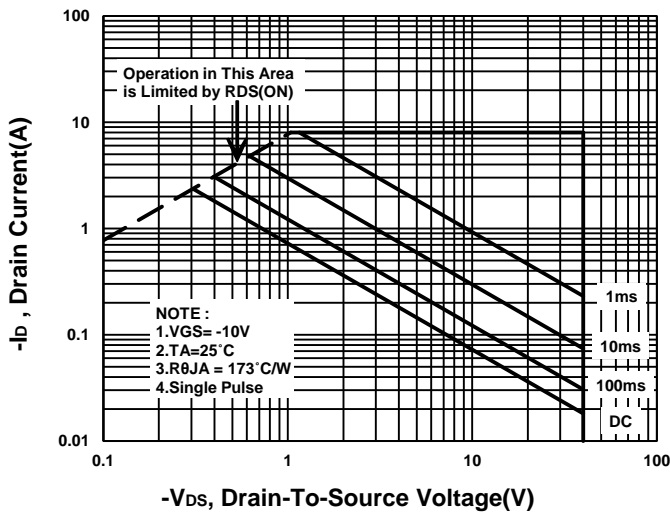
On-Resistance VS Temperature



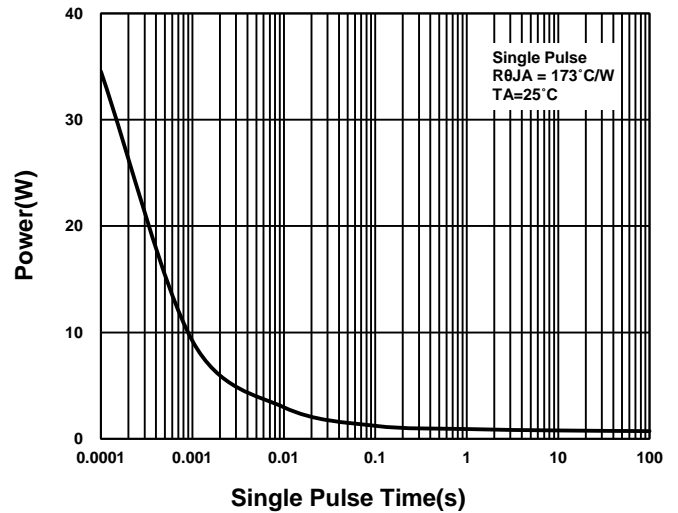
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

