Low-saturation, Low-voltage 1 channels Bi-directional Motor Driver (Micro-actuator Driver)

Description

FP5502 is a micro-actuator driver IC with miniature package (SOT-23-6). It is one channel low voltage bi-directional motor driver IC. The design is optimal for driving different type micro-actuator, such as voice coil motor, piezo-actuator, or other DC motor actuators. It is suitable for camera module application or other portable device.

Features

- Low Voltage Operation (VDD min=1.8V)
- Low Saturation Voltage(Upper Transistor + Low Transistor Residual Voltage; 0.46V typ. at 400mA
- Low Input Current
- Low Operating Current, Sleep Mode with Zero Current Drain
- Crossover-Current Protection
- High Output Sinking and Driving Capability
- Small, Thin, Highly Reliable Package (SOT-23-6)
- Thermal Shutdown Protection
- RoHS Compliant

Applications

- Voice Coil Motor
- Piezo-actuator
- Camera Module
- Other DC Motor Actuator

Pin Assignments

S6 Package (SOT-23-6)

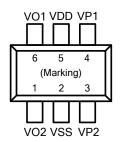
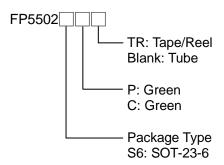


Figure 1. Pin Assignment of FP5502

Ordering Information



SOT-23-6 Marking

Part Number	Product Code		
FP5502S6C	C4G		

FP5502-1.7-FEB-2014

Block Diagram & Application Circuit

Shutter Application:

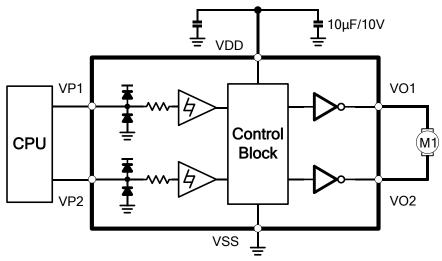


Figure 2. The Block Diagram and Application Circuit of the FP5502

Functional Pin Description

Pin Name	Pin Function				
VP2	Input pin which determines driving mode.				
VDD	Power supply pin.				
VO2	Output sinking / driving pin.				
VSS	Ground pin.				
VO1	Output sinking / driving pin.				
VP1	Input pin which determines driving mode.				

Block Diagram

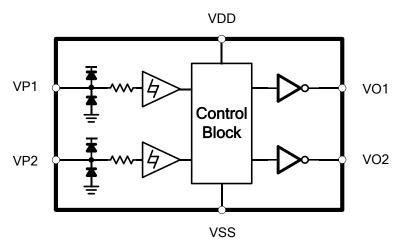


Figure 3. The Block Diagram of the FP5502



Absolute Maximum Ratings

• VDD to GND	0.3V to +5.5V
• VIN and VO to GND	
• Output Current (Note 1)	0mA to 400mA
• Power Dissipation @T _A =25°C, (P _D)	
SOT-23-6	400mW
 Package Thermal Resistance, (θ_{JA}): 	
SOT-23-6	+250°C/W
Junction Temperature	+150°C
Storage Temperature Range	
• Lead Temperature (Soldering, 10sec.)	260°C
Operating Temperature Range	40°C to +125°C
• ESD (Human Body Model) (Note 2)	4000V
Note 1: Output current rating may be limited by ambient temperature and heat cipking. Under any	set of conditions do not exceed the

Note 1 : Output current rating may be limited by ambient temperature and heat sinking. Under any set of conditions, do not exceed the specified.

Note 2 : ESD caution: ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000V readily accumulate on the human body and test equipment and can discharge without detection. Although this product features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

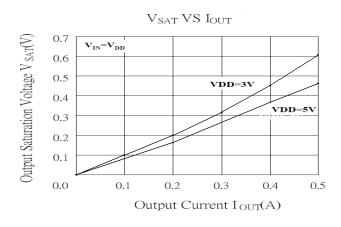
FP5502-1.7-FEB-2014

Electrical Characteristics

(Unless otherwise noted, TA=25°C & V_{DD}=3V)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit		
Supply Voltage	V_{DD}		1.8	3.0	5.5	V		
Supply Current	I _{DD0}	VVP1,VP2=0V	-	0.1	2	μA		
(IDD + IS)	I _{DD1}	VVP1,VP2≠0V	-	0.10	0.2	mA		
VP1 / VP2 Input Terminal (VP1 / VP2 Input Terminal (T _J = 25°C)							
Input Voltage "H"	VIH	-	0.5*V _{DD}	-	V _{DD} +0.3	V		
Input Voltage "L"	VIL	-	-0.3	-	0.2*V _{DD}	V		
Input Voltage Hysteresis	VHsy	-	-	0.42	-	V		
Input Current "H"	lн	VIN = VDD	-	-	3	μΑ		
Input Current "L"	Iμ	VIN = 0 V	-	-	3	μA		
VO1 / VO2 Output Terminal (T _J = 25°C)								
Output Voltage (upper + lower)	VOUT1	IOUT = 200mA	-	0.22	0.4	V		
	VOUT2	IOUT = 400mA	-	0.46	0.7	V		
	VOUT3	VDD=1.8V,I _{OUT} = 200mA	-	0.55	1.0	V		
Thermal Protection Circuit								
Protection Temperature	TTSD	VVP1,VP2=H	-	150	-	°C		
Temperature Hysteresis	THsy	VVP1,VP2=H	-	25	-	°C		

Typical Performance Curve



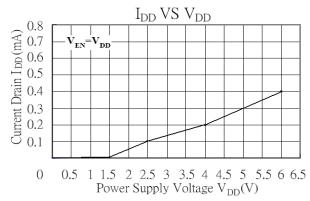


Figure 4a. V_{SAT} VS I_{OUT}

Figure 4b. I_{DD} VS V_{DD}

Truth Table

VP1	VP2	VO1	VO2	Mode
Low	Low	OFF	OFF	Standby
High	Low	High	Low	Forward
Low	High	Low	High	Reverse
High	High			Keep the previous mode (Forward/ Reverse)

Truth Table Test Wave

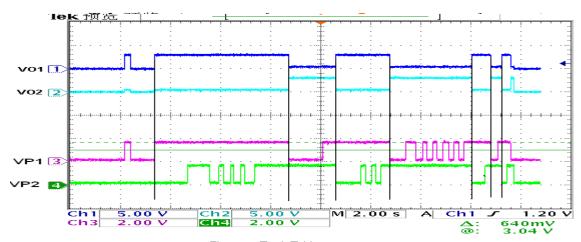


Figure 5. Truth Table test waves

Function Description

Device Operation

FP5502 is a micro-actuator driver IC with miniature package (SOT-23-6). It is one channel low voltage bi-directional motor driver IC. The design is optimal for driving different type micro-actuator, such as voice coil motor, piezo-actuator, or other DC motor actuators. It is suitable for camera module application or other portable device. MOSFET output stages substantially to reduce the voltage drop and the power dissipation of the outputs of the FP5502 compared to typical drivers with bipolar transistors.

Internal circuit protection includes thermal shutdown with hysteresis and crossover current protection. FP5502 is designed for portable applications with a power-off (sleep mode) current of 100nA typical, and an operating voltage of 1.8 to 5.5. FP5502 logic inputs are 3 to 5V logic compatible.

In conditions where the logic supply voltage drops below 1.8V, both the sink and the source voltage drop will increase beyond the specified values. In extreme cases, no power will be delivered to the motors. However, the device will not be damaged.

Sleep Mode

Pulling all inputs to 0.4V or less drives the FP5502 to sleep mode. And it will draw 100nA typical during sleep mode.

Thermal Shutdown

The FP5502 will disable the outputs if the junction temperature reaches 150°C. When it enters thermal shutdown, after the junction temperature drops 15°C, the outputs will be re-enabled

Application Note

The capacitor connected between the output nodes VO1/VO2 will reduce the noise generating by motor when the motor is switched to opposing direction.

The power is dissipated by the IC varies widely with supply voltage, output current and loading. It is important to ensure the application does not exceed the allowable power dissipation of the IC package.

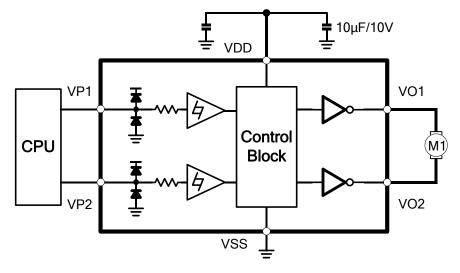


Figure 6. Typical DC Motor Application

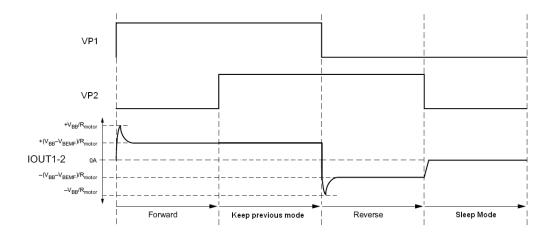
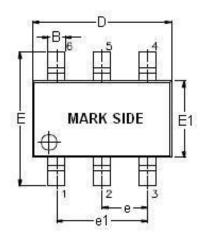


Figure 7. Typical DC motor control application (timing chart)

Outline Information

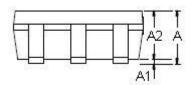
SOT-23-6 Package (Unit: mm)



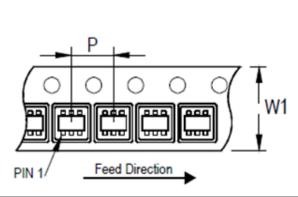


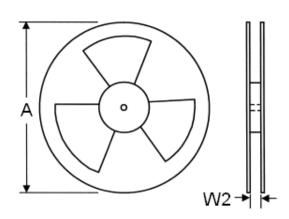
SYMBOLS	DIMENSION IN MILLIMETER			
UNIT	MIN	MAX		
Α	0.90	1.45		
A1	0.00	0.15		
A2	0.90	1.30		
В	0.30	0.50		
D	2.80	3.00		
E	2.60	3.00		
E1	1.50	1.70		
е	0.90	1.00		
e1	1.80	2.00		
Ĺ	0.30	0.60		

Note: Followed From JEDEC MO-178-C.



Carrier Dimensions





Tape Size	Pocket Pitch	Reel Size (A)		Reel Width	Empty Cavity	Units per Reel
(W1) mm	(P) mm	in	mm	(W2) mm	Length mm	
8	4	7	180	8.4	300~1000	3,000