







Typical Features

- ◆ Fixed input voltage, Isolated & unregulated output, Output power 2W
- ◆ High Efficiency up to 79%
- ◆ Small compact SIP packing
- ◆ No external component required
- ◆ Isolation Voltage 1500VDC
- ◆ Operating Temperature: -40°C~+85°C
- ◆ Plastic Case, meet UL94-V0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and $Ta=25^{\circ}$ C

Application Field

nical Product Lie

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product	List									
Model	Input Voltage Range (VDC)		Output Voltage/ Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitiv e Load	Ripple & Noise (Max.)	(%) load, nom	iency)full input ninal age
	Nominal	Range	Voltage (VDC)	Current(mA) MAX./Min.	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Тур.
FN2-05S3V3CN		4.5	3.3	600	533	50	100	150	73	75
FN2-05S05CN			5	400	519	50	100	150	75	77
FN2-05S09CN	_		9	220	513	50	47	150	76	78
FN2-05S12CN	5	5.5	12	167	513	50	47	150	76	78
FN2-05S15CN			15	133	506	50	47	150	77	79
FN2-05S24CN			24	83	506	50	22	150	77	79
FN2-09S3V3CN			3.3	600	296	25	100	150	73	75
FN2-09S05CN			5	400	289	25	100	150	75	77
FN2-09S09CN		8.1	9	220	289	25	47	150	75	77
FN2-09S12CN	9	9.9	12	167	285	25	47	150	76	78
FN2-09S15CN			15	133	285	25	47	150	76	78
FN2-09S24CN			24	83	285	25	22	150	76	78
FN2-12S05CN		10.8	5	400	214	18	100	150	76	78
FN2-12S09CN	12	-	9	220	211	18	47	150	77	79
FN2-12S12CN		13.2	12	167	211	18	47	150	77	79



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FN2-12S15CN			15	133	216	18	47	150	75	77
FN2-24S3V3CN		21.6	3.3	600	113	10	100	150	73	74
FN2-24S05CN			5	400	108	10	100	150	75	77
FN2-24S09CN	0.4		9	220	107	10	47	150	76	78
FN2-24S12CN	24	26.4	12	167	105	10	47	150	77	79
FN2-24S15CN			15	133	105	10	47	150	77	79
FN2-24S24CN			24	83	105	10	22	150	77	79

Note:

1. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.

Input Specifications									
Item	Working Conditions	Min.	Тур.	Max.	Unit				
	5Vdc Input	-0.7		9					
Input Overshoot Voltage	9Vdc Input	-0.7		15	\/D0				
(1 Second.max.)	12Vdc Input	-0.7		18	VDC				
	24Vdc Input	30							
Input Filter	Capacitor Filter								

Output Specifications						
Item	Working Conditions			Тур.	Max.	Unit
Output Power		0.2		2	W	
Output Voltage Accuracy	Nominal inp		±2	±5		
Load Regulation	10% ~ 100% nominal load	3.3Vdc output			20	
		Other output			15	%
Line Voltage Regulation	Input Voltage Change±1%	3.3Vdc output			±1.5	
		Other output			±1.2	
Ripple & Noise①	Nominal input,f band		100	150	mVp-p	
Temperature Drift Coefficient	100% F			±0.03	%/°C	
Output Short Circuit Protection	Continuous		short-ci	rcuit protection	n, self-recover	у

NOTE: 1 Ripple & Noise tested by twisted-pair method;

General Specifications		
Switching Frequency	Typical	100KHz (Typ.)
Operating Temperature	Refer to Temperature Derating Curve	-40℃ ~ +85℃

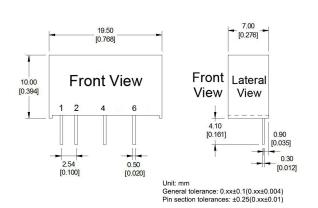




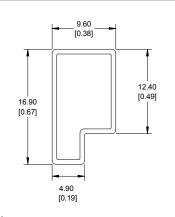


Storage Temperature		-55℃ ~ +125℃
Shell temperature rise during work	Within Temperature Derating Curve	25℃
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic(UL94-V0)
Product Weight		3g (Typ.)
Isolation Voltage	Test 1 minute, leakage current< 0.5mA	1500Vdc ≤ 0.5mA / 1min
Isolation Capacitor	Input/Output,100KHz/0.1V	20 pF (Typ.)
MTBF	MIL-HDBK-217F@25℃	35X10⁵Hrs

Packing Dimension



Print board vertical view
Grid: 2.54mm(0.1inch)



Note: Unit: mm[inch]

General tolerance: x.xx±0.5mm[x.xx±0.020 inch] x.x±0.2mm[x.x±0.008 inch]

Packing Code		LxWxH							
С	19.	19.50×7.00 × 10.00mm 0.768 × 0.276 × 0.394inch				ch			
Pin Function	Pin Function								
Pin Function	1	2	3	4	5	6			
Single (S)	+Vin	GND		-Vo		+Vo			

Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.





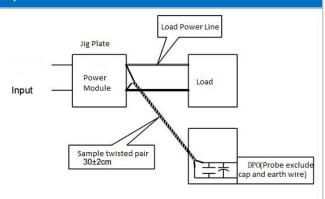


Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

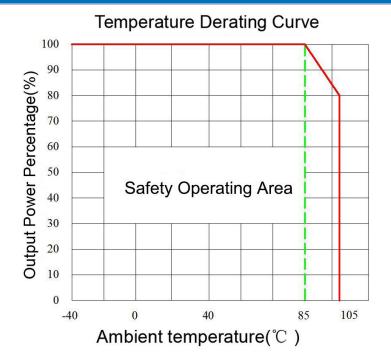
Test Method:

a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.



Temperature Curve



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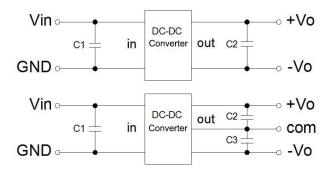


Design and Application Circuit Recommended

- 1. Output load requirements
- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
- b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

2. Recommended circuit

In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1. (But for the actual output power of application circuit is less than 0.5W, suggest not to connect external capacitor)



Vin (Vdc)	C1 (µF)	Vout (Vdc)	C2 (µF)	Vout (Vdc)	C2,C: (µF)
3.3/5	4.7	3.3/5	10	±3.3/±5	4.7
12	2.2	9	4.7	±9	2.2

15

24

2.2

0.47

±12

±15

±24

0.47

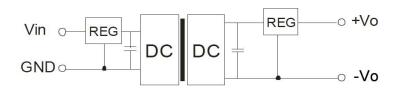
0.22

Output regulated voltage and over voltage protection circuit

The simplest device to protect output regulated voltage, over voltage and over current is to cascade a linear regulator with overheat protection at input or output terminal, and connect a capacitor filter net(see below picture), filter capacitive value recommended see table 1, Linear regulator is chosen according to the actual voltage, current needed in working, or choose our NW series products.

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Single Output



Note:

- 1. This product cannot be used in parallel, and do not support hot-plugging;
- 2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual:
- 3. All index testing methods in this datasheet are based on our Company's corporate standards
- 4. The product specification may be changed at any time without prior notice.